

SRM INSTITUTE OF SCIENCE & TECHNOLOGY

Kattankulathur, Kancheepuram 603203, Tamil Nadu, India

23. B. Tech in Electronics and Communication Engineering

23. (a) Mission of the Department

Mission Stmt - 1	Build an educational process that is well suited to local needs as well as satisfies the international accreditation
	requirements.
Mission Stmt - 2	Attract the right people and retain them by building an environment that foster work freedom and empowerment.
Mission Stmt - 3	With the right talent pool, create knowledge and disseminate, get involved in collaborative research with reputed
MISSION Stint - 3	universities and produce competent graduates.

23. (b) Program Educational Objectives (PEO)

The Program Educational Objectives for the Electronics and Communication Engineering program describe accomplishments that graduates are expected to attain within five years after graduation. Graduates within 5 years of graduation will / should demonstrate:

	and socially acceptable.
PEO – 2	Broad knowledge to establish themselves as creative practicing professionals, locally and globally, in fields such as design, research, testing and manufacturing of Electronics and Communication Systems.
PEO – 3	Communication skills (in both written and oral forms) and critical reasoning skills in bridging the divide between advanced technology and end users in the practice of Electronics and Communication Engineering.
PEO – 4	Sustained learning and adapting to a constantly changing field through graduate work, professional development, self-study and collaborative activities.
PEO – 5	Leadership and initiative to ethically advance professional and organizational goals, facilitate the achievements of others, and obtain substantive results.
PEO – 6	Ability to work productively as individuals and in groups (teamwork) of diverse cultural and multidisciplinary backgrounds.

23. (c) Mission of the Department to Program Educational Objectives (PEO) Mapping

	Mission Stmt 1	Mission Stmt 2	Mission Stmt. – 3
PEO - 1	L	Μ	Н
PEO - 2	Н	L	Н
PEO - 3	L	L	М
PEO - 4	М	L	М
PEO - 5	L	Н	Н
PEO - 6	Н	Н	Н

H – High Correlation, M – Medium Correlation, L – Low Correlation

23. (d) Mapping Program Educational Objectives (PEO) to Program Learning Outcomes (PLO)

						Pro	gram Lea	rning Out	tcomes (P	LO)					
		Graduate Attributes (GA)													cific SO)
250 (Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modern Tool Usage	: Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	Design, Prototype and Test Modern ECE Svstems	Project Management Techniques	Implement ECE Systems
PEO - 1	Н		Н			Н	М	Н			Н		Н		Н
PEO - 2		Н	М	Н	М								М		M
PEO - 3					L			М		Н				М	
PEO - 4												Н	L		
PEO - 5						L			М					М	
PEO - 6						М			Н					Н	L

H – High Correlation, M – Medium Correlation, L – Low Correlation

Program Specific Outcomes (PSO) Graduates of baccalaureate degree program in ECE must demonstrate knowledge and hands-on competence in the ability to:

PSO – 1	Design, prototype and test modern electronics and telecommunication engineering systems as per the specifications for the professional achievement in an industry and organization
PSO – 2	Apply project management techniques to electrical/ electronic/ telecommunications systems
PSO – 3	Analyze and research appropriate technologies for implementation of the electronics and telecommunication engineering systems

23. (e) Program Structure (B.Tech in Electronics and Communication Engineering)

	4 11 12 0.0 1.10 1					Г
	1. Humanities & Social Sciences					
_	including Management Courses (H)				-	ŀ
Course	Course	Ηοι	irs/ V	Veek		l
Code	Title	L	Т	Ρ	С	
18LEH101J	English	2	0	2	3	ŀ
18LEH102J	Chinese					
18LEH103J	French					
18LEH104J	German	2	0	2	3	
18LEH105J	Japanese					
18LEH106J	Korean					
18PDH101L	General Aptitude	0	0	2	1	ļ
18PDH102T	Management Principles for Engineers	2	0	0	2	ļ
18PDH103J	Social Engineering	1	0	2	2	ļ
18PDH201L	Employability Skills & Practices	0	0	2	1	ļ
	Total Learning Credits				12	l

	2. Basic Science Courses (B)							
	2. Dasic Science Courses (D)							
Course	Course	Hours/ Week						
Code	Title	L	Т	Ρ	С			
18PYB101J	Physics: Electromagnetic Theory, Quantum Mechanics, Waves and Optics	3	1	2	5			
18CYB101J	Chemistry	3	1	2	5			
18MAB101T	Calculus and Linear Algebra	3	1	0	4			
18MAB102T	Advanced Calculus and Complex Analysis	3	1	0	4			
18MAB201T	Transforms and Boundary Value Problems	3	1	0	4			
18MAB203T	Probability and Stochastic Process	3	1	0	4			
18MAB302T	Discrete Mathematics for Engineers	3	1	0	4			
18BTB101T	Biology	2	0	0	2			
	Total Learning Credits				32			

	3. Engineering Science Courses (S)						4. Professional Core Courses (C)				
Course	Course		lours Neek	-		Course	Course	H			
Code	Title	L	Т	Ρ	С	Code	Title	L	Т	Ρ	С
18MES101L	Engineering Graphics and Design	1	0	4	3	18ECC102J	Electronic Devices	3	0	2	4
18EES101J	Basic Electrical and Electronics Engineering	3	1	2	5	18ECC103J	Digital Electronic Principles	3	0	2	4
18MES103L	Civil and Mechanical Engineering Workshop	1	0	4	З	18ECC104T	Signals and Systems	3	1	0	4
18CSS101J	Programming for Problem Solving	3	0	4	5	18ECC105T	Electromagnetics and Transmission Lines	3	0	0	3
18ECS201T	Control Systems	3	0	0	3	18ECC201J	Analog Electronic Circuits	3	0	2	4
	Total Learning Credits	edits 19		18ECC202J	Linear Integrated Circuits	3	0	2	4		
	· · · · · · · · · · · · · · · · · · ·					18ECC203J	Microprocessor, Microcontroller and Interfacing Techniques	3	0	2	4
						18ECC204J	Digital Signal Processing	3	0	2	4
						18ECC205J	Analog and Digital Communication	3	0	2	4
						18ECC206J	VLSI Design	3	0	2	4
						18ECC301T	Wireless Communications	3	1	0	4
						18ECC302J	Microwave & Optical Communications	3	0	2	4
							Computer Communication Networks	3	0	2	4
						18ECC350T	Comprehension	0	1	0	1
							Total Learning Credits				52

	5. Professional Elective Courses (E)							6. Open Elective Courses (O)							
Course	Course	Hou	irs/ V	/eek			Course	Course	Ho	urs/ \	Veek				
Code	Title	L	Т	Ρ	С		Code	Title	L	Т	Ρ	С			
	Professional Elective – 1	3	0	0	3			Open Elective – 1	3	0	0	3			
	Professional Elective – 2	3	0	0	3			Open Elective – 2	3	0	0	3			
	Professional Elective – 3	3	0	0	3			Open Elective – 3	3	0	0	3			
	Professional Elective – 4	3	0	0	3			Open Elective – 4	3	0	0	3			
	Professional Elective – 5	3	0	0	3			Total Learning Cr	Total Learning Credits 1						
	Professional Elective – 6	3	0	0	3	1									
	Total Learning Credits				18										

	7. Project Work, Seminar, Internship In Industry / Higher Technical Institutions (P)									
Course	Course Hours/ Week									
Code	Title	L	Т	Ρ	С					
18ECP101L	MOOC-1									
18ECP102L	Industrial Training-1	0	0	2	1					
18ECP103L	Seminar - 1									
18ECP104L	MOOC-2									
18ECP105L	Industrial Training-2	0	0	2	1					
18ECP106L	Seminar - 2									
18ECP107L	Minor Project	0	0	6	3					
18ECP108L	Internship (4-6 weeks)									
18ECP109L	Project	0	0	20	10					
18ECP110L	Semester Internship									
Total Learning Credits										

	8. Mandatory Courses (M)									
Course	Course		lour: Nee							
Code	Title	L	Т	Ρ	С					
18PDM101L	Professional Skills and Practices	0	0	2	0					
18PDM201L 18PDM203L	Competencies in Social Skills Entrepreneurial Skill Development	0	0	2	0					
18PDM202L 18PDM204L	Critical and Creative Thinking Skills Business Basics for Entrepreneurs	0	0	2	0					
18PDM301L 19PDM302L	Analytical and Logical Thinking Skills Entrepreneurship Management	0	0	2	0					
18LEM101T	Constitution of India	1	0	0	0					
18LEM102J	Value Education	1	0	1	0					
18GNM101L	Physical and Mental Health using Yoga	0	0	2	0					
18GNM102L 18GNM103L 18GNM104L	NSS NCC NSO	0	0	2	0					
18LEM109T	Indian Traditional Knowledge	1	0	0	0					
18LEM110L	Indian Art Form	0	0	2	0					
18CYM101T	Environmental Science	1	0	0	0					
	Total Learning Credits				-					

	List of Professional Elective Courses (E) Any 6 Courses				
Course	Course	Hou	irs/W	leek	
Code	Title	1	T	P	С
0000	Sub-Stream: Electronic System Engineering	-			•
18ECE201J	Python and Scien236tific Python	2	0	2	3
18ECE202T	Micro- and Nano-Fabrication Technologies	3	0	0	3
18ECE203T	Semiconductor Device Modeling	3	0	0	3
18ECE204J	ARM based Embedded System Design	2	0	2	3
18ECE205J	FPGA based Embedded System Design	2	0	2	3
18ECE206J	Advanced Digital System Design	2	0	2	3
18ECE207J	Real Time Operating Systems	2	0	2	3
18ECE301J	CMOS Analog IC Design	2	0	2	3
	MEMS Technologies	3	0	0	
18ECE303T	Nanoelectronic Devices and Circuits	3	0	0	3
18ECE304T	Microwave Integrated Circuits	3	0	0	3
	ARM-SoC	2	0	2	3
18ECE306J	ARM based Digital Signal Processing	2	0	2	3
18ECE307J	Applied Machine Learning	2	0	2	3
102020010		-	Ŭ	-	v
	Sub-Stream: Communication System Engg.				
18ECE220T	Advanced Mobile Communication Systems	3	0	0	3
	Radar And Navigational Aids	3	0	0	3
18ECE222T	Adhoc and Sensor Networks	3	0	0	3
18ECE223T	Satellite Communication and Broadcasting	3	0	0	3
	Cryptography and Network Security	3	0	0	3
18ECE225T	Information Theory and Coding	3	0	0	3
18ECE226T	Optical Components, Systems and Networks	3	0	0	3
18ECE320T	Software Defined Networks	3	0	0	3
18ECE321T	RF and Microwave Semiconductor Devices	3	0	0	3
18ECE322T	Opto Electronics	3	0	0	3
18ECE323T	Advanced Optical Communication	3	0	0	3
		-	-	-	-
	Sub-Stream: Signal Processing				
18ECE240T	Wavelets and Signal Processing	3	0	0	3
18ECE241J	Signal Processing for Auditory System	2	0	2	3
18ECE242J	Pattern Recognition and Neural Networks	2	0	2	3
18ECE243J	Digital Image and Video Processing	2	0	2	3
18ECE244J	DSP System Design	2	0	2	3
18ECE245T	Adaptive Signal Processing	3	0	0	3
18ECE340T	Machine Perception with Cognition	3	0	0	3
					-
18ECE341T	Multimedia Compression Techniques	3	0	0	3
		3 3	0	0	3 3

	List of Open Elective Courses (O)				
	Any 4 Courses				
Course	Course		rs/W		_
Code	Title	L	Т	Ρ	С
	Short-Range Wireless Communication	3	0	0	3
	Electronic Circuits & Systems	2	0	2	3
	Modern Wireless Communication Systems	3	0	0	3
18ECO104J	Audio and Speech Processing	2	0	2	3
18ECO105T	Underwater Acoustics	3	0	0	3
18ECO106J	PCB Design and Manufacturing	2	0	2	3
18ECO107T	Fiber Optics and Optoelectronics	3	0	0	3
18ECO108J	Embedded System Design using Arduino	2	0	2	3
18ECO109J	Embedded System Design using Raspberry Pi	2	0	2	3
18ECO110J	3D Printing Hardware and Software	2	0	2	3
18ECO131J	Virtual Instrumentation	2	0	2	3
18ECO132T	Analytical Instrumentation	3	0	0	3
18ECO133T	LOGIC and Distributed Control System	3	0	0	3
18ECO134T	Sensors and Transducers	3	0	0	3
18ECO135T	Fundamentals of MEMS	3	0	0	3
18ECO121T	Basics of Biomedical Engineering	3	0	0	3
18ECO122T	Hospital Information Systems	3	0	0	3
18ECO123T	Biomedical Imaging	3	0	0	3
	Human Assist Devices	3	0	0	3
18ECO125T	Quality Control for Biomedical Devices	3	0	0	3
	Sports Biomechanics	3	0	0	3

23. (f) Program Articulation (B.Tech in Electronics and Communication Engineering)
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					l	Prog	ram L	earn	ing C	outco	mes	(PLO))			
						Grad	uate	Attril	outes						PSO	
Course Code	Course Name	Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modern Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	System Design & Analvsis	Project Management	Natural Sciences and Mathematics
18EES101J	Basic Electrical and Electronics Engineering	Н	М	Н	М	L										Н
18MES103L	Civil and Mechanical Engineering Workshop	М			Н	Н					Н		L	М	L	
18ECS201T	Control Systems	L		Н	М									Н		М
18ECC102J	Electronic Devices	L	М	Н					М	Н				L	L	Μ
18ECC103J	Digital Electronic Principles	L	М	Н					М	Н				Н	L	
18ECC104T	Signals and Systems	L	Н	М												Н
18ECC105T	Electromagnetics and Transmission Lines	L		М	Н									М		Μ
18ECC201J	Analog Electronic Circuits	М	L	Н	Н									Н	L	
18ECC202J	Linear Integrated Circuits	М	L	Н	Η									Н	L	
18ECC203J	Microprocessor, Microcontroller and Interfacing Techniques		М	Н		Н							L	Н	L	
18ECC204J	Digital Signal Processing	М	L	Н	Η	Н	М	L							L	Η
18ECC205J	Analog and Digital Communication	L	L	Н	Н	Н			Н	М			Μ	Н	L	
18ECC206J	VLSI Design															
18ECC301T	Wireless Communication	М					L	Н	М					L		М
18ECC302J	Microwave & Optical Communications	L	L	Н		М			L	L				L	L	
18ECC303J	Computer Communication Networks			М			Н	Н	L	L			М	L	L	
18ECC350T	Comprehension															
18ECP101L	MOOC / Industrial Training / Seminar – 1						М	L			Н		Н		М	
18ECP102L	MOOC / Industrial Training / Seminar – 2						М	L			Н		Н		М	
18ECP103L	Project (Phase-I) / Internship (3-4 weeks)	М	М	Н	Н	М	Н	Н	L	Н	Н	Н	Н	Н	Н	М
18ECP103L	Project (Phase-II) / Semester Internship	Μ	М	Н	Н	М	Н	Н	L	Н	Н	Н	Н	Н	Н	Μ

H – High Correlation, M – Medium Correlation, L – Low Correlation, PSO – Program Specific Outcomes (PSO)

23. (g) Implementation Plan (B. Tech in Electronics and Communication Engineering)

	Semester - I						Semester – II				
		Hours	s/ We	ek				Hoi	irs/\	Neek	-
Code	Course Title			P	С	Code	Course Title	L	T		С
18LEH102J-	Foreign Language (Chinese/ French/ German/	2	0	2	3	18LEH101J	English	2	0		3
18LEH106J	Japanese / Korean)					18MAB102T	Advanced Calculus and Complex Analysis	3	1	0	4
18MAB101T	Calculus and Linear Algebra	3		0	4	18PYB101J	Physics: Electromagnetic Theory, Quantum	3	1	2	5
18CYB101J	Chemistry			2	5		Mechanics, Waves and Optics				-
18CSS101J 18MES103L	Programming for Problem Solving			4	5 3	18MES101L	Engineering Graphics and Design	1	0		3
	Civil and Mechanical Engineering Workshop Professional Skills and Practices			2	0	18EES101J 18PDH101L	Basic Electrical and Electronics Engineering General Aptitude	3	0	2	5
18LEM102J	Value Education			1	0	18LEM101T	Constitution of India	1	0		0
				2	0	18GNM101L	Physical and Mental Health using Yoga	0	0		0
	Total Learning Credit		•		20		Total Learning Credits	-		-	21
	Someodor III						Comester IV				
	Semester - III	Llaura	/ \\/-				Semester - IV	Lin		M1-	1
Code	Course Title		Т	Р	С	Code	Course Title	L	Τ	Neek P	С
18MAB201T	Transforms and Boundary Value Problems				4	18MAB203T	Probability and Stochastic Process	3	1	-	4
18ECS201T	Control Systems				3	18BTB101T	Biology	2	0	0	2
18ECC102J	Electronic Devices				4		Analog Electronic Circuits	3	0	2	4
18ECC103J 18ECC104T	Digital Electronic Principles Signals and Systems			-	4	18ECC202J	Linear Integrated Circuits Professional Elective-1	3	0	2	4
18ECC1041	Electromagnetics and Transmission Lines				3		Open Elective-1	3	0	-	3
	Social Engineering				-	18PDH102T	Management Principles for Engineers	2	0		2
18PDH103.1				2							0
18PDH103J 18PDM201L					2		Critical and Creative Thinking Skills	0	0		0
18PDM201L	Competencies in Social Skills	0	0	2 0	2 0 0 24	18PDM202L	Critical and Creative Thinking Skills Total Learning Credit	-	0	2	22
18PDM201L	Competencies in Social Skills Environmental Science	0	0	2 0	0 0			-		2	
18PDM201L	Competencies in Social Skills Environmental Science Total Learning Credits	0 1 s	0 0 s/ We	2 0	0 0		Total Learning Credit	S Hou	urs/ \	Neek	
18PDM201L 18CYM101T Code	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title	0 1 s Hours	0 0 s/ We T	2 0	0 24	18PDM202L	Total Learning Credits Semester - VI Course Title	Hou		Week	22
18PDM201L 18CYM101T Code 18MAB302T	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers	0 1 s Hours L 3	0 0 s/ We T 1	2 0 2 8 2 8 8 8 8 7 0	0 0 24 C 4	18PDM202L	Total Learning Credits	S Hou	urs/ \ T	Week	22 C
18PDM201L 18CYM101T Code	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques	0 1 s Hours L 3	0 0 s/ We T 1	2 0	0 0 24 C	18PDM202L Code 18ECC206J	Total Learning Credits Semester - VI Course Title VLSI Design	Hou L 3	urs/ \ T 0	Week P 2 2	22 C 4
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing	0 1 s L 3 3 3	0 0 5/ We T 1 0	2 0 2 2 2	0 0 24 C 4 4 4	18PDM202L Code 18ECC206J 18ECC302J	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension	Hou L 3 3 3 0	urs/ \ T 0 0 1	Neek P 2 2 2 2 0	22 C 4 4 4 1
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication	0 1 s Hours L 3 3 3 3 3	0 0 5/ We T 1 0 0	2 0 2 2 2	0 0 24 C 4 4 4 4 4	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3	Hou L 3 3 3 0 3	urs/ \ T 0 0 1 0	Week P 2 2 2 0 0	22 C 4 4 4 1 3
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2	0 1 s Hours L 3 3 3 3 3 3 3	0 0 5/ We T 1 0 0 0	2 0 2 2 2 0	0 0 24 C 4 4 4 4 3	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4	Hou L 3 3 3 0 3 3 3	urs/ \ T 0 0 1 0 0	Week P 2 2 2 2 0 0 0	22 C 4 4 4 1 3 3
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2	0 1 s Hours L 3 3 3 3 3 3 3 3	0 0 0 0 1 1 0 0 0 0 0 0	2 0 0	0 0 24 C 4 4 4 4 3 3	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC303J	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3	Hou L 3 3 3 3 3 3 3 3 3 3	urs/ \ T 0 0 1 0 0 0	Week P 2 2 2 0 0 0 0 0	22 C 4 4 4 1 3 3 3 3
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECC205J	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1	0 1 s Hours L 3 3 3 3 3 3 0	0 0 5/ We T 1 0 0 0 0 0 0	2 0 0	0 0 24 C 4 4 4 4 3 3 1	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC350T 18ECC350T	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3 MOOC / Industrial Training / Seminar – 2	Hou L 3 3 3 0 3 3 0 3 0 0	urs/ \ T 0 0 1 0 0 0 0 0	Week P 2 2 2 0 0 0 0 0 0 2	22 C 4 4 4 1 3 3 3 1
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills	0 1 s Hours L 3 3 3 3 3 0 0 0	0 0 5/ We T 1 0 0 0 0 0 0 0 0 0	2 0 0	0 0 24 C 4 4 4 3 3 1 0	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC3050T 18ECC102L 18ECP102L 18PDH201L	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices	Hou L 3 3 3 3 0 0 3 3 0 0 0	urs/ \ T 0 0 0 1 0 0 0 0 0	Weekk P 2 2 2 2 0 0 0 0 0 0 0 2 2 2	22 C 4 4 4 1 3 3 3 1 1 1
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1	0 1 1 1 S S	0 0 5/ We T 1 0 0 0 0 0 0 0 0 0	2 0 0	0 0 24 C 4 4 4 4 3 3 1	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC350T 18ECC350T	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3 MOOC / Industrial Training / Seminar – 2	Hou L 3 3 3 0 0 3 3 3 0 0 0 1	urs/ \ T 0 0 1 0 0 0 0 0	Weekk P 2 2 2 2 0 0 0 0 0 0 0 2 2 2	22 C 4 4 4 1 3 3 3 1
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits	0 1 1 1 S S	0 0 5/ We T 1 0 0 0 0 0 0 0 0 0	2 0 0	0 0 24 C 4 4 4 4 4 3 3 1 0 0	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC3050T 18ECC102L 18ECP102L 18PDH201L	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credits	Hou L 3 3 3 0 0 3 3 3 0 0 0 1	urs/ \ T 0 0 0 1 0 0 0 0 0	Weekk P 2 2 2 2 0 0 0 0 0 0 0 2 2 2	22 C 4 4 4 1 3 3 3 1 1 1 0
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form	0 1 s Hourse L 3 3 3 3 3 3 0 0 0 0 0 s s	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	0 0 24 C 4 4 4 4 4 3 3 1 0 0	18PDM202L Code 18ECC206J 18ECC302J 18ECC303J 18ECC3050T 18ECC102L 18ECP102L 18PDH201L	Total Learning Credits Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge	Hou L 3 3 3 3 0 0 3 3 3 0 0 0 0 0 1 5	urs/ \ T 0 0 0 1 0 0 0 0 0 0 0 0	Week P 2 2 2 2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	22 C 4 4 4 1 3 3 3 1 1 1 0
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits	0 1 S Hours 3 3 3 3 3 3 3 3 3 3 3 3 3	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 3 3 1 0 0	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credit Semester - VIII Course Title	Hou L 3 3 3 0 0 3 3 3 0 0 0 1	urs/ \ T 0 0 0 1 0 0 0 0 0 0 0 0	Week P 2 2 2 2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	22 C 4 4 4 1 3 3 3 1 1 1 0
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L 18LEM110L	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits Semester - VII Course Title Wireless Communications	0 1 1 1 S - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - -	0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 4 4 4 23 C 23 C C 4	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-4 Open Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credits Semester - VIII	Hou L 3 3 3 3 0 0 3 3 3 0 0 0 0 0 1 5	urs/ \\ T 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 7 5/ W T	Week P 2 2 2 0 0 0 0 0 2 2 0 0	22 C 4 4 4 4 1 3 3 3 1 1 1 0 24
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L 18LEM110L Code	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits Semester - VII Course Title Wireless Communications Professional Elective-5	0 1 1 1 S	0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 4 4 4 3 3 1 0 0 23 C 4 3 3	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credit Semester - VIII Course Title	Hou L 3 3 3 3 0 0 0 1 5	urs/ \\ T 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 7 5/ W T	Week P 2 2 2 0 0 0 0 0 2 2 0 0 0 0 0 0 0 0 0	22 C 4 4 4 4 1 3 3 3 1 1 1 0 24 C
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC205J 18ECP101L 18PDM301L 18LEM110L Code	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits Semester - VII Course Title Wireless Communications Professional Elective-5 Professional Elective-6	0 1 1 1 S - L - 3 3 3 3 3 3 3 3 3 3 S -	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 4 4 4 3 3 1 0 0 23 C 4 3 3 3 3	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credit Semester - VIII Course Title	Hou L 3 3 3 3 0 0 0 1 5	urs/ \\ T 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 7 5/ W T	Week P 2 2 2 0 0 0 0 0 2 2 0 0 0 0 0 0 0 0 0	22 C 4 4 4 4 1 3 3 1 1 1 0 24 C
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits Semester - VII Course Title Wireless Communications Professional Elective-5 Professional Elective-6 Open Elective-4	Hours L 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3 J 3	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 4 4 3 3 1 0 0 23 C 4 4 4 4 3 3 3 3 3 3 3	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credit Semester - VIII Course Title	Hou L 3 3 3 3 0 0 0 1 5	urs/ \\ T 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 7 5/ W T	Week P 2 2 2 0 0 0 0 0 2 2 0 0 0 0 0 0 0 0 0	22 C 4 4 4 4 1 3 3 3 1 1 1 0 24 C
18PDM201L 18CYM101T Code 18MAB302T 18ECC203J 18ECC204J 18ECC205J 18ECC	Competencies in Social Skills Environmental Science Total Learning Credits Semester - V Course Title Discrete Mathematics for Engineers Microprocessor, Microcontroller and Interfacing Techniques Digital Signal Processing Analog and Digital Communication Professional Elective – 2 Open Elective – 2 MOOC / Industrial Training / Seminar – 1 Analytical and Logical Thinking Skills Indian Art Form Total Learning Credits Semester - VII Course Title Wireless Communications Professional Elective-5 Professional Elective-6	Image: 0 Image: 0 Image: 1 1 Image: 1 3 Image: 2 3 Image: 3 3	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 24 C 4 4 4 4 4 4 4 4 3 3 1 0 0 23 C 4 3 3 3 3	18PDM202L Code 18ECC206J 18ECC303J 18ECC305 18EC	Total Learning Credit Total Learning Credit Semester - VI Course Title VLSI Design Microwave and Optical Communications Computer Communication Networks Comprehension Professional Elective-3 Professional Elective-3 MOOC / Industrial Training / Seminar – 2 Employability Skills and Practices Indian Traditional Knowledge Total Learning Credit Semester - VIII Course Title	Hou L 3 3 3 3 0 0 0 1 5	urs/ \\ T 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 7 5/ W T	Week P 2 2 2 0 0 0 0 0 2 2 0 0 0 0 0 0 0 0 0	22 C 4 4 4 4 1 3 3 3 1 1 1 0 24 C

B. Tech in Electronics and Communication Engineering

2018 Regulations

Engineering Science Courses (S)

Department of Electronics and Communication Engineering SRM Institute of Science and Technology SRM Nagar, Kattankulathur – 603203, Kancheepuram District, Tamilnadu

Course Code	18ECS201T	Course Name		CONTROL	SYSTEMS	Course Category	Professional Core	L 3	Т 0	P 0	C 3
Pre-requis Courses		18MAB10	27 Co-requis Courses	e	18ECC104T	Progressive Courses	Nil				
Course Offe	ring Department		Electronics and Communication	Engineering	Data Book / Codes/Standards		Nil				

Course Le	earning Rationale (CLR):	The purpose of learning this course is to:	Le	earni	ng				Prog	ram l	earn	ing O	utcor	nes (I	PLO)			
CLR-1 :	Learn about mathematical r	nodeling techniques of mechanical and electrical systems	1	2	3	1 :	2 3	4	5	6	7	8	9	10	11	12	13 14	15
CLR-2 :	Impart knowledge about the	e transient and steady state error and analysis									,						ent	ь Н
CLR-3 :	Identify and analyze stabilit	y of a system in time domain using root locus technique		_	_			сh			Sustainability						Ĕ	Research
CLR-4 :	Know about different freque	ncy domain analytical techniques	(Bloom)	(%)	(%)	ge	ţ	sea			aina		Work		e		onal Manage	Res
		controller for specific applications	(B	ency.	Jent	Nec.		Re 1	ge		uste		٨u		Finance	p	Ma	~ð
CLR-6 :	Impart knowledge on contro	Iler tuning methods	king	Proficiency	Attainment	ê.	Analysis	esign,	Usa	Culture	~ð		Team	ы	& Fi	eaming	ofessio nt roject l	es Analyze
			Thinking	A Pro		bui			ō	& Cu	rent		~ð	icati	Mgt. 8		Profe	
Course Le	earning Outcomes (CLO):	At the end of this course, learners will be able to:	Level of .	Expected	Expected	Engineering Knowledge	<u> </u>	Analysis,	Modern 7	Society 8	Environment	Ethics	Individual	Comm unication	Project N	Life Long	PSO-1: Achiever PSO - 2:	PSO - 3
CLO-1 :	Determine Transfer function	of a system by mathematical modeling, block diagram reduction and signal flow graphs	1,2	80	80	H I	Ηŀ	H	-	-	-	-	-	-	-	Н	Η -	Н
CLO-2 :	Identify the standard test in	outs, time domain specifications and calculate steady state error	1,2	85	80	H I	H F	H	Н	-	-	-	-	-	-	Н	Η -	Н
CLO-3 :	Plot a root locus curve and	analyze the system stability using Routh array	2,3	90	85	H I	ΗH	H	Н	-	-	-	-	-	-	Н	Н -	Н
CLO-4 :			2,3	90	85	H I	ΗH	H	Н	-	-	-	-	-	-	Н	Н -	Н
CLO-5 :				80	80	ΗI	ΗH	H	Н	-	-	-	-	-	-	Н	H H	Н
	Identification of controller pa		1,2,3	85	85													

	ration iour)	9	9	9	9	9
	SLO-1	Open and closed loop control system	Standard test signals and their expression	Poles and zeros of a system	Frequency domain analysis	Controllers-Significance and Need
S-1	SLO-2	Feedback and Feed forward control systems	Type number and order of a system	Pole zero plot and concept of s plane	Frequency domain specifications	Stability of closed loop systems
S-2	SL0-1	Transfer function of a system and basis of Laplace transforms		Proper, Strictly Proper and Improper systems	Frequency domain plots, minimum and non minimum phase systems	SISO and MIMO control systems
0-2	SLO-2	Need for mathematical modeling	Transfer function of First order system Impulse and parabolic signal	Characteristic equation	Correlation between time and frequency domain	Types of controllers-ON-OFF,P,I,D
S-3	SLO-1	Representation of mechanical translational systems using differential equation and	General transfer function of second order system	Concept of stability from pole zero location	Bode plot approach and stability analysis	Composite Controller-PI,PD and PID
	SLO-2	determination of transfer function	s Step and ramp signal systems mathematical modeling Transfer function of First order system Impulse and parabolic signal Characteristic equation tation of mechanical translational using differential equation and tition of transfer function General transfer function of second order system Concept of stability from pole zero location Identification of damping factor and classification based on it Need for Stability analysis and available techniques Step response of critically damped second Necessary and sufficient Condition		Rules for sketching bode plot	Controller parameters and tuning methods
S-4	SL0-1	Representation of mechanical rotational systems		Necessary and sufficient Condition for stability	Bode plot of typical systems	Design Specification, controller
0-4	SLO-2	persentation of mechanical rotational systems order system stability d determination of transfer function Significance of Routh Hurwitz order system Technique	0		configurations- ON-OFF controller	
S-5	SLO-1	Conversions of Mechanical system to Electrical system	Step response of over damped second order system	Computation of Routh array		Design Specification, controller configurations-PID controller

	SLO-2	f-V and f-I electrical analogies	Step response of undamped second order system	Routh array of stable systems		
S-6	SL0-1	Block diagram reduction rules and methodology	Time domain specifications and their significance	Routh array of Unstable systems	Polar plot and significance	Design of speed control system for DC motor
	SLO-2		Numerical solution	Routh array of Unstable systems	Nyquist stability criterion	motor
S-7	SLO-1	Evaluation of transfer function using block diagram	Transient and steady state error analysis	Root locus technique		Design of control system for Twin Rotor Multi input Multi output System(TRMS)
3-1	SLO-2	reduction	Static and dynamic Error coefficients	Rules for sketching root locus		with one degree of freedom
S-8	SLO-1	Signal flow graphs and evaluation of transfer	Static error constants and evaluation of	Post logue plat of turical quaterna	Palar plat of tunical avatama	Case study 1
3-0	SLO-2	function	steady state error	Root locus plot of typical systems	Polar plot of typical systems	Case sludy 1
S-9	SLO-1	Plack diagram to signal flow conversion	Dynamic error constants and evaluation of	Post logue plat of turical quaterna	Polor plot of tunical systems	Coop of under 2
5-9	SLO-2	Block diagram to signal flow conversion	steady state error	Root locus plot of typical systems	Polar plot of typical systems	Case study 2

_		 Nagrath.J and Gopal.M,, "Control System Engineering", 5th Edition, New Age, 2007 Benjamin C Kuo, "Automatic Control System", 9th edition, John Wiley & Sons, 2010 	3. G 4. S
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Gopal.M, "Control System Principles and Design", 2nd Edition, TMH, 2002 Sivanandam and Deepa, "Control system Engineering using MATLAB", 2nd edition, Vikas publishers, 2007

Learning Assess	sment					
	Bloom's		Continuous Learnin	g Assessment (50% weightage)		Final Examination (50% weightage)
	Level of Thinking	CLA – 1 (10%)	CLA – 2 (15%)	CLA – 3 (15%)	CLA – 4 (10%)	
Level 1	Remember Understand	40%	30%	30%	30%	30%
Level 2	Apply Analyze	40%	40%	40%	40%	40%
Level 3	Evaluate Create	20%	30%	30%	30%	30%
	Total	100 %	100 %	100 %	100 %	100 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	Dr. T.Deepa, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	Mrs.R.Bakhya Lakshmi, SRMIST

B. Tech in Electronics and Communication Engineering

2018 Regulations

ProfessionalCore Courses (C)

Department of Electronics and Communication Engineering SRM Institute of Science and Technology SRM Nagar, Kattankulathur – 603203, Kancheepuram District, Tamilnadu

Course Code	18ECC102J	Course Name		ELEC	TRONIC DEVICES		Cours Catego	-	С				Prof	essior	nal Co	ore					L 3	T 0	P 2	C 4
Pre-requis Course				Co-requisite Courses	Nil			rogres Cours		18ECC20	1J, 18	ECC2	02J, 1	8ECE	203T	, 18E	CE30)3T, 1	8ECE	53217	, 18E	CE32	?T	
Course Offe	ering Department	Electron	nics and Comm	nunication Enginee	ring Data Book	/ Codes/Standards	Nil																	
Course Lea	rning Rationale (CL	R): The pur	pose of learnin	g this course is to:				Learn	ing					Progr	am L	earni	ng O	utcor	nes (PLO)				
					unction is formed and its		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-3: D CLR-4: D CLR-5: D CLR-5: U	iscuss the basic cha escribe the basic str escribe the basic str	racteristics of s ructure, operati ructure, operati ring tools such	several other ty on and charact on and charact as PSPICE to	vpes of diodes that teristics of BJT, and teristics of MOSFE carry out design ex	propriate diode applicatio are designed for specific I discuss its use as a swi T, and discuss its use as periments and gain expe	applications itch and an amplifier. a switch and an amplifi	er.	Expected Proficiency (%)	Expected Attainment (%)	Enaineerina Knowledae	alysis	& Development	Design, Research	I Usage	Culture	t & Sustainability		Team Work	tion	& Finance	aming	oSO-1: Professional Achievement	Project Management	Analvze & Research
Course Lea	rning Outcomes (C	LO): At the e	end of this cour	se, learners will be	able to:		40	Expected P	Expected A	Engineering	Problem Analysis	Design & Do	Analysis, De	Modem Tool Usage	Society & C	Environment &	Ethics	Individual &	Communication	Project Mgt.	Life Long Leaming	PSO-1: Pro	FSU - Z: PI	PSO - 3. Ar
	Inderstand the opera	tion, character	istics, paramet	ers and specification	ons of semiconductor dio	des and special diodes	1	90	80	Н	-	-	-	-	-	-	-	-	-	-	М	-	-	-
	emonstrate importar						2	2 80	75	-	-	-	-	-	-	-	-	-	-	-	М	-	-	-
	eview bipolar transis nd switching.	stor constructio	n, operation, c	haracteristics and p	parameters, as well as its	application in amplifica	tion	90	80	Н	-	-	-	-	-	-	-	-	-	-	М	-	-	-
	eview field-effect tra mplification and swit		ction, operatio	n, characteristics a	nd parameters, as well a	s its application in	1	80	75	Н	-	-	-	-	-	-	-	-	-	-	М	-	L	-
					operating characteristics		3		75	-	-	-	-	Н	-	-	-	-	-	-	-	L	L	-
CLO-6 : S	olve specific design	problem, which	h after complet	ion will be verified	using modern engineering	g tools such as PSPICE	. 3	3 90	75	-	-	-	-	Н	-	-	L	Н	М	-	М	-	-	-
Duration	Semio	conductor Dio	des	Diod	e Circuits	Special	Diodes			Bip	olar Ju	inctio	n Tra	nsisto	ors			MOS	Field	d-Effe	ct Tra	ansist	ors	
(hour)		15			15	1:	i					15	;							1	5			
SLC	Basic semicond	luctor theory: li	ntrinsic &	HWR operation Et	ficiency and rinnle factor	Backward diode				Physical s	ructur	'n					Dhuci	cal st	ructur	'n				

Subscription (hour) SLO-1 Basic semiconductor extrinsic semicondu SLO-1 Basic semiconductor extrinsic semicondu SLO-2 Current flow in semi SLO-2 SLO-1 PN junction theory: SLO-2 Forward biased PN SLO-1 Reverse biased PN SLO-2 Relation between C SLO-2 Relation between C SLO-1 Lab 1: PN Junction	Semiconductor Diodes	Diode Circuits	Special Diodes	Bipolar Junction Transistors	MOS Field-Effect Transistors	
(h	nour)	15	15	15	15	15
S 1	SLO-1	Basic semiconductor theory: Intrinsic & extrinsic semiconductors	HWR operation, Efficiency and ripple factor	Backward diode	Physical structure	Physical structure
3-1	SLO-2	Current flow in semiconductors	Problem solving	Varactor diode	Device operation of BJT	Device operation of E-MOSFET & D- MOSFET
6.2	SLO-1		Center-Tapped Transformer FWR operation, Efficiency and ripple factor	Step recovery diode	Current-Voltage characteristics of CE BJT configuration	I-V characteristics of E-MOSFET
3-2	SLO-2	Forward biased PN junction	Problem solving	Point-contact diode	Current-Voltage characteristics of CE BJT configuration	Problem solving
6.2	SLO-1	Reverse biased PN junction	Bridge FWR operation, Efficiency and ripple factor	Metal-semiconductor junction: Structure, Energy band diagram	Current-Voltage characteristics of CB BJT configuration	Derive drain current
3-3	SLO-2	Relation between Current and Voltage	Problem solving	Forward & Reverse Characteristics of Schottky Diode	Current-Voltage characteristics of CB BJT configuration	Problem solving
-		Lab 1: PN Junction Diode Characteristics	Lab 4: Diode clipping and clamping circuits	Lah / Sorios and Shunt Regulators	Lab 10: BJT and MOSFET Switching Circuits	Lab 13: Repeat Experiments
S-6	SLO-1	Calculate depletion width	Filters: Inductor & Capacitor Filters	Tunnel Diode	Current-Voltage characteristics of CC BJT configuration	Derive transconductance
3-0	SLO-2	Calculate barrier potential	Problem solving	Tunnel Diode	Current-Voltage characteristics of CC BJT configuration	Problem solving

	01.0.4			Querra Diada	P IT as an amplifiar	0400 557
S-7	SLO-1	Derive diode current equation	Filters: LC & CLC Filters	Gunn Diode	BJT as an amplifier	CMOS FET
5-7	SLO-2	Derive diode current equation	Problem solving	Gunn Diode	BJT as a switch	MOSFET as an amplifier
S-8	SLO-1	Effect of Capacitance in PN junction: Transition Capacitance	Diode Clippers	IMPATT Diode	BJT circuit models – h-parameter	MOSFET as a switch
3-0	SLO-2	Diffusion Capacitance	Problem solving	IMPATT Diode	BJT circuit models – hybrid-π parameter	Problem solving
S 9-10	SLO-1 SLO-2	Lab 2: Zener diode characteristics	Lab 5: BJT Characteristics	Lab 8: MOSFET Characteristics	Lab 11: Photoconductive Cell, LED, and Solar Cell Characteristics	Lab-14: Model Examination
S-11	SLO-1	Energy band structure of PN Junction Diode	Diode Clampers	PIN Diode	BJT biasing circuits and stability analysis: Base bias and emitter bias	Biasing Circuits for MOSFET: Gate Bias
3-11	SLO-2	Ideal diode and its current-voltage characteristics	Problem solving	PIN Photodiode	Problem solving	Problem Solving
S-12	SLO-1	Terminal characteristics & parameters	Voltage Multipliers	Avalanche photodiode	Voltage-divider bias	Self-bias
5-12	SLO-2	Diode modeling	Zener diode: Characteristics, breakdown mechanisms	Laser diode	Problem solving	Problem Solving
S-13	SLO-1	DC load line and analysis	Zener resistances and temperature effects Zener diode as voltage regulator	Problem solving	Collector-feedback bias	Voltage-divider bias
3-13	SLO-2	Problem solving	Problem solving	Problem solving	Problem solving	Problem Solving
S 14-15	SLO-1 SLO-2	Lab 3: Diode rectifier circuits	Lab 6: BJT Biasing Circuits	Lab 9: MOSFET Biasing Circuits	Lab 12: Simulation experiments using PSPICE	Lab 15: End-Semester Practical Examination

	1.	David A. Bell, Electronic Devices and Circuits, 5 th ed., Oxford University Press, 2015	5.	Robert L. Boylestad, Louis Nashelsky, Electronic Devices and Circuit Theory, 11th ed., Pearson Education, 2013
Learning	2.	Donald Neamen, Electronic Circuits: Analysis and Design, 3rd ed., McGraw-Hill Education, 2011	6.	Muhammad Rashid, Microelectronic Circuits: Analysis & Design, 2 nd ed., Cengage Learning, 2010
Resources	3.	Adel S. Sedra, Kenneth C. Smith, Microelectronic Circuits: Theory and Applications, OUP, 2014	7.	Muhammed H Rashid, Introduction to Pspice using OrCAD for circuits and electronics, 3rd ed., Pearson, 2004
	4.	Thomas L. Floyd, Electronic Devices", 9th ed., Pearson Education, 2013	8.	Laboratory Manual, Department of ECE, SRM University

Learning Asses	ssment												
	Bloom's	Continuous Learning Assessment (50% weightage)								Einal Examinatio	n (50% weightage)		
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – 3	3 (15%)	CLA – 4	4 (10%)#		ii (50 % weigiilage)		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Lovel 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%		
Level 1	Understand	20%	20%	1376	1376	1370	1376	1376	1370	1370	1376		
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
	Analyze	2070	2070	2070	2070	2070	2070	2070	2070	2070	2070		
Level 2	Evaluate	100/	100/	150/	150/	1 = 0/	150/	150/	150/	150/	150/		
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%		
	Total	100	0 %	100	0%	100 %		10	0 %	-			

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. Manikandan AVM, SRMIST
2. Mr. Hariharasudhan – Johnson Controls, Pune, hariharasudhan.v@ici.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. Diwakar R Marur, SRMIST

Course Code	18ECC103J	Course Name	DIGITAL ELECTRO	DNIC PRINCIPLES		urse gory		С				Prof	ession	nal Co	ore					L 1 3 (Г F) 2	`	2 4
Pre-requ Cours		18EES101J	Co-requisite Courses	Nil		Prog Cor	ressi urses					18E	CC20	3J, 1	8ECC	206J,	18EC	CE206	J				
Course O	ffering Department	Electronics and (Communication Engineering	Data Book / Codes/Standards	s /	Vil																	
Course Le	earning Rationale (CL	R): The purpose of le	earning this course is to:			Lea	arnin	g				I	Progra	am L	earni	ng Ou	tcom	ies (Pl	LO)				
CLR-1 :	Understand binary cod	es, digital arithmetic op	perations and able to simplify Bo	oolean logic expressions		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 ′	14 1	15
CLR-3 : CLR-4 : CLR-5 :	Able to design simple of Familiarize with basic s analyze sequential logi Know how to implemen Use modern engineeri	combinational logics us sequential logic compo ic circuits and Finite St nt logic circuits using P ing tools such as PSPI	ate Machines. LDs.	ters and their usage, and able to desi experiments and gain experience wi		Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment (%)	Engineering Knowledge	Problem Analysis	L Development	Analysis, Design, Research	Modem Tool Usage	& Culture	nent & Sustainability		ndividual & Team Work	nication	Agt. & Finance	Life Long Learning	Professional Achievement	Jes	: Analyze & Research
Course Le	earning Outcomes (CL	.O): At the end of this	s course, learners will be able to	:		Level of .	Expected	Expected		Problem	Design & I	Analysis,	Modem 7	Society 8	Environment &	Ethics	Individua	Communication	Project Mgt.	Life Long	PSO-1: F	Ξ	PSO – 3:
CLO-1 :	Have a thorough under	rstanding of the fundan	nental concepts and techniques	used in digital electronics.		1	90	75	Н	-	-	-	-	-	-	-	-	-	-	М	-	-	-
CLO-2 :	Understand the basic e	electronics of various lo	gic families and able to use Inte	egrated Circuits.			80	70	Н	-	-	-	-	-	-	-	-	-	-	М	-	-	-
CLO-3 :			t various combinational logic cir				90	75		М	Н	-	Н	-	-	-	-	-	-	М	-	-	-
CLO-4 :			t various clocked sequential log	ic circuits.			90	75		М	Н	-	Н	-	-	-	-	-	-	М	-	-	-
CLO-5 :			I logic circuits using PLDs			2,3	80	75	-	М	Н	-	Н	-	-	-	-	-	-	-	-	-	-
CLO-6 :	Solve specific design p Logisim	roblem, which after co	mpletion will be verified using m	odern engineering tools such as PSF	PICE /	3	90	75	-	М	Н	-	Н	-	-	L	Н	М	L	М	М	-	L

	ration	Binary Codes, Digital Arithmetic and Simplification of Boolean Functions	Logic Families	Combinational Systems	Sequential Systems	Memory and Programmable Logic
, u	iour)	15	15	15	15	15
S-1		Binary Codes, Digital Arithmetic and Simplification of Boolean Functions	Introduction	Binary arithmetic units	Flip-flop and Latch: SR latch,	RAM Memory decoding
0-1	SLO-2	Error detecting codes	TTL Logic Family	Adder	JK flip-flop, T flip-flop,D flip-flop	ROM
S-2	SLO-1	Error correcting code	Totem-pole TTL	Design of Half adder		Programmable Logic Devices (PLDs): Basic concepts
0-2	SLO-2	Hamming Code	open-collector and tristate TTL	Design of Full adder	Master-slave JK flip-flop	PROM
S-3	SLO-1	Arithmetic number representation	Schottkey TTL, standard TTL characteristics	Subtractor	Registers & Counters	PROM as PLD
0-0	SLO-2	Binary arithmetic	Metal Oxide Semiconductor logic families	Design subtractor using logic gates	Shift registers (SISO, SIPO, PISO, PIPO)	Programmable Array Logic (PAL)
S 4-5	SLO-1 SLO-2	LAB 1: Study of logic gates		LAB 7: Implement combinational logic functions using standard ICs	LAB 10: Design and implement Synchronous Counters	LAB 13: Construct combinational circuit using Logisim
S-6	SLO-1	Hexadecimal arithmetic	N-MOS	n-bit parallel adder & subtractor	Universal shift register	Programmable Array Logic (PAL)

	SLO-2	Hexadecimal arithmetic	P-MOS	look ahead carry generator	Counters: Asynchronous/Ripple counters	Programmable Logic Array (PLA)
	SLO-1	BCD arithmetic simplification	CMOS logic circuits	Decoder	Synchronous counters, Modulus-n Counter	Programmable Logic Array (PLA)
S-7	SLO-2	Minimization of Boolean Functions: Algebraic simplification	Characteristics of MOS logic	Encoder	Ring counter, Johnson counter	Design combinational circuits using PLD's
S-8	SLO-1	Problems on Algebraic simplification	Compare MOS logic circuits(CMOS) with TTL digital circuit	Multiplexer	Up-Down counter	Design combinational circuits using PLD's
0-0	SLO-2	Karnaugh map simplification	Electrical characteristics	Demultiplexer	Mealy and Moore model	Design combinational circuits using PLD's
S 9-10		LAB 2: Design and implement Adder and Subtractor using logic gates	LAB 5: Design and implement Multiplexer and Demultiplexer using logic gates	LAB 8: Verify characteristic table of flip- flops	LAB 11: Construct and verify shift registers	LAB 14: Model Practical Examination
S-11	SLO-1	Problems on Karnaugh map simplification	Fan-out	Code converters	Synchronous (Clocked) sequential circuits	Design of combinational circuits using PLD's
0-11	SLO-2	Problems on Karnaugh map simplification	Propagation Delay	Magnitude comparators	Synchronous (Clocked) sequential circuits	Design sequential circuits using PLD's
	SLO-1	Quine McCluskey	Power dissipation	Magnitude comparators	Synchronous (Clocked) sequential circuits	Design sequential circuits using PLD's
S-12	SLO-2	Tabulation method	Noise margin	Parity generators (Odd parity)	Analyze and design synchronous sequential circuits	Design sequential circuits using PLD's
6 12	SLO-1	Problems on Quine McCluskey or Tabulation method.	Supply voltage levels	Parity generators (Even parity)	State reduction	Design sequential circuits using PLD's
S-13	SLO-2	Exercise problems using Tabulation method	Operational voltage levels	Implementation of combinational logic by standard IC's.	State assignment	Design sequential circuits using PLD's
S 14-15		Lab 3: Design and Implement 2-bit Magnitude Comparator using logic gates	LAB-6: Design and implement code converters using logic gates	LAB 9: Construct and verify 4-bit ripple counter, Mod-10/Mod-12 ripple counters	Lab 12: Construct mini project work	LAB 15: University Practical Exam

 	1.	Morris Mano M, Michael D. Ciletti, Digital Design with an Introduction to the Verilog HDL, 5 th ed., Pearson Education. 2014	4.
Learning Resources	2.	Charles H Roth (Jr), Larry L. Kinney, Fundamentals of Logic Design, 5th ed., Cengage Learning India	5.
	3	Edition, 2010 Thomas L. Elovd. Digital Fundamentals, 10 th ed. Pearson Education, 2013	6.

Ronald J. Tocci, Digital System Principles and Applications, 10th ed., Pearson Education, 2009 Donald P Leach, Albert Paul Malvino, Goutam Saha, Digital Principles and Applications, 6th ed., Tata-

Mcgraw Hill, 2008

LAB MANUAL, Department of ECE, SRM University

Learning Ass	sessment													
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examination	(EO9/ weightege)			
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#	 Final Examination (50% weightage 				
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
r. Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%			
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%			
Level 3	Evaluate Create	10%	10%	15% 15% 15% 15%		15% 15%		15%	15%					
	Total	100 %		100 %		100	0 %	100	0 %	-				

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18EC:C1041	Course Name	SIGNALS	AND SYSTEMS	-	ourse tegory	,	С				Prot	fessior	nal Co	ore					L	T F 1 0	C 4
Pre-requisite Courses		Nil	Co-requisite Courses	18MAB201T			gress ourse				18EC	CC204	4J, 18E		201T, 1	8ECI	E2401	r, 18E	ECE24	41J		
Course Offerin	ig Department	Ele	ectronics and Communication Enginee	ring Data Book / Codes/Standards										Nil								
Course Learnin	ng Rationale (CLR):	The pur	pose of learning this course is to:			L	earniı	ng					Progr	am L	earnir	ng Ou	utcom	es (P	LO)			
CLR-1 : Know	w about requirements	of signal a	and system analysis in communication			1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 1	4 15
CLR-3 : Educ CLR-4 : Unde CLR-5 : Unde	cate about Continuous erstand the characteri erstand the concept of elop expertise in time-	s time syst zation of t f Z-Trans domain al		nvolution integral	ems	Thinking (Bloom)	Expected Proficiency (%)	d Attainment (%)	Enrineering Knowledge	Problem Analysis	Jesign & Development	Analysis, Design, Research	Modern Tool Usage	Society & Culture	ment & Sustainability		al & Team Work	Communication	Project Mgt. & Finance	life Long Leaming	Professional Achievement .: Proiect Management	ques 3: Analyze & Research
Course Learni	ng Outcomes (CLO):	At the e	end of this course, learners will be able	to:		Level of	Expecte	Expected /	Encinee	Problem	Design {	Analysis	Modern	Society	Environment	Ethics	Individual &	Commu	Project I	Life Lon	PSO-1: PSO - 2	Techniques PSO – 3: An
			fications of Signals and Systems			1	85	70	H		-	-		-	-	-	-	-	-	-	-	
CLO-2 : Anal	yze Periodic and Ape	riodic Con	tinuous time Signals using Fourier ser	ies and Fourier Transform		2	85	70	H	M	-	-	М	-	-	-	-	-	-	-	-	
			nuous time system through Laplace tra			2	85	70	E			-	М	-	-	-	-	-	-	-	-	
			ete time signals and system through D	TFT, Convolution sum		2	85	70	H		-	-	М	-	-	-	-	-	-	-	-	
			ete time system using Z transform			2	85	70	H		-	-	М	-	-	-	-	-	-	-	-	
CLO-6 : Pres	ent the mathematica	l techniqu	ues used for continuous-time signal a	nd discrete-time signal and system analys	is	3	85	70	H	M	-	-	-	-	-	-	-	-	-	L	L	

	iration iour)	Classification of Signals and Systems	Analysis of Continuous Time Signals	Analysis of LTI CT System	Analysis of DT Signals and Systems	Analysis of LTI DT System using Z-Transform
,	ioury	12	12	12	12	12
	SLO-1	Introduction to signals and systems	Introduction to Fourier series	System modeling	Representation of sequences	Z transform – introduction
S-1	SLO-2	Requirements of signal and system analysis in communication	Representation of Continuous time Periodic signals	Description of differential equations	Discrete frequency spectrum and range	Region of convergence of finite duration sequences
S-2	SLO-1	Continuous time signals (CT signals)	Fourier series: Trigonometric representation	Solution of Differential equation	Discrete Time Fourier Transform (DTFT) – Existence	Properties of ROC
5-2	SLO-2	Discrete time signals (DT signals)	Fourier series: Trigonometric representation	Differential equation: Zero initial conditions	DTFT of standard signals	Properties of ROC
S-3	SLO-1	Representation of signals: Step, Ramp, Pulse, Impulse	Fourier series: Cosine representation	Differential equation: Zero state response	Properties of DTFT	Properties of Z transform
3-3	SLO-2	Representation of signals: Sinusoidal, Exponential	Fourier series: Cosine representation	Differential equation: Zero Input response	Properties of DTFT	Properties of Z transform
S-4	SLO-1	Basic operation on the signals	Symmetry conditions	Total Response	Inverse DTFT	Unilateral z transforms
3-4	SLO-2	Problems on signal operations	Properties of Continuous time Fourier series	Step response	Practice on IDTFT	Properties of z transform
S-5	SLO-1	Classification of CT and DT signals: Periodic & Aperiodic signals.	Practice problems on Fourier series	Impulse response	Impulse response of a system with DTFT	Bilateral Z transforms

	SLO-2	Classification of CT and DT signals: Deterministic & Random signals.	Practice problems on Fourier series	Frequency response	Frequency response of a system with DTFT	Properties of z transform
S-6	SLO-1	Energy signal	Gibb's Phenomenon	Convolution integral	Practice problems	Relation between DTFT and Z transform
3-0	SLO-2	Power signal	Parseval's relation for power signals	Properties of convolution	Practice problems	Practice problems
S-7	SLO-1	Even & Odd signals	Power density spectrum,	Practice Problems	Solution of linear constant coefficient difference equations	condition for causality in Z domain
0-1	SLO-2	Even & Odd signals	Frequency spectrum.	Practice Problems	Initial conditions	condition for stability in Z domain
S-8	SLO-1	CT systems and DT systems	Fourier transform: Introduction	Signal and system analysis with Laplace transform	Solution of difference equations	Inverse Z transform
0-0	SLO-2	Classification of systems: Static & Dynamic	Representation of Continuous time signals	Convergence of Laplace Transform	Zero input response	Power series expansion
S-9	SLO-1		Properties of Continuous time Fourier transform	Properties of Laplace transform	Solution of difference equations with Zero state response	Inverse Z transform with Partial fraction
9-9	SLO-2	Linear & Nonlinear system	Properties of Continuous time Fourier transform	Properties of Laplace transform	Total response	Inverse Z transform with Partial fraction
S-10	SLO-1	Time-variant & Time-invariant system	Parseval's relation for energy signals	Inverse Laplace transform	Evaluation of Impulse response	Residue method
3-10	SLO-2	Time-invariant system	Energy density spectrum	Problems	Evaluation of Step response	Convolution method
S-11	SLO-1	Causal system	Analysis of LTI system using Fourier Transform	Analysis and characterization of LTI system using Laplace transform	Convolution Properties	Analysis and characterization of DT system using Z-transform
3-11	SLO-2	Noncausal system	Analysis of LTI system using Fourier Transform	Analysis and characterization of LTI system using Laplace transform	Convolution Sum	Analysis and characterization of DT system using Z-transform
S-12	SLO-1	Stable & Unstable,LTI System	Practice problems on Fourier Transform	Practice problems on Laplace transform	Circular convolution	Practice problems on LTI-DT systems in Z transform
3-12	SLO-2	Unstable, LTI System	Practice problems on Fourier Transform	Practice problems on Laplace transform	Frequency response	Practice problems on LTI-DT systems in Z transform

	1.	Alan V Oppenheim, Ronald W. Schafer Signals & Systems, 2 nd ed., Pearson Education, 2015	5.	John G. Proakis, Manolakis, Digital Sigr
Learning	2.	P.Ramakrishna Rao, Shankar Prakriya, Signals & Systems, 2 nd ed., McGraw Hill Education, 2015		Pearson Education, 2007.
Resources	З.	Simon Haykin, Barry Van Veen, Signals and Systems, 2 nd ed., John Wiley & Sons Inc., 2007	6.	Software: Matlab Student Version Relea
	4.	Lathi B.P. Linear Systems & Signals, 2 nd ed., Oxford Press, 2009		toolboxes may be purchased through th

John G. Proakis, Manolakis, Digital Signal Processing, Principles, Algorithms and Applications, 4th ed., Poarson Education, 2007

Software: Matlab Student Version Release 2011a, Mathworks, Inc. The Matlab Student Version and toolboxes may be purchased through the Mathworks website at http://www.mathworks.com/

Learning As	sessment										
	Bloom's			Einal Examination	n (50% weightage)						
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#		r (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	10	0 %	10	0 %	100	0 %	10	0 %	10	0 %

Course Designers		
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2. Mr. Harihara	sudhan - Johnson	Controls	, Pune, <u>h</u>	nariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT,	Chennai,	venka	at@niot.i	res.in					2. I	Dr. D.	Mala	thi, S	RMIS	Т			
Course Code	18ECC105T	Cour: Nam		ELECTROMAGNETICS AND 1	TRANSMISSION LINES	Course Category		С				Proi	fessio	nal C	ore					L 3	T P 0 0	
Pre-requisite Courses		18PYB1		Co-requisite Courses	Nil		gress ourse								8EC							
Course Offerir	ng Department		Electro	onics and Communication Engineering	Data Book / Codes/Standards						С	Clark's	s Tabl	e, IS	: 456	-2000)					
Course Learni	ing Rationale (CL	. R): The	e purpose	e of learning this course is to:		Le	arnir	ng					Prog	ram L	earn	ing C	Outcoi	nes (I	PLO)			
				and insights of Electric field		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 1	4 1
CLR-2 : equa CLR-3 : Inter CLR-4 : Acqu CLR-5 : Acqu	ations. rpret the wave pro uire the fundamen uire the knowledge uire knowledge on	pagation Ital knowle e on trans	in guideo edge on mission al conce	and insights of Magnetic field and Empha d waveguide. Transmission Line Theory. line parameter calculation and impedand pts and analysis techniques to find solut Transmission line Theory.	ce matching concepts.	f Thinking (Bloom)	Expected Proficiency (%)	ed Attainment (%)	Engineering Knowledge	Problem Analysis	& Development	Analysis, Design, Research	Aodern Tool Usage	& Culture	Environment & Sustainability		lal & Team Work	Communication	^o roject Mgt. & Finance	ife Long Leaming	 SO-1: Professional Achievement SO - 2: Project Management 	jues , , , , , , , , , , , , , , , , , , ,
	•			of this course, learners will be able to:		Level of		Expected /			Design	Analysi	Modern	Society &	Environ	Ethics	Individual	Commu	Project	Life Lon	PSO-1 PSO-3	- E
				olve problems related to electric field.		2	80	70	М	Н												
				netic field and Maxwell's equations in the		2	80	70	Н	М												
				wave propagation and its mode of propa		1	80	70	Н	М												
				line theory applicable to low frequency tr		1	80	70	M	Н												
				mpedance matching through analytical a		2	80	70	М	Η												
CLO-6 : Und	ersiana now elect sfor electromagne	romagnet tic energy	IC WAVES	s are generated using Maxwell's equation to point to another with minimum losses	is and now Transmission lines are used	2 2	80	70	М	Н										Н		

	iration	Electrostatics	Magnetostatics and Maxwells Equations	Electromagnetic Waves and Waveguides	Transmission Line Theory	Transmission Line Calculator and Impedance Matching
,	hour)	9	9	9	9	9
S-1	SLO-1	Introduction	Energy density in electrostatic field Introduction		Transmission line parameters	Introduction
5-1	SLO-2	Rectangular co-ordinate	Problem discussion.	Waves in general	Transmission line parameters	Smith chart Introduction
S-2	SLO-1	Cylindrical & Spherical Co-ordinate	Biot savart law-Magnetic field intensity due to Infinite line charge	Plane wave in lossless dielectric		Reflection coefficient, Standing wave ratio Input impedance calculation in smith chart
0-2	SLO-2	Review of vector calculus	H- due finite and semi finite line charge	Plane wave in free space	Explanation	Practice problems.
S-3	SLO-1	Coulomb's Law and field intensity	Ampere's circuital law& application: Infinite line current	Plane wave in good conductor	Transmission line equation derivation	Single stub matching Introduction
3-3	SLO-2	Problem based on coulomb's law	Infinite Sheet current	Problems based on plane waves in lossless, free space and good conductor	Problem discussion.	Procedure for single stub matching
S-4	510-1	Electric field due to continuous charge distributionConcept	Infinitely long coaxial Transmission line	Rectangular waveguide	Transmission line characteristics: lossless line	Problems solving in smith chart
3-4	SLO-2	Derivation of E due Infinite Line charge	Problem based on ACL.	Rectangular waveguide-Problems	Distortionless line.	Problems solving in smith chart

S-5	SLO-1	Electric field due to sheet charge	Magnetic flux density	Transverse Electric (TE) mode	Input impedance derivation	Impedance matching using Quarter wave transformer
3-5	SLO-2	Problem based on sheet charge	Problem based on magnetic field and flux.	Transverse Electric (TE) mode-problems	Problems for input impedance calculation.	Problems.
S-6	SLO-1	Electric field due to volume charge	Maxwell's equation for static field	Transverse Electric (TE) mode	Standing wave ratio	Single stub tuner
3-0	SLO-2	Electric flux density	Faraday's law	Transverse Electric (TE) mode-Problems	Calculation of standing wave ratio.	Problem discussion
0.7	SLO-1	Gauss law application-point charge	Transformer EMF	Wave propagation in guide	Reflection coefficient	Slotted Line (Impedance Measurement)
S-7	SLO-2	Electric flux due infinite line charge	Motional EMF	Problem discussion	Problem discussion.	Problem discussion
S-8	SLO-1	Electric flux due sheet charge	Displacement current.	Power Transmission	Shorted line, open circuited line	Transmission Lines as circuit Elements
5-8	SLO-2	Electric flux due coaxial cable	Maxwell's equation in time varying field	Calculation of Pavg and Ptotal	Matched line	Problem discussion
S-9	SLO-1	Relation between E&V	Time varying potential concepts	Power attenuation	Power calculations	Additional smith chart problem solving.
2-9	SLO-2	Electric dipole and flux lines	Time varying potential derivation.	Calculation of αTE and αTE	Problem discussion.	Additional smith chart problem solving.

 Matthew N. O. Sadiku., S. V. Kulkarni, Elements of Electromagnetics, 6th ed., Oxford University Press, 2015
 G. S. N. Raju, Electromagnetic Field Theory and Transmission Lines, Pearson Education, 2006
 Nannapaneni Narayana Rao, Principles of Engineering Electromagnetics, 6th ed., Pearson Education, 2016 William H. Hayt, Jr., John A.Buck., Engineering Electromagnetics, 8th ed., Tata McGraw-Hill 2012 John D.Ryder, Networks, Lines and Fields, PHI, 2009 4. 5. Learning Resources

Learning Asses	sment										
	Bloom's			Einal Examinatio	n (50% weightage)						
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		ii (50 % weightage)
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40 %		30 %		30 %		30 %		30%	
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %		40 %		40 %	-	40%	
Level 2	Analyze	40 /0	-	40 70	-	40 /0	-	40 /0	-	4070	-
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%	
Level 5	Create	20 %	-	30 %	-	30 %	30 % - 30 %		-	30%	-
	Total 100 % 100 % 100 % 100 %) %	10	0 %	

Course Designers		
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Course Code	1	I8ECC201J	Course Name	ANALOG EL	ECTRONIC CIRCUITS		ourse egory	,	С				Prot	fessio	nal C	ore					L 3	T 0	P 2	C 4
Pre-requ Cours		1	18ECC102J	Co-requisite Courses	18ECC202J			gress ourse								N	ïl							
Course Of	ffering	Department	Electronic	s and Communication Engineeri	ng Data Book / Codes/Standards		Nil																	
	-	Rationale (CLR)		se of learning this course is to:			L	earni	5					Prog	ram L	earn	ing O	utcon						
				of BJT amplifier circuits for a give			1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-3 : CLR-4 : CLR-5 :	Unders to deter Unders Unders current	tand the effects of rmine the frequer tand the operation tand how matcher sources.	of negative fee <u>ncy of oscillatio</u> n and design o ed transistor ch	on of various types of power amplifie	nalyze the different RC and LC oscillator circ or circuits. lesign and to be able to design BJT and MOS		of Thinking (Bloom)	Expected Proficiency (%)	d Attainment (%)	Engineering Knowledge	Problem Analysis	& Development	Analysis, Design, Research	Modern Tool Usage	& Culture	ment & Sustainability		ndividual & Team Work	Communication	Mgt. & Finance	ife Long Leaming	>SO-1: Professional Achievement	2: Project Management tues	3: Analyze & Research
Course Le	earning	Outcomes (CLC): At the end	d of this course, learners will be a	ble to:		Level of	Expecte	Expected	Enginee	Problem	Design	Analysis	Modern	Society a	Environment &	Ethics	Individu	Commu	Project Mgt.	Life Lon	PS0-1:	PSO – 2: Pr Techniques	PSO - 0
					ons, and to Analyze the frequency response c mine the bandwidth of the circuit.	f	2,3	80	70	L	М	Н	-	-	-	-	-	-	-	-	-	-	-	-
CLO-2 :	amplifie	er circuits, taking	into account v	arious circuit capacitors, to deter	ations, and to Analyze the frequency respons mine the bandwidth of the circuit.		2,3	80	70	L	М	Н	-	-	·	-	-	-	-	-	-	-	-	-
	circuits	to meet certain s	pecifications.		uits and oscillator circuits to analyze and des	•	2,3	80	70	L	М	Н	-	-	-	-	-	-	-	-	-	-	-	-
CLO-4 :	type of	power amplifier	•		maximum possible conversion efficiency of ea		2,3	80	70	L	М	Н	-	-	1	1	-	-	-	-	-	-	-	-
					C amplifiers, namely current mirrors and sour		2,3	80	70	L	М	Н	-	-	-	-	-	-	-	-	-	-	-	-
				circuits using discrete componen laboratory with theoretical analy	ts, and take measurement of various analog sis.	circuits	3	90	80	-	-	Н	-	М	-	-	L	М	-	-	М	Н	L	-

Durati	ion (hour)	BJT Amplifiers	FET Amplifiers	Feedback amplifies & Oscillators	Oscillators & Power Amplifiers	IC Biasing & Amplifiers with Active Load
Durau	ion (hour)	15	15	15	15	15
S-1	SLO-1	Overview of DC analysis of BJT circuits		Basic feedback concepts, general feedback structure	('nystal ()scillators	BJT current sources: Cascode current source, Widlar current source
3-1	SLO-2	Overview of BJT models	Problem solving	Properties of negative feedback	Problem solving	Multi-transistor current source Problem solving
S-2	SLO-1		Graphical analysis, load lines, and small- signal models	Feedback Topologies: Voltage-Series & Current-Series feedback connections	Negative-resistance oscillator	FET current sources: 2-transistor MOSFET current source
5-2	SLO-2	Problem solving	Problem solving	Problem solving	Problem solving	Problem solving
S-3	SI ()_1	,	AC analysis of Common-Source MOSFET amplifier configuration	Feedback Topologies: Voltage-Shunt & Current-Shunt feedback connections	Power Amplifiers: Definitions and amplifier types	FET current sources: Cascode current mirror and Wilson current mirror
0.0	SLO-2	Problem solving	Problem solving	Problem solving	Q point placement	Problem solving
S 4-5			Lab 4: Design & analyze differential amplifier with resistive load	Lab 7: Design and analyze RC oscillators	Lab 10: BJT & FET Current Sources	Lab 13: Design and analyze differential amplifier with active load

S-6		AC analysis of Common-Base BJT amplifier configuration using hybrid-π model	AC analysis of Common-Gate MOSFET amplifier configuration	Practical Feedback Amplifier Circuits	Maximum dissipation hyperbola	Analysis of CE BJT amplifier circuit with active load
	SLO-2	Problem solving	Problem solving	Problem solving	Heat sink	Problem solving
S-7	SLO-1	AC analysis of Common-Collector BJT amplifier config. using hybrid-π model	AC analysis of Common-Drain MOSFET amplifier configuration	Oscillators: Principles of Oscillation	Class A amplifier	Analysis of CS FET amplifier circuit with active load
•	SLO-2	Problem solving	Problem solving	Types of Oscillators	Problem solving	Problem solving
S-8	SLO-1	Multi-stage amplifier configurations: CE - CE, CE - CC amplifiers	BiFET amplifier configuration	Audio Frequency Oscillators: RC Phase- Shift Oscillator	Class B and Class AB push-pull amplifiers	DC and small-signal analysis of basic BJT differential pairs
0.0	SLO-2	Problem solving	Problem solving	Problem solving	Problem solving	Problem solving
S 9-10		Lab 2: Design and analyze BJT amplifier configurations	Lab 5: Design and analyze negative feedback amplifier configurations	Lab 8: Design and analyze LC oscillators	Lab 11: Design and analyze BJT CE amplifier with active load	Lab 14: Model Practical Examination
S-11	SLO-1	Multi-stage amplifier configurations: CE - CB, and CC - CC amplifiers	Low Frequency response analysis of a basic FET CS amplifier	Audio Frequency Oscillators: Wein Bridge Oscillator	Class C amplifiers	DC and small-signal analysis of basic FET differential pairs
3-11	SLO-2	Problem solving	Problem Solving	Problem Solving	Problem solving	Problem solving
S-12	SLO-1	Low Frequency response analysis of a basic BJT CE amplifier	High Frequency response analysis of a basic FET CS amplifier	Radio Frequency Oscillators: Hartley Oscillator	Class D and Class E amplifiers	Analysis of BJT differential amplifier with active load
0-12	SLO-2	Problem Solving	Problem Solving	Problem solving	Amplifier distortions	Problem solving
S-13	SLO-1	High Frequency response analysis of a basic BJT CE amplifier	Design problems in MOSFET amplifier configurations	Radio Frequency Oscillators: Colpitts & Clapp Oscillators	IC Biasing & Amplifiers with Active Load: BJT current sources: 2- & 3-transistor current sources	Analysis of FET differential amplifier with active load
	SLO-2	Problem Solving	Operational voltage levels	Problem solving	Problem solving	Problem solving
S 14-15		Lab 3: Design and analyze multistage amplifier configurations	Lab 6: Design and analyze MOSFET amplifier configurations	Lab 9: Classes of power amplifier (efficiency calculation)	Lab 12: Design and analyze FET CS amplifier with active load	Lab 15: End Semester Practical Examination

 Learning
 1.
 David A. Bell, Electronic Devices and Circuits, 5th ed., Oxford University Press, 2015

 Learning
 2.
 Donald Neamen, Electronic Circuits: Analysis and Design, 3rd ed., McGraw-Hill Education, 2011

 Resources
 3.
 Muhammad Rashid, Microelectronic Circuits: Analysis & Design, 2rd ed., Cengage Learning, 2010

 4.
 Adel S. Sedra, Kenneth C. Smith, Microelectronic Circuits: Theory and Applications, OUP, 2014

 Robert L. Boylestad, Louis Nashelsky, Electronic Devices and Circuit Theory, 11th ed., Pearson Education, 2013

Albert P. Malvino, David J. Bates, Electronic Principles, 8th ed., Tata McGraw Hill, 2015

Learning Ass	sessment												
	Bloom's			Conti	nuous Learning Ass			Final Examination (50% weightage					
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
r. Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%		
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
Level 3	Evaluate Create	10%	10%	15%	15%	15% 15%		15%	15%	15%	15%		
	Total	100) %	10	0 %	10	0 %	10	0 %		-		

6.

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. Manikandan AVM, SRMIST

2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com

2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in

Course Code	18ECC202J	Course Name					ourse ategory	,	С				Proi	fessio	nal C	ore					L 1 3 (T F 0 2		-
Pre-requ Cours		18ECC102J		requisite ourses	18ECC201J			gress ourse								Ni	1							
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards							Nil																	
Course Le		L	earnir	ng					Progr	ram L	.earni	ng O	utcon	nes (F	PLO)									
CLR-1 :							1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 1	14 1	5
CLR-1 : Study the basic principles, configurations and practical limitations of op-amp CLR-2 : Understand the various linear and non-linear applications of op-amp CLR-3 : Understand the operation and analysis of op-amp oscillators, single chip oscillators and frequency generators CLR-3 : Understand the operation and analysis of op-amp oscillators, single chip oscillators and frequency generators CLR-4 : Identify the active filter types, filter response characteristics, filter parameters and IC voltage regulators. CLR-5 : Gain knowledge on data converter terminology, its performance parameters, and various circuit arrangements for A/D and D/A conversions. CLR-6 : Gain hands-on experience to put theoretical concepts learned in the course to practice. Course Learning Outcomes (CLO): At the end of this course, learners will be able to:						Level of Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment (%)	Encineering Knowledge		Design & Development	Analysis, Design, Research	Modem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	ő	PSO-1: Professional Achievement PSO - 2: Project Management	Techniques DSO - 3: Analyze & Deceamb	– J. Analyze &	
CLO-1 :	Infer the DC and AC c	haracteristics	of operational amplifie	rs and its effe	ct on output and their compensation tec	hniques	3	80	70	ŀ	M	Н	-	-	-	-	-	-	-	-	-	-		
CLO-2 :	2: Elucidate and design the linear and non-linear applications of an opamp and special application ICs					3	85	75	٨	M	Н	-	-	-	-	-	-	-	-	-	-			
CLO-3 :	O-3: Explain and compare the working of multivibrators using special application IC 555 and general purpose opamp					3	75	70	L	М	Н	-	-	-	-	-	-	-	-	-	-			
CLO-4 :						3	85	80	L	М	Н	-	-	-	-	-	-	-	-	-	-			
CLO-5 :						3	85	75	L	М	Н	-	-	-	-	-	-	-	-	-	М	- H	1	
	CLO-6 : Analyze and design electronic circuits and systems using linear ICs, and take measurement of various analog circuits to compare experimental results in the laboratory with theoretical analysis					ircuits to	3	85	75		Н	Н	-	М	-	-	-	М	-	-	-	Н	L -	

Duratio	on (hour)	15	15	15	15	15
S-1	SLO-1	Op-amp symbol, terminals, packages	Basic op-amp circuits: Inverting & Non- inverting voltage amplifiers	Waveform Generators: Sine-wave Generators - Design	Filters: Comparison between Passive and Active Networks	Digital to Analog Conversion: DAC Specifications
0-1	SLO-2	Op-amp-Specifications	Voltage follower	Implementation & Solving problems	Active Network Design	Solving problems
S-2	SLO-1	Block diagram Representation of op-amp	Summing, scaling & averaging amplifiers,	Square Wave generators- Design	Filter Approximations	Weighted Resistor DAC
3-2	SLO-2	Ideal op-amp & practical op-amp - Open Ioop & closed loop configurations	AC amplifiers	Implementation & Solving problems	Design of LPF & Solving problems	Solving problems
S-3	SLO-1	DC performance characteristics of op-amp	Linear Applications: Instrumentation Amplifiers	Triangle wave generators	Design of HPF & Solving problems	R-2R Ladder DAC
5-5	SLO-2	Solving Problems	Instrumentation Amplifiers, Solving Problems	Saw-tooth Wave generators.	Design of BPF& Solving problems	Solving problems
S 4-5	SLO-1 SLO-2	Lab-1:Basic op-amp circuits	Lab 4: Comparators	Lab 7: Waveform generators: using op- amp & 555 Timer	Lab 10: Design of LPF, HPF, BPF and Band Reject Filters	Lab 13: Flash Type ADC
S-6	SLO-1	AC performance characteristics of op-amp	V-to-I Converters	IC 555 Timer: Circuit schematic	Design of Band Reject Filters	Inverted R-2R Ladder DAC
3-0	SLO-2	Solving Problems	I-to-V converters	Operation and its applications	Solving problems	Monolithic DAC
S-7	SLO-1	Frequency response	Differentiators	IC 555 Timer: Monostable operation	State Variable Filters – All Pass Filters,	Analog to Digital conversion: ADC specifications

	SLO-2	Frequency response	Integrators	Applications & Solving problems	Solving problems	Solving problems
	SLO-1	Frequency compensation	Non-linear Applications: Precision Rectifiers	IC 555 Timer: Astable operation	Switched Capacitor Filters.	Ramp Type ADC
S-8	SLO-2	Frequency compensation	Wave Shaping Circuits (Clipper and Clampers)	Applications & Solving problems	Solving problems	Solving problems
S 9-10	SLO-1 SLO-2	Lab 2: Integrators and Differentiators	anip & 555 Timer		Lab 11: IC Voltage regulators	Lab 14: Simulation experiments using EDA tools
S-11	SLO-1	Basic op-amp internal schematic	Log and Antilog Amplifiers,	PLL: Operation of the Basic PLL Voltage Regulators: Basics of Voltage Regulator		Successive Approximation ADC
5-11	SLO-2	operations of blocks	Analog voltage multiplier circuit and its applications,	Closed loop analysis of PLI	Specifications and characteristic parameters	Solving problems
S-12	SLO-1	Basic op-amp internal schematic	Operational Trans-Conductance Amplifier (OTA)	Voltage Controlled Oscillator	Linear Voltage Regulators using Op-amp,	Dual Slope ADC
3-12	SLO-2	operations of blocks	Comparators : operation	Solving problems	IC Regulators (78xx, 79xx, LM 317, LM 337, 723),	Flash Type ADC,
S-13	SLO-1	Review of data sheet of an op-amp.	Comparators applications	PLL applications	Switching Regulators -operation	Solving problems on Flash Type ADC,
5-13	SLO-2	Solving Problems	Sample and Hold circuit.	Solving problems	Types	Monolithic ADC
S 14-15	SLO-1 SLO-2	Lab 3: Rectifiers	.ab 6: Waveform generators: using op- Imp & 555 Timer Lab 9: Design of LPF, HPF, BPF and Band Reject Filters		Lab 12: R-2R ladder DAC	Lab 15: Simulation experiments using EDA tools

Learning Resources	 Ramakant A. Gayakwad, Op-Amps and Linear Integrated Circuits, 4th ed., Prentice Hall, 2000 David A. Bell, Operational Amplifiers and Linear ICs, 3rd ed., OUP, 2013 Roy Choudhury, Shail Jain, Linear Integrated Circuits, 4th ed., New Age International Publishers, 2014 Robert F. Coughlin, Frederick F. Driscoll, Operational-Amplifiers and Linear Integrated Circuits, 6th ed., Prentice Hall, 2001 Sergio Franco, Design with operational amplifier and analog integrated circuits, McGraw Hill, 1997 	 LABORATORY MANUAL, Department of ECE, SRM University David A Bell, Laboratory Manual for Operational Amplifiers & Linear ICs, 2nd ed., D.A. Bell, 2001 David La Lond, Experiments in Principles of Electronic Devices and Circuits, Delmar Publishers, 1993 Muhammed H Rashid, Introduction to PSpice using OrCAD for circuits and electronics, 3rd ed., Pearson, 2004 L. K. Maheshwari, M. M. S. Anand, Laboratory Experiments and PSPICE Simulations in Analog Electronics, PHI, 2006 	
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Learning Assess	ment										
	Bloom's			Contir		Final Examination (50% weighta					
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – S	3 (15%)	CLA – 4	(10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Lever	Understand	2070	2070	1370	1570	1370	1370	1370	1370	1370	1370
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level Z	Analyze	2070	2070	2070	2070	2070	2070	2070	2070	2070	2070
Laval 2	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Level 3	Create	10%	10%	10%	10%	10%	13%	10%	10%	15%	10%
	Total	100) %	100) %	100 %		100 % 100 %			-

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. Manikandan AVM, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. M. Sangeetha, SRMIST

Course Code	18ECC203J		urse Mame M	licroprocessor, Mic	crocontroller	and Interfacing Techniques	Course Category	С	Professional Core	L 3	Т 0	P 2	C 4
Pre-requisite Courses		18EC	C103J	Co-requisite Courses		Nil	Progressive Courses	!	18ECE204J, 18ECE205J				
Course Offering	g Department		Electronics and Comn	nunication Enginee	ring	Data Book / Codes/Standards		Nil					

Course Offering Department	Electronics and Communication Engineering	Data Book / Codes/Standards
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Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	na					Progr	am L	earni	ina O)utco	nes (PLO)				
CLR-1 :	Understand basic architecture of Intel 8086 microprocessor and Intel 8051 Microcontroller	1	2	3	1	2	3	4	5	6	7	8	9	10	11		13	14	15
CLR-2 :	Familiarize the students with the programming and interfacing of microprocessors and microcontrollers with memory and peripheral chips																	sau	
CLR-3 :	Interface a microprocessor / microcontroller to external input/output devices and perform input/output device programming in assembly																ent	Techniqu	
CLR-4 :	Use the computer to write and assemble assembly language programs and also run them by downloading them to the target microprocessor	(Bloom)	(%)	Attainment (%)				rch			bility						Achievement		Research
CLR-5 :	R-5 : Understand the hardware and software interrupts and their applications, and as well the properties and interfacing of parallel and serial ports				wledge	(0	pment	, Resea	age	đ	Sustainability		Team Work		Finance	þ		Project Management	۰ð
CLR-6 :					on X Br	vnalysis	Develo	Design,	ool Usa	Culture	∞ ŏ		ంన	cation	~ŏ	Leaming	Professional	Project	Analyze
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of Thinking	Expected	Expected	Engineering Knowledge	Problem Analysis	Design & Development	Analysis, I	Modern Tool Usage	Society &	Environment	Ethics	Individual	Communication	Project Mgt.	Life Long	PSO-1: P	PSO – 2:	PSO – 3:
CLO-1 :	Recall and apply a basic concept of digital fundamentals to Microprocessor based personal computer system	1	80	70		Ħ			L										
CLO-2 :	Solve basic binary math operations using the microprocessor. / microcontroller	2	90	80	М											М			
CLO-3 :	Demonstrate programming preficiency using the various addressing modes and data trapefor instructions of the target					М	Н		Н										L
CLO-4 :	4: Distinguish and analyze the properties of Microprocessors & Microcontrollers.		80	70		М										Н			
CLO-5 :	D-5 : Train their practical knowledge through laboratory experiments.		90	80		М	М		Н					Н					Н
CLO-6 :	.0-6 : Design,program and interface external devices, memory chips and various peripheral chips with microprocessor / microcontroller		90	80			М		Н							Н	L		М

		Learning Unit / Module 1: Intel 8086 – Architecture, Signals and Features	Learning Unit / Module 2: Programming with Intel 8086	Learning Unit / Module 3: 8086 Interfacing with Memory and Programmable Devices	Learning Unit / Module 4: Intel 8051 – Architecture and Programming	Learning Unit / Module 5: Interfacing of 8051
Duratio	n (hour)	15	15	15	15	15
S-1		Introduction: History of computers, Block diagram of a microcomputer	Addressing modes of 8086	Semiconductor memory interfacing	Introduction: Differences between microprocessor and microcontroller	8051 parallel ports, and
3-1	SLO-2	Intel 80x86 evolutions		Dynamic RAM interfacing	Intel's family of 8-bit microcontrollers, and feature of 8051 microcontroller	its programming
S-2	SLO-1	Features of 8086 microprocessor	Instruction Set of 8086: Data Transfer Instructions	Programmable Peripheral Interface 8255	Architecture of 8051	8051 timers, and
3-2	SLO-2	Register organization of 8086	Example programs	Interfacing 8255 with 8086 and programming		its programming
S-3	SLO-1	Architecture of 8086	Data Conversion Instructions, Arithmetic Instructions	Interfacing ADC with 8086 and programming	Signal descriptions of 8051	8051 interrupts, and
3-3	SLO-2		Example programs	Interfacing DAC with 8086 and programming		its programming
S-4,5	SLO-1	Lab-1: (a) Learning to Program with	Lab-4: General Purpose Programming	Lab-7: Interfacing DAC / ADC with 8086	Lab-10: Programming timer / counter in	Lab-13: Simulation of 8051 using Keil

		8086 processor kit; Learning the hardware features of the 8086 processor kit	in 8086	/ 8051	8086 / 8051	Software
S-6	SLO-1	Instruction queue and pipelining	Logical instructions and Processor control instructions	Stepper Motor interfacing	Register set of 8051	8051 serial port, and
	SLO-2	Segmentation of memory used with 8086	Example programs		Operational features of 8051	its programming
S-7		Methods of generating physical address in 8086	String instructions	Programmable Interval Timer 8254	Memory and I/O addressing by 8051	Interfacing program memory with 8086
3-7	SLO-2	Pin signals of 8086: Common signals	Example programs	Interfacing 8254 with 8086 and programming	Interrupts and Stack of 8051	Interfacing data memory with 8086
S-8	SLO-1			Programmable Interrupt Controller 8259	Addressing modes of 8051	Interfacing input devices: push-button / matrix keypad
3-0	SLO-2	Maximum mode signals	Example programs	Interfacing 8259 with 8086 and programming		Example programs
S-9,10		Lab-2: General Purpose Programing in 8086	Lab-5: Simulation of 8086 using MASM Software / 8086 Emulator	Lab-8: Interfacing DC motor / stepper motor / servo motor with 8086 / 8051	Lab-11: Programming interrupts in 8086 / 8051	Lab-14: Model Practical Exam
S-11	SLO-1	Minimum mode 8086 system, and	Assembly Language Programming of 8086	Programmable Keyboard / Display Controller 8279	8051 Instruction Set: Arithmetic and Logical Instructions	Interfacing display devices: LED / 7- segment / LCD displays
5-11	SLO-2	Timings	Assembly Language Programming of 8086	Interfacing 8279 with 8086 and programming	Example Programs	Example programs
S-12	SLO-1	Maximum mode 8086 system, and	Stack structure, and	Programmable Communication Interface 8251 USART	Data Transfer Instructions	Interfacing DAC
0-12	SLO-2	Timings	related programming	Interfacing 8251 with 8086 and programming	Example Programs	Interfacing ADC
S-13	SLO-1	Intel 8088 Microprocessor: Pins signals and Architecture	Interrupt structure, and	DMA Controller 8257	Boolean Variable Instructions and Branch Instructions	Interfacing DC motor / stepper motor / servo motor
3-13	SLO-2	Differences between 8086 & 8088 microprocessors	related programming	Interfacing 8257 with 8086 and programming	Example Programs	Example programs
S-14,15		Lab-3: General Purpose Programing in 8086	Lab-6: Interfacing 8255 with 8086 / 8051	Lab-9: General Purpose Programming in 8051	Lab-10: Programming serial communication in 8086 / 8051	Lab-15: End-Semester Exam

	1.	K. M. Bhurchandi and A. K. Ray, "Advanced Microprocessors and Peripherals-with ARM and	4.	Kenneth.J.Ayala, "8051 Microcontroller Architecture, Programming and Applications", 3rd edition, Thomson,	٦
		an Introduction to Microcontrollers and Interfacing ", Tata McGraw Hill, 3rd edition 2015		2007	
Learning	2.	Muhammad Ali Mazidi and Janice GillispieMazidi, "The 8051 - Microcontroller and	5.	Subrataghoshal " 8051 Microcontroller Internals Instructions ,Programming And Interfacing",2nd edition	
Resources		Embedded systems", 7th Edition, Pearson Education, 2011.		Pearson 2010	
	3.	Doughlas.V.Hall, "Microprocessor and Interfacing : Programming and Hardware", 3rd	6.	Yu-cheng Liu, Glenn A.Gibson, "Microcomputer systems: The 8086/8088 family-Architecture, programming and	1
		edition McGraw Hill 2015		design" 2nd edition. Prentice Hall of India 2007	

Learning Assess	sment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examinatio	n (E00/ woightogo)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Lovel 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	150/
Level 1 Ur	Understand	20%	20%	13%	13%	13%	15%	15%	15%	15%	10%
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	200/
LEVEIZ	Analyze	2070	2070	2070	2070	2070	2070	2078	2070	2070	15% 20% 15%
Level 3	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Level 5	Create	10%	10%	1376	1376	1376	1376	1376	1376	1376	1370
	Total	100) %	100) %	100	0 %	100) %	10	00 %

Course Designers									
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts							
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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in								

Course Code	18ECC204J	Course Name		DIGIT	AL SIGNAL PROCESSING			urse egory	, C				P	rofess	ional	Core					L -	Г Р) 2	C 4
Pre-requis Courses		18ECC104T		Co-requisite Courses	Ν	il	Progressive 18ECE243J, 18ECE244J, 18ECE245T																
Course Offer	ring Department	Electro	onics and Comm	unication Enginee	ring Data Book / C	Codes/Standards									Nil								
	ning Rationale (C						Le	earnir	<u> </u>					Progi									
	nderstand the oper				nals.		1	2	3	1	2	3	4	5	6	7	8	9	10	11		13 1	4 15
CLR-2: Pe	ealize a digital filter erform efficient com	putation of DF	T using radix 2 F	FT									-			λ,						Professional Achievement : Project Management	сh
	esign digital FIR filt						Ê	()					arch			abili						thie demo	Research
					rsion of analog filter to digi	ital filter	(Bloom)	y (%	Attainment (%)	app		ent	Design, Research			Sustainability		Work		g		al Ac	Re
					QMF, sub band coding.		g (B	enc	mer	ahm	s	Development	Å.	age	æ	Sust		۲ ۲		Finance	Ð	t Ma	e &
CLR-6 : Ut	ilize the techniques	s for digital con	versions, filter de	signs and multi ra	te signal processing to solv	ve real time problems	Thinking	ofici	tain	k l	alysi	svelo	sigr	١Us	ltur	∞ŏ		Team	ion	∞ŏ	Leaming	fess ojec	es Analyze 8
	ning Outcomes (0						Level of	Expected Proficiency (%)	Expected	Endineering Knowledge		Design & De	Analysis, De	Modern Tool Usage	Society & Culture	Environment	Ethics	Individual &	Communication	Project Mgt.	ö	PSO–1: Professional Achieverr PSO – 2: Project Management	Techniques PSO – 3: Ar
					rrors that arise due to quar	ntization.	2	90	70	H		-	L	-	-	-	-	-	-	-	-	LI	. М
	nderstand the conc			putation by using	FFT algorithm.		2	95	75	H		-	L	-	-	-	-	-	-	-	-	L	. M
	esign FIR filters usi						2	85	70	-	М	Н	Н	-	-	-	-	-	-	-	-	L	. Н
	esign IIR filters usir						2	85	80	-	М	Н	Н	-	-	-	-	-	-	-	-	L	. Н
	nderstand the basic						2	90	75	H		-	М	-	-	-	-	-	-	-	-	L	. M
CLO-6 : Ap	oply the concepts o	f digital filter de	esigns and multi r	rate signal proces	sing for real time signals		2	80	70	E	М	-	Н	-	-	-	-	-	-	-	-	ΜI	1 H
					Learning Unit / FIR Filte		ə 3:			Learn	ing Ur IIR F	nit / M Filters		4:						it / Mo al Pro			

		Learning Unit / Module 1: Signals and Waveforms	Learning Unit / Module 2: Frequency Transformations	Learning Unit / Module 3: FIR Filters	Learning Unit / Module 4: IIR Filters	Learning Unit / Module 5: Multirate signal Processing
Duratio	n (hour)	15	15	15	15	15
	SLO-1	Basic Elements of DSP	Realization of digital filters Direct form of realization	Design of Linear Phase FIR filters General consideration	Design of digital IIR filters Comparison of FIR and IIR filters	Introduction to Multirate signal processing
S-1	SLO-2	Advantages and applications of DSP	Cascade form of realization	Causality and its implication Characteristics of practical frequency selective filters	Analog IIR filter design	Decimation
	SLO-1 Continuous Time vs Discrete time sig		Parallel form of realization	Frequency response of symmetric FIR filter	Properties of Butterworth filters	Interpolation
S-2	·		Introduction to DET		Properties of chebyshev filters Comparison of Butterworth and chebyshev filters	Spectrum of interpolated signal
S-3	SLO-1	Concepts of frequency in analog signals	Computation of DFT	Frequency response of symmetric FIR filter	Analog IIR filter design	Sampling rate conversion by a rational factor I/D
3-3	SLO-2	Continuous and discrete time sinusoidal signals	Properties of DFT Periodicity, linearity and symmetry properties	N is even	Design of low pass Butterworth filter	Anti-aliasing and anti-imaging filters
S-4	SLO-1	Lab 1 Constantion of basis simple	Lab 7: Linear convolution	Lab 13: Design of digital FIR Low Pass and High Pass filter using rectangular	Lab 19: Design of analog Butterworth	I ab 25: Internalation
3-4	Lab 1 :Generation of basic signals			window	filter	Lab 25: Interpolation
0.5	SLO-1			Lab14: Design of digital FIR Band Pass	Lab 20: Design of analog Chebyshev	Lab 26: Effect of interpolation in
S-5	SLO-2	Lab 2: Unit step, ramp and impulse	Lab 8: Circular convolution	and Band Stop filter using rectangular window	filter	frequency domain

S-6	SLO-1	Sampling of analog signals Sampling theorem	Circular convolution	Frequency response of antisymmetric FIR filter	Analog IIR filter design	Polyphase structure of decimator Polyphase decimation using z transform
3-0	SLO-2	Aliasing Quantization of continuous amplitude signals	Matrix method and concentric circle method	N is odd and N is even	Design of low pass Chebyshev filter	Polyphase structure of interpolator Polyphase interpolation using z transform
	SLO-1	Analog to digital conversion Sample and hold,	Efficient Computation of the DFT	Design of FIR filters Fourier series method	Design of digital filters Impulse invariance method	Advantages of multirate DSP
S-7	SLO-2	Quantization and coding	Divide and Conquer Approach to Computation of the DFT Using FFT	Need for filter design using window Comparison of various windowing techniques	Design of digital filters Bilinear transformation	Applications of multirate DSP
S-8		Oversampling A/D converters	N Point DFT Decimation-in-Time FFT Radix-2 FFT Algorithm	Filter Design using windowing technique	Design of digital filters Impulse invariance method	Practical Applications of multirate DSP
3-0	SLO-2	Digital to analog conversion Sample and hold	N Point DFT Decimation-in-Frequency FFT	Rectangular window	Design of digital filters Bilinear transformation	interfacing of digital systems with different sampling rates
S-9	SLO-1 SLO-2	Lab 3: Generation of waveforms	Lab9: Autocorrelation and cross correlation	Lab 15: Design of digital FIR Low Pass and High Pass filter using Hanning and Hamming window	Lab 21: Design of digital Butterworth filter using impulse invariance method	Lab 27: Decimation
S-10	SLO-1 SLO-2	Lab 4: Continuous and discrete time	Lab10: Spectrum analysis using DFT	Lab 16: Design of digital FIR Band Pass and Band Stop filter using Hanning and Hamming window	Lab 22: Design of digital Butterworth filter using bilinear transformation	Lab 28: Effect of decimation in frequency domain
S-11	SLO-1	Oversampling D/A converters	Radix-2 FFT Algorithm Implementation of FFT Using DIT	Filter Design using windowing technique Hanning window	Design of digital Chebyshev filters	Practical Applications of multirate DSP Sub band coding of speech signals
3-11	SLO-2	Quantization noise	Implementation of FFT Using DIF	Filter Design using windowing technique Hamming window	Impulse invariance method	Filter banks Analysis filter bank
S-12	SLO-1	Errors due to truncation	IDFT	Filter Design using windowing technique	Design of digital Chebyshev filters	Synthesis filter bank
3-12	SLO-2	Probability of error	Using DIT FFT	Blackmann window	Bilinear transformation	Subband coding filterbank
S-13	SLO-1	Errors due to rounding	IDFT	Design of FIR filters	Frequency transformation in analog domain	Quadrature Mirror Filter
0-10	SLO-2	Probability of error	Using DIF FFT	Frequency sampling method	Frequency transformation in digital domain	Alias free filter bank
S-14	SLO-1 SLO-2	Lab 5: Study of sampling theorem	Lab 11: Efficient computation of DFT using FFT	Lab 17: Design of digital FIR Low Pass, High Pass, Band pass and band stop filter using Blackmann window	Lab 23: Design of digital Chebyshev filter using impulse invariance method	Lab 29: Design of anti-aliasing filter
S-15	SLO-1 SLO-2	Lab 6: Aliasing effects	Lab12: Computation of IDFT	Lab 18: Design of digital FIR filter using frequency sampling method	Lab 24: Design of digital Chebyshev filter using bilinear transformation	Lab 30: Design of anti-imaging filter

Learning Resources		John G. Proakis, Dimitris G. Manolakis, "Digital Signal Processing, Principles, Algorithms and Applications", Pearson Education, 4th edition, 2014 Alan V. Oppenheim, Ronald W. Schafer, "Discrete-Time Signal Processing", Pearson Education, 1st edition, 2015	3. 4.	Sanjit Mitra, "Digital Signal Processing –A Computer Based Approach", McGraw Hill, India, 4th Edition, 2013. Fredric J. Harris, "Multirate Signal Processing for Communication Systems", 1st edition, Pearson Education, 2007	
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	Bloom's			Conti	nuous Learning Asse	essment (50% weig	htage)			Final Examination (50% waight		
	Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	4 (10%)#	Final Examination (50% weightage)		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100) %	10	0 %	10	0 %	100	0 %	10	0 %	

Course Designers									
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts							
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	Dr. M.S. Vasanthi,,SRMIST							
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in								

Course	195002051	Course		Course	0	Professional Core	L	Т	Ρ	С
Code	10ECC205J	Name	ANALOG AND DIGITAL COMMUNICATION	Category	U	Professional Core	3	0	2	4

Pre-requisite Courses	18MAB2031		Co-requisite Courses	Nil	Progressive Courses	18ECC301T, 18ECC302J, 18ECE221T & 18ECE223T
Course Offering Depa	partment	ECE		Data Book / Codes/Standards	Nil	

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng					Prog	ram L	earni	ing O	utcor	nes (l	PLO)				
CLR-1 :	Introduce and Understand the need for modulation, various Amplitude modulators/demodulators, frequency modulators and demodulators	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Comprehend the radio transmitters and receivers using the modulators and demodulators and to analyze the noise performance	Ê	_								,						Achievement	at	÷
CLR-3 :	To introduce basics of Digital modulation and detection techniques	(Bloom)	(%)	(%)				arch			Sustainability						hiev	em	Research
CLR-4 :	To analyze the pass band data transmission techniques in terms of probability of error	B	Proficiency	Attainment	dge		ent	see			aina		Work		9		IAc	luag	
CLR-5 :	To introduce basics of spread spectrum techniques and information theory concepts	Thinking	icie	m	Me	s	Development	, Re	Usage	Ð	ust		۲ ۲		Finance	þ	onal	E Ma	e Se
CLR-6 :	Gain hands-on experience to put theoretical concepts learned in the course to practice.	Ľ.	l d	vtta	Å Å	Analysis	velo	Design,	Usi	Culture	~ŏ		Team	ou	α Έ	earning	essi	ject	alyze
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	evel of Th	Expected I	Expected /	Engineering Knowledge	² roblem An	Design & De	Analysis, De	Modern Tool	Society & C	Environment	Ethics	ndivid ual &	Communication	^o roject Mgt.	-ife Long Le	PSO-1: Profe	PSO – 2: ⊢r Fechniques	⁵ SO – 3: An
CLO-1 :	Understand the concepts of analog modulation and demodulation techniques	2	80	70	M	-	-	-	-	-	-	-	-	Ĥ	-	-	H	-	-
CLO-2 :	Learn the function of radio transmitters and receivers and familiarize with noise performance of various receivers	2	85	75	-	М	Н	-	-	-	-	-	-	-	-	-	Н	-	-
CLO-3 :	Understand various digital modulation schemes and matched filter receiver	2	75	70	М	-	-	-	-	-	-	-	-	-	-	-	-	М	Н
CLO-4 :	Understand and analyze various digital pass band data transmission schemes	2	85	80	-	-	-	М	-	-	-	-	-	-	-	-	-	М	-
CLO-5 :	Understanding data transmission using spread spectrum and error coding techniques	2	85	75	-	Н	-	-	-	-	-	-	-	-	-	-	М	-	Н
CLO-6 :	Analyze the operation of analog and digital communication systems and take measurement of various communication systems to compare experimental results in the laboratory with theoretical analysis	2	85	75	-	-	Н	-	Н	-	-	-	Н	-	-	М	-	М	Η

		Analog Modulation	Radio Transmitters and Receivers	Digital Modulation System and Baseband Detection	Passband Data Transmission	Spread Spectrum Techniques and Information theory Concepts
	ration nour)	15	15	15	15	15
S-1	SLO-1	Modulation, Need for Modulation,	AM transmitter : Low Level,	Pulse modulation systems, Overview of PAM,PWM,PPM	Overview of ASK, FSK, PSK	Spread spectrum Communications, Frequency Hopping Spread Spectrum (FHSS)
3-1	SLO-2	Amplitude Modulation, Types of Amplitude Modulation	AM transmitter : High Level Transmitter	Pulse modulation systems, Overview of PAM,PWM,PPM	Overview of ASK, FSK, PSK	Spread spectrum Communications, Frequency Hopping Spread Spectrum (FHSS)
S-2	SLO-1	Double sideband Full carrier	FM transmitter: Direct Method	Pulse modulation systems, Sampling and quantization	Generation, Signal Space Diagram and detection of FSK	Direct Sequence Spread Spectrum (DSSS)
5-2	SLO-2	Double sideband Full carrier	FM transmitter: Direct Method	Pulse modulation systems, Sampling and quantization	Generation, Signal Space Diagram and detection of FSK	Direct Sequence Spread Spectrum (DSSS)
	SLO-1	Double sideband Suppressed carrier	FM transmitter: Indirect Method	PCM systems	Probability of Error for FSK	Direct Sequence Spread Spectrum (DSSS)
S-3	SLO-2	Single sideband Suppressed carrier, VSB	FM transmitter: Indirect Method	Bandwidth of PCM, PCM TDM signal multiplexing, Limitations of PCM system	Probability of Error for FSK	Code Division Multiple Access of DSSS
S	SLO-1	Lab-1: AM modulator and Demodulator	Lab-4: Pre emphasis and De-emphasis	Lab-7: DPCM and its Demodulation	Lab-10: QPSK Modulation and	Lab-13: Mini Project

4-5	SLO-2				Demodulation			
S-6	SI O-1		Classification of radio receiver, Functions and Characteristics of radio receiver	Data formatting	Generation, Detection, Signal Space Diagram of PSK	Code Division Multiple Access of DSSS		
3-0	SLO-2	Generation of AM waves: Linear method- Collector modulator	Tuned Radio Frequency receiver	Data formatting	Generation, Detection, Signal Space Diagram of PSK	OFDM Communication		
S-7	SLO-1	Non-linear Modulation-Balanced Modulator	Super-heterodyne receiver- AM	Differential PCM (DPCM)	Probability of Error for PSK	OFDM Communication		
3-1	SLO-2	Non-linear Modulation-Balanced Modulator	Super-heterodyne receiver- AM	Differential PCM (DPCM)	Probability of Error for PSK	OFDM Communication		
S-8	SLO-1	Demodulation of AM waves : Linear diode detector	Super-heterodyne receiver- FM	Delta modulation (DM)	Generation, signal space diagram and detection of QPSK	Measures of Information		
3-0	SLO-2	Demodulation of AM waves : Linear diode detector	Super-heterodyne receiver- FM	Delta modulation (DM), Noise in DM	Generation, signal space diagram and detection of QPSK	Measures of Information		
s	SLO-1	Lab-2: DSB-SC modulator and	Lab-5: PAM,PPM,PWM modulation and	I ah 8: DM and its Domodulation	Lab-11: DPSK Modulation and	Lab-14: Model Practical Exam		
9-10	SLO-2	demodulator	demodulation	Lab-o. Dim and its Demodulation	Demodulation			
S-11	SLO-1	Frequency modulation, Types of FM	Sources of Noise	Demodulation and detection process	Probability of Error for QPSK	Source encoding, Shannon's Channel capacity theorem		
3-11	SLO-2	Narrow Band FM, Wide Band FM, Phase modulation	Sources of Noise	Demodulation and detection process	Probability of Error for QPSK	Shannon's Channel capacity theorem		
S-12	SLO-1	Generation of Narrowband FM	Noise in AM (Envelope Detection),	Maximum likelihood receiver structure, Matched filter receiver	Generation, signal space diagram and detection of π/4QPSK	Linear block codes		
5-12	SLO-2	Generation of Narrowband FM	Noise in AM (Envelope Detection),		Generation, signal space diagram and detection of π/4QPSK	Linear block codes		
S-13	SLO-1	Demodulation of FM : Foster seely discriminator		Probability error of the Matched filter, Inter symbol interference, Eye pattern	Generation, signal space diagram and detection of QAM	Cyclic codes		
5-13	SLO-2	Demodulation of FM : Foster seely discriminator	Threshold effect, Pre-emphasis and De- emphasis		Generation, signal space diagram and detection of QAM	Cyclic codes		
s	SLO-1	Lab-3: FM Modulator and Demodulator	Lab-6: Pulse Code Modulation and	Lab-9: PSK Modulation and	Lab-12: BER performance analysis of	Lah 45. University Practical Even		
14-15	SLO-2	Lap-3: FINI MOQUIATOR and Demodulator	Demodulation	Demodulation	various Modulation Schemes	Lab-15: University Practical Exam		

Learning Resources	 Simon Haykin and Michael Moher, "Communication Systems," 5th edition, John Wiley & Sons, 2013 Singh. R. P & Sapre. S. D, "Communication Systems: Analog & Digital," 3rd edition, McGrawHill Education, Seventh Reprint, 2016. Simon Haykin, "Communication Systems", John Wiley & Sons, 4th Edition, 20008. Bernard Sklar, "Digital Communication, Fundamentals and Application", Pearson Education Asia 2nd Edition, 2001 	 B.P. Latini, Modern Digital and Analog Communication System, Oxford University Press, 3rd Edition, 2005. Shu Lin, Daniel Costello, "Error control coding – Fundamentals and Applications", Prentice Hall, Upper Saddle Piver NJ, 2rd Edition, 2004.
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Learning Asses	ssment												
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examination (50% weightage)			
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	(10%)#				
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Level 4	Remember	20%	20%	15%	15%	15%	15%	15%	15%	150/	15%		
Level 1	Understand	20%	20%	15%	10%	15%	15%	15%	10%	15%	10%		
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
Level Z	Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
1	Evaluate	400/	400/	450/	450/	450/	450/	450/	450/	450/	450/		
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%		
	Total	100) %	100	0 %	10	0 %	100	0 %		-		

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	Mrs. S. Vasanthadev Suryakala, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@jci.com</u>	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course	18	ECC206J	Course		VL	SI Design		ourse	С				Pr	rofessi	ional	Core					L	T P	<u> </u>)
Code			Name			5	Cat	egory													3	0 2	. 4	ł
Pre-requ Cours		18	ECC103J		Co-requisite Courses	Nil		gress ourse							1	8ECE	301J							
Course Of	ffering De	epartment	Electro	onics and Comm	nunication Engineering	Data Book / Codes/Standards									Nil									
					g this course is to: 'GA in electronic design a	utomation of digital circuits	L.	earnir 2	1g 3	1	2	3	4	Progr	am L 6	earniı 7	ng O	utcom 9	n es (F 10		12	13 1	4 1	15
	Understa	construct and sim and MOSFET ope nt a given logic fu	eration		ultipliers. logic styles for improved p	performance	-															ement nt		÷
CLR-5 :	Understa rules.	and the basic proc	cesses in l	C fabrication, st	teps in the fabrication of N	IOS ICs, and as well the layout design	(mool	y (%)	nt (%)	aup		ent	Research			Sustainability		Vork		JCe		Professional Achievement : Project Management	0 1	Research
CLR-6 :		lern engineering t design and analys				ut design experiments and gain experience	Thinking (Bloom)	Proficiency (%)	Attainment (%)	a Knowle	nalysis)evelopm		ol Usage	Culture	∞ŏ		& Team Work	ation	t. & Finar	eaming	ofessiona roject Ma		Analyze & I
Course Le	earning C	Outcomes (CLO):	: At the e	end of this cours	se, learners will be able to	:	Level of Th	Expected F	Expected /	Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design,	Modern Tool Usage	Society & Culture	Environment	Ethics	Individual 8	Communication	Project Mgt. & Finance	Life Long Leaming	PSO-1: Pr PSO - 2: F	i jidite	PSO – 3: A
CLO-1 :	Design a	nd implement dig	ital circuit	s using Verilog I	HDL to simulate and verif	/ the designs.	3	85	75	-	Н	Н	-	Н	-	-	-	-	-	-	-	-	-	-
					ells and multipliers to addr	ess the design of datapath subsystem.	3	85	75	-	Н	Н	-	Н	-	-	-	-	-	-	-			-
		the characteristic					2	80	70	H	М	-	-	-	-	-	-	-	-	-	-	-		-
					ates designed using differ		2	80	70	-	L	L	-	-	-	-	-	-	-	-	-		- ·	-
					d the physical implement		2	80	70	-	L	L	-	-	-	-	-	-	-	-	-			-
CLO-6 :	Use HSF	PICE computer an	nalysis pro	gram and Verilo	og HDL for simulation and	analysis of MOS circuits and building blocks	; 3	85	75	-	-	-	-	-	-	-	-	Н	М	L	М	-		М

Duratio	n (hour)	Learning Unit / Module 1: Introduction to Verilog HDL & Coding	Learning Unit / Module 2: Subsystem Design	Learning Unit / Module 3: MOS Transistor	Learning Unit / Module 4: CMOS Inverter and Circuit Design Styles	Learning Unit / Module 5:
		15	15	15	15	15
	SLO-1 Introduction to HDL & Verilog HDL		General VLSI System Components: Multiplexers	Generic overview of the MOS device: MOS transistor symbols	CMOS Inverter Characteristics: Operation and properties of static CMOS inverter	Properties of basic materials used in microelectronics: Silicon, Silicon dioxide
S-1	SLO-2	Introduction to Verilog HDL, modules and ports	Decoders	MOS structure demonstrating (a) accumulation, (b) depletion, and (c) inversion; nMOS transistor demonstrating cutoff, linear, and saturation regions of operation	VTC of static CMOS inverter	Polysilicon and Silicon Nitride
S-2	SLO-1	Lexical Conventions: White Space and Comments, Operators	Comparators	MOS Transistor under Static Conditions: The threshold voltage	DC Inverter Calculations	Basic Processes in Integrated-Circuit Fabrication: Wafer Formation, Photolithography, Well and Channel Formation
	SLO-2	Numbers, Strings, Identifiers, System Names, and Keywords	priority encoder	Resistive operation	Symmetrical Inverter	Silicon Dioxide (SiO ₂), Isolation, Gate Oxide
S-3	SLO-1	Verilog Data Types: Nets, Register Variables, Constants	shift and rotate operations	Saturation region	Inverter switching characteristics	Gate and Source/Drain Formations, Contacts and Metallization, Passivation, Metrology
		Referencing Arrays of Nets or Regs	Adders: Standard adder cells	Current-voltage characteristics	Output capacitance	Some Recurring Process Steps:Diffusion and Ion Implantation, Deposition, Etching, Planarization

	SLO-1	Lab-0: Verilog Operators:				
S-4, 5	SLO-2	Arithmetic Operators, Bitwise Operators, Reduction Operators, Logical Operators, Relational Operators, Shift Operators, Conditional Operator, Concatenation Operator, Expressions and Operands, Operator Precedence	Lab-3:Design using FSM and ASM charts		Lab-9:Design and Analysis of CMOS Inverter using HSPICE	Lab-12: Design and Analysis of 4-input Dynamic NAND gate using HSPICE
S-6	SLO-1		Ripple Carry Adder (RCA)	Dynamic behavior: MOSFET Capacitances, viz., MOS structure capacitances	Secondary Parasitic Effects: Leakage Currents, Parasitic Resistances	Simplified CMOS Process flow
-	SLO-2	Realization of Combinational and sequential circuits	Carry Look-Ahead Adder (CLA)	Channel capacitance and Junction (or, depletion) capacitances	Inverter layout	
0.7	SLO-1	Compilation and simulation of Verilog	Carry Select Adder (CSL)	Parasitic Resistances, viz., Drain and Source Resistance. Contact Resistance	Power-Delay Product: Static Power Consumption	Layout design rules: Well rules, transistor rules
S-7	SLO-2	Test bench	Carry Save Adder (CSA)	Non-ideal I-V effects: Mobility Degradation, Velocity Saturation	Dynamic Power Consumption, Total Power Consumption, PDP	Contact rules, metal rules, via rules and other rules
	SLO-1	Dataflow modelling	Carry Skip Adder (CSK)	Channel Length Modulation, Threshold Voltage Effects	CMOS Circuit Design Styles:Static CMOS logic styles	Gate Layouts
S-8	SLO-2	Realization of Combinational and sequential circuits	Carry Bypass Adder (CBA)	Leakage, Temperature Dependence, Geometry Dependence, Subthreshold Current	CMOS circuits, pseudo-nMOS, tristate circuits, clocked CMOS circuits	Stick diagrams
S-9, 10	SLO-1 SLO-2	Lab-1:Realization of combinational and sequential circuits using gate-level and dataflow modeling	Lab-4:Realization of VLSI adders - I	Lab-7: Realization of VLSI multipliers - II	Lab-10: (a) Design and Analysis of complex CMOS gate using HSPICE (b) Design and Analysis of Pseudo-NMOS gates using HSPICE	Lab-13: Model Practical Examination
S-11	SLO-1	Behavioral modelling	Multipliers: Overview of multiplication (unsigned multiplication, shift/add multiplication algorithms, multiplication of signed numbers, types of multiplier architectures)	Short-channel MOSFETS: Hot carriers, Lightly-Doped Drain (LDD)	Differential Cascade Voltage Switch Logic (DCVSL), Pass Transistor Logic (PTL)	CMOS Process Enhancements: Transistors (Multiple Threshold Voltages and Oxide Thicknesses, Silicon-on- Insulator, High-k Gate Dielectrics, Higher
-	SLO-2	Realization of Combinational and sequential circuits	Braun multiplier	MOSFET scaling	Dynamic CMOS logic styles: Basic dynamic logic	Mobility, Plastic Transistors,)
S-12	SLO-1	Switch-level modelling	Baugh-Wooley multiplier	Short-channel effects: Negative Bias Temperature Instability (NBTI), oxide breakdown	Signal integrity issues in dynamic design	Interconnects
3-12	SLO-2	Realization of MoS circuits	Wallace Tree multiplier	Drain-Induced Barrier Lowering (DIBL), Gate-Induced Drain Leakage (GIDL), Gate Tunnel Current	Signal integrity issues in dynamic design	Circuit elements
S-13	SLO-1	Design using FSM	Booth multiplier	Tutorials	Domino Logic Circuits: Differential Domino logic, multiple-output domino	Beyond conventional CMOS
	SLO-2	Realization of sequential circuits	Booth multiplier	Tutorials	Compound domino, NORA, TSPC	Tutorials
S-14, 15	SLO-1	Lab-2: (a) Realization of digital circuits using behavioral modeling (b) Realization of MOS circuits using switch-level mdeling	Lab-5:Realization of VLSI adders - II	Lab-8: Realization of RAM & ROM	Lab-11: (a) Design and Analysis of AND/NAND gate in DCVSL using SPICE (b) Design and Analysis of Pass- Transistor gates and CPL gates using HSPICE	Lab-14: End-Semester Practical Examination

	9.	Jan Rabaey, Anantha Chandrakasan, B Nikolic, "Digital Integrated Circuits: A Design	12. R. Jacob Baker, "CMOS Circuit Design, Layout, and Simulation", Wiley, (3/e), 2010.
Loorning		Perspective". Second Edition, Feb 2003, Prentice Hall of India.	13. John P. Uyemura, "CMOS Logic Circuit Design", Kluwer, 2001.
Learning Resources	10.	Weste, Harris, "CMOS VLSI Design: A Circuits and Systems Perspective", 4th edition, Addision-	14. S. Palnitkar , Verilog HDL – A Guide to Digital Design and Synthesis, Pearson , 2003
Resources		Wesley, 2011.	15. Paul. R.Gray, Robert G. Meyer, "Analysis and Design of Analog Integrated Circuits", Wiley, (4/e), 2001.
	11.	Wayne Wolf, "Modern VLSI Design: IP-based Design", 4th edition, PHI, 2009.	16. M.D. Ciletti, Modeling, Synthesis and Rapid Prototyping with the Verilog HDL, Prentice Hall, 1999

Learning As	sessment											
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Einal Examination	n (50% woightaga)	
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#	 Final Examination (50% weightage) 		
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100	0 %	10	0 %	10	0 %	100	0 %	10	0 %	

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. Manikandan AVM, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. J. Manjula, SRMIST

Cours Code	-	18ECC301T	Course Name	Wireless Communication				ourse egory	С				F	Profess	sional	Core				L 3	T 1	P 0	C 4
Pre-rec Cour		18ECC205J, 18ECC105T Co-requisite Courses				Nil	18ECE220T																
Course Offering Department Electronics and Communication Engineering Data Book					ok / Codes/Standards									Nil									
Course Learning Rationale (CLR): The purpose of learning this course is to: CLR-1: Understand the elements of Wireless Communication and mobile communications CLR-2: Understand the Mobile Radio Wave Propagation - Large Scale Fading CLR-3: Analyze how to apply Mobile Radio Wave Propagation - Small Scale Fading CLR-4: Study the Capacity and Diversity concepts in wireless communications CLR-5: Acquire the knowledge of Wireless System and Standards CLR-6: Understand and design various wireless systems Course Learning Outcomes (CLO): At the end of this course, learners will be able to: CLO-1: Acquire the knowledge of Wireless communication and basic cellular concepts CLO-2: Understand` the essential Radio wave propagation and mobile channel models CLO-3: Familiarize about Various performance analysis of mobile communication system. CLO-4: Attain the knowledge of Diversity and capacity concepts CLO-5: Be familiar with the various standards of Mobile Communication Systems					d link performance	N N N Level of Thinking (Bloom)	75 75 75 75 75	00 00 00 00 00 00 Expected Attainment (%) &	H H H H Engineering Knowledge	H · H H · Problem Analysis	H - Design & Development E	H - Analysis, Design, Research	Prog 5 5 Wodern Tool Usage - - - - - - - - - - -	1 man 0 0 1 - - - - - - - - -		- Ethics	9 1 Work		12 M M Fife Long Learning	X · X × PSO-1: Professional	PSO - 2: Project Management 1	H T H H T PSO – 3: Analyze & Research G	
		ommunication: Mobile nmunications	Large S	Scale Fading	Small Scale Fac	ading			Improvement on Link performance						e	Wireless systems and standards							
(100	<i></i> ,		12		12	12								12									
S-1	SLO-1	mobile radio com	vireless communication and nmunication wireless communications -	Introduction to Rad	lio wave Propagation	Introduction Small scale mu propagation Impulse response model of	. Introduction to diversity, equalization and								d	AMPS Voice modulation Process							
5	SLO-2	simplex, half dup		Large scale and sn	nall scale fading	channel	•	path capacity															
S-2	SLO-1	Paging and Cord	dless systems		equation- Free space	Impulse response model of channel	Space diversity									GSM system architecture and its interfac					ices		
-	SLO-2	Cellular telephon	ne systems	propagation model	- pathloss model	Small scale multipath meas Direct Pulse measurement	uremer	nts -	Sc	Scanning diversity						Ì	Som system aronitecture and its internates						
S-3	SLO-1	Timing diagram -	- landline to mobile	Two Ray model		Small scale multipath meas Sliding correlator measurem	nent		Ma	Maximal ratio combiner							GSM frame structure						
	SLO-2	0 0	- mobile to mobile	Two rtay model		Small scale multipath meas Swept frequency measurem		nts -	Eq	Equal gain diversity							- GOM TRAINE STRUCTURE						
S-4 SLO-1 SLO-2		near field	arameters, Far field and	Simplified pathloss	model	Parameters of mobile multip									GSM speech operations input - output				itout				
		Frequency reuse directional anten	e, sectored and omni- inas	Emperical model -		Time dispersion and Cohere	ent ban	dwidth								ĺ	Gow spectri operations input - output						
S-5		Channel assignn Handoff and its t		Emperical model - Emperical model -	Hata model Walfish and bertoni	Parameters of mobile multip Doppler spread and Cohere			- Ca	Capacity in AWGN						F	Forward CDMA process						
	-		system capacity	model Piecewise linear m		I Types of fading: Flat and Fr											Reverse CDMA Process						
Geo GEO-1 Interference and system capacity in receive and induct righted						Capacity of hat launing channels								I LEVELSE ODIMIN F IOCESS									

	SLO-2			selective fading		
S-7	SLO-1	Trunking and Grade of Service	Shadowing	Types of fading: Flat and Frequency	Equalizer and its mode	Multicarrier modulation
3-1	SLO-2		Combined pathloss and shadowing	selective fading		
S-8	SLO-1	Cell splitting	Outage Probabilty	Types of fading: Fast and Slow fading	Adaptive equalizer block diagram	OFDM Transmitter Block diagram
0-0	SLO-2			Types of lading. Tast and olow lading		
S-9	SLO-1	Sectoring	Cell Coverage Area	Types of fading: Fast and Slow fading	Types of Equalizers - elementary level only	OFDM Receiver Block diagram
00	SLO-2	Cooling		Types of loaning. T ast and blow loaning		
S-10	SLO-1	Microcell zone concepts	Solving problems – Brewster angle	Ricean distribution	Introduction to MIMO antennas	Importance of Cyclic Prefix
0-10	SLO-2					
S-11	SLO-1	Umbrella cells	Solving problems –empirical model	Rayleigh distribution	Introduction to MIMO antennas	Case study - Modern antennas
0-11	SLO-2					
S-12	SLO-1	Solving Problems Solving problems – friis transmission Solving problems – Doppler effect		Case study :Recent trends in Diversity and	Case study - Modern antennas	
0-12	SLO-2	formula		Sowing problems – Doppier enect	MIMO antennas	Case study - modern antennas

Learning Resources	John D Kraus , Ronal McGraw Hill, 2010 Constantine Balanis.	eless Communications: Principles and Practice", 2 nd Edition, Pearson, 2011. d J Marhefka, Ahmed S Khan "Antenna and Wave Propagation", 4th Edition, Tat A, "Antenna Theory: Analysis and Design", 3rd Edition, John Wiley, 2012. Mireless Communications", Wiley, 2 nd Edition-2005, Reprint-2014	5. 6. 7.	Andrea Goldsmith, "Wireless Communications", Cambridge University Press, Aug 2005 Schiller, "Mobile Communications", Pearson Education Asia Ltd., Reprint 2012 Lee W.C.Y., "Mobile Communications Engineering: Theory and Applications", McGraw Hill, New York, 2nd Edition, 1998
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Learning Asse	essment										
	Bloom's		Einal Examinatio	n (50% weightage)							
	Level of Thinking	CLA –	1 (10%)	CLA – 2	CLA – 2 (15%)		3 (15%)	CLA – 4	l (10%)#		ii (50 % weigiilage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100	0 %	100	0 %	10	0 %	100	0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. Sandeep Kumar P, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. T. Ramarao, SRMIST

Course	18ECC302J	Course	Microway	ve & Optical Communications	Course	C	Professional Core	L	Т	Р	С
Code	102003020	Name	WICIOWAY	Value & Optical Communications Category		i i olessional obre	3	0	2	4	
Pre-requis	ite	18ECC205.	Co-requisite	Nil	Progressiv	ve	18ECE226T & 18ECE323T				
Courses		102002000	Courses		Courses		102022201 & 1020231				
Course Offer	ring Department	El	ectronics and Communication Engi	ineering Data Book / Codes/Standards			Nil				

Course Learning Rationale (CLR): The purpose of learning this course is to:	Learning		ng	Program Learning Outcomes (PLO)															
CLR-1: Identify Microwave active devices and Microwave generators	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14 <i>°</i>	15
CLR-2 : Analyze Microwave passive devices																		Ħ	ے
CLR-3 : Explore Microwave Measurements	(Bloom)	(%)	(%)					<u>c</u>			oility							iemei	earc
LR-4 : Analyze Optical Fibers Optical Sources, Amplifier and Transmitter Optical Detectors , Receiver and Performance Measurements		Proficiency	Attainment		Engineering Knowledge		oment	Resear	ge		Sustainability		n Work		Finance	b	onal	Ř	å Res
R-5 : Explore Optical Communication System Design and Concepts		d Proficiel	ttair		loc V	lysis	/elopi	sign,	Usage	Culture	s S		eam	ы	& Fir	arning	essio	ject	alyze
Analyze Microwave and optical components		P ₽			ing	Analysis	De	Des	Tool	& Cu	Tent		1&1	icati	Mgt. 8	Lea	Profe		Analy
		ecte	ecte		neer	Problem	sign &	ysis,	dem 7	ety	Environment	ĸ	dividual	ommunication	Sct V	Long	H-1: ever	nigt	- 3:
Course Learning Outcomes (CLO): At the end of this course, learners will be able to:		Expected	Expected		Engi	Prob	Desi	Analy	Mod	Soci	Envi	Ethic	Indiv	Com	Project	Life	PSO Achi	Tect DSC	PSC
CLO-1: Acquire knowledge on the theory of microwave transmission, microwave generators and associated components.		80	70		Н	-	-	L	-	1	-	-	-	-	-	-	-	-	L
CLO-2 : Analyse microwave passive devices and components. 2		80	70		Н	М	Н	Н	-	I	-	-	-	-	-	-	L	- 1	М
CLO-3: Understand microwave measurements and associated techniques with equipment 2		80	70		Н	М	Н	М	-	I	-	-	-	-	-	-	М	-	Н
CLO-4 : Familiarize with the fundamentals of light transmission through fiber		80	70		Н	Н	-	М	-	-	-	-	-	-	-	-	L	-	L
CLO-5 : Design a basic optical communication system.	2	80	70]	Н	Н	-	Н	-	-	-	-	-	-	-	-	М	- 1	М
CLO-6: Understand the working principle of microwave components , Microwave measurements, optical sources, detector and fibers		80	70		Н	Н	Н	Н	-	-	-	-	-	-	-	-	М	-	Н

Du	ration					
(h	our)	15	15	15	15	15
S-1	SLO-1 SLO-2		High frequency parameters: S parameters and S matrix analysis for N-port microwave device	Impedance matching.	Elements of Optical fiber communication	Point-to-Point link –Analog system design considerations and design steps
S-2	SLO-1 SLO-2	History of Microwave Engineering, Microwave transmission and Applications; Maxwell Equations	Directional coupler	onal coupler VSWR and Impedance measurement		Point-to-Point link – Digital system design considerations and design steps
S-3		Microwave Tubes Klystron amplifier	E and H plane Tee	Measurement of Power	Optical fiber structure, Light Propagation in Optical fibers: Ray theory, Total Internal reflection, Skew rays	Digital Link Design: Link power budget
S-4-5	SLO-1 SLO-2	II ap-1 Unaracteristics of Reflex	Lab- 4 Gain and radiation pattern of Horn antenna	Lab- 7 Practice session	Lab- 10 Measurement of Numerical Aperture, propagation and bending losses of optical fiber	Lab- 13 Design of basic Optical Communication system using computational tool
S-6	SLO-1 SLO-2	Reflex Klystron oscillators	Magic Tee	Measurement of Frequency and Q factor	Optical Sources: Light source materials, LED Structures	Rise time budget
S-7	SLO-1 SLO-2	Magnetron oscillators	Microwave Circulators, Isolators	Insertion loss measurements	LED Characteristics	Overview of Analog links: Radio over Fiber;
S-8	SL0-1	Microwave Bipolar Transistors	Attenuators and Phase Shifters	Attenuation measurements	Semiconductor Laser Diode, Laser	Key link parameters

	SLO-2	Field effect transistor			Characteristics	
S-9-	SL0-1	Lab- 2 Study of power distribution in Directional coupler, E plane, H plane	Lab- 5 Characteristics of filters, Microstrip patch antenna and parallel	Lab- 8 DC characteristics of LED and	Lab- 11 Analysis of Analog optical link	Lab- 14 Practice Session
10	SLO-2	and Magic Tee	line coupler	Laser diode		
S-11	SLO-1	IMPATT. TRAPATT and Tunnel diode	Rectangular Waveguides	Measurement of Scattering parameters	Optical Detectors: PIN and APD photo	Multichannel System: Need for multiplexing
3-11	SLO-2		Rectangular wavegulues	measurement of Scattening parameters	detector	Operational principles of WDM, DWDM
S-12	SLO-1	Gunn diode	Rectangular Waveguides	Measurement of Scattering parameters	Responsivity and efficiency of APD	WDM Components: Coupler/Splitter, Fabry
3-12	SLO-2	Guint diode	Rectangular wavegulues	measurement of Scattening parameters	Responsivity and eniciency of APD	Perot Filter
	SLO-1			Functioning details of Vector Network		WDM Components: Optical MEMS
S-13	SLO-2	Gunn Oscillation modes		Analyzer; Signal Analyzer; Spectrum analyzers	IF IDER ATTENUATION and dispersion	switches
S-14-	SLO-1	Lab- 3 Impedance measurement by	Lab- 6 Design of RF Filters and	Lab- 9 DC characteristics of PIN and		Lab- 15 Study experiment - Gunn Diode
15	SLO-2	slotted line method	3	APD photo-diode	Lab- 12 Analysis of Digital optical link	(Microwave) and Optical WDMA (Optical)

Learning Resources	 David M. Pozar, "Microwave Engineering", 4th Edition, John Wiley & Sons, 2012. David M. Pozar, "Microwave & RF Design of Wireless Systems", John Wiley & Sons, 2001. Samuel Y. Liao, "Microwave Devices and Circuits", 3rd Edition, Pearson Education, 2013. Robert. E. Collin, "Foundations for Microwave Engineering", 2nd edition, Wiley, Reprint 2014. Annapuma Das, Sisir K. Das, "Microwave Engineering", 3rd Ed., McGraw Hill, 2015. I. Hunter, "Theory and design of microwave filters", The Institution of Engineering & Technology, 2001. Keiser G, "Optical Fiber Communication Systems", 5th Edition, 6th Reprint, McGraw Hill Education (India), 2015. 	 Vivekanand Mishra, Sunita P. Ugale, "Fiber Optic Communication: Systems and Components", Wiley-India, 1st edition, 2013 Djafar.K. Mynbaev and Lowell and Scheiner, "Fiber Optic Communication Technology", Pearson Education Asia, 9th impression, 2013 John M. Senior, "Optical fiber Communications: Principles and Practice", Pearson Education, 3rd Edition, 2009 R.P. Khare, "Fiber Optics and Optoelectronics", Oxford University Press, 2007. 12. Rajiv Ramaswami, Kumar N. Sivaranjan, Galen H.Sasaki "Optical Networks A practical perspective", 3nd edition, 2013
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Learning Assess	ment										
	Bloom's		Einal Examinatio	n (50% weightage)							
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		in (50 % weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Lever	Understand	2070	20%	1376	1376	1376	1376	1370	1570	1376	1570
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 2	Analyze	2070	20%	2076	20%	20%	20%	20%	2076	20%	2070
Level 3	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Level J	Create	10%	10%	13%	15% 15% 15%		13%	13%	10%	10%	1370
	Total	100) %	100) %	100 %		10	0 %	10	0 %

Course Designers											
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts									
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. P. Sandeep Kumar, SRMIST									
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. T. Ramarao, SRMIST									

Course Code	18 - ((((((((((((((((((ourse Iame	Comp	Computer Communication Networks			ourse		С				Pı	rofess	ional	Core					L 3	T	P 2	C
																		5	0	2	7			
Pre-requisite Courses	· /8/////				gres ours	sive es							1	8ECE	320T									
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards												Nil												
Course Learning Rationale (CLR): The purpose of learning this course is to:				earn	ing						Progr	ram L	earni	ng O	utcon	nes (F	PLO)							
CLR-1: Introduce the basic concepts in the field of computer networks.			1	2	3		1	2	3	4	5	6	7	8	9	10		12	13	14	15			
CLR-2: Understand the functional aspects of OSI model architecture. CLR-3: Acquire knowledge of the Network Layer protocols CLR-4: Analyze the various issues and challenges of Transport Layer. CLR-5: Familiarize the various Application Layer Protocols. CLR-6: Utilize the networking concepts to analyze the performance of Routing protocols. Course Learning Outcomes (CLO): At the end of this course, learners will be able to:			evel of Thinking (Bloom)	Expected Proficiency (%)	Attainment		Engineering Knowledge	² roblem Analysis	Design & Development	Analysis, Design, Research	Aodem Tool Usage	Society & Culture	Environment & Sustaina bility	Ethics	ndividual & Team Work	Communication	^o roject Mgt. & Finance	Life Long Learning	O–1: Professional nievement	O – 2: Project Management chniques	0 – 3: Analyze & Research			
			o Le	Щ 80	山 70		Ē	-		An:	~	Я М	لطَ M		pu	0	ш.	۳ ۳	H Act B	S H S H	PSO			
CLO-1: Understand the basic services and concepts related to internetworking. CLO-2: Explain the basic OSI model architecture and its lower layer functions.			2	80			н Н	-	-	-	-	M	M	-	-	-	-	M	H	M	-			
CLO-3 : Give an insight of the various Network Layer concepts, mechanisms and protocols.		2	75			Н	-	-	-	-	M	M	-	-	-	-	M		M	Η				
CLO-4 : Appreciate the services and techniques of Transport Layer.		2	85			Н	-	-	-	-	M	М	-	-	-	-	M	-	H	M				
CLO-5: Discuss the various services and protocols in Application Layer.		2	85			Н	-	-	-	-	М	М	М	-	-	-	М	Н		М				
CLO-6 : Implement and analyze the various Networking concepts and Routing protocols.		2	80	70		-	-	-	-	Н	-	-	-	-	-	-		М		Н				

	ration nour)	DATA COMMUNICATION & NETWORKING BASICS	OSI LOWER LAYERS	NETWORK LAYER	TRANSPORT LAYER	APPLICATION LAYER
		15	15	15	15	15
S-1	SLO-1	Introduction to Data Communication and Networking	Network models	Introduction to Network Layer	Introduction to Transport Layer	Introduction to Application Layer
3-1	SLO-2	Data transfer modes-Serial and Parallel transmission	OSI layer architecture	Need for Internetworking	TCP/IP Model	Application Layer Paradigms
S-2	SLO-1	Protocols & Standards	Data Link Layer-Introduction	Addressing-Classful	User Datagram Protocol(UDP)	Client Server Interaction
3-2	SLO-2	Layered Architecture	Link Layer Addressing	Addressing-Classful	User Datagram Protocol(UDP)	Client Server Interaction
	SLO-1	Principles of Layering & Description	Error Detection	Addressing-Classless	Transmission Control Protocol(TCP)	SIP
S-3	SLO-2	Brief description of concepts in OSI & TCP/IP model	Error Detection	Addressing-Classless	Transmission Control Protocol(TCP)	SIP
	SLO-1	Lab 1:To build and configure a simple	.			Lab 13: Create a Socket (TCP&UDP)
S 4-5	SLO-2	network of four nodes connected with point-to-point links.	Lab 4: To simulate token ring protocol and to study its performance.	Lab 7:To simulate CSMA/CA protocol and to study its performance	Lab 10: Implementation and study of Selective Repeat protocol.	between two computers and enable file transfer between them.
S-6	SLO-1	Switching Types- Circuit- & Packet switching	Error Correction	Network Layer Protocol-IPV4	TCP Services & Features	Compression Techniques
3-0	SLO-2	Switching Types- Message switching, Comparison of switching types	Error Correction	Internet Protocol(IP)-IPV4	TCP Services & Features	Compression Techniques

S-7	SLO-1	LAN, MAN & WAN	Data link control-LLC	Internet Protocol(IP)-IPV6	Congestion Control	Introduction to Cryptography
3-1	SLO-2	LAN, MAN & WAN	Data link control-LLC	Internet Protocol(IP)-IPV6	Congestion Control	Types, Attacks and Services
S-8	SLO-1	Network topologies-Types	Data link control-MAC	Routing Protocols- Distance Vector& Link State	Congestion Control	DES
3-0	SLO-2	Comparison of topologies	Data link control-MAC	Routing Issues-Delivery, Forwarding and Routing	Congestion Control	DES
S	SLO-1	Lab 2: To simulate star and bus network	Lab 5:Implementation of Error detection	Lab 8: Implementation and study of	Lab 11: To configure a network using	Lab 14: Implementation of Data
9-10	SLO-2	topologies.	and Correction scheme.	stop and wait protocols	Link State Routing protocol .	Encryption and Decryption.
S-11	SLO-1	IEEE standards for LAN-Ethernet	Flow & Error Control Protocol	Routing Information Protocol-RIP	QOS-Quality of Service	RSA
3-11	SLO-2	Types of Ethernet	Flow & Error Control Protocol	Routing Information Protocol-RIP	QOS-Quality of Service	RSA
S-12	SLO-1	Token Bus	ARQ Schemes	Open Shortest Path First-OSPF	Techniques to improve QOS	Email
3-12	SLO-2	Token Ring	ARQ Schemes	Open Shortest Path First-OSPF	Techniques to improve QOS	FTP
S-13	SLO-1	FDDI	HDLC	Border Gateway Protocol-BGP	Techniques to improve QOS	HTTP
3-13	SLO-2	FDDI	HDLC	Border Gateway Protocol-BGP	Techniques to improve QOS	SNMP
S	SLO-1	Lab 3:To simulate token bus protocol	Lab 6:To simulate CSMA/CD protocol	Lab 9: Implementation and study of Go	Lab 12: To configure a network using	Lab 45 Mini Dustant
14-15	SLO-2	and to study its performance.	and to study its performance	back N protocol.	Distance Vector Routing protocol.	Lab 15: Mini Project

Learning	1.	Behrouz A.Fehrouzan, "Data communication & Networking", Mc-Graw Hill, 5 th Edition Reprint, 2014.	3 4
Resources	2.	Andrew S.Tanenbaum, "Computer Networks", Pearson Education India, 5th Edition, 2013.	5

William Stallings, "Data & Computer Communication", Pearson Education India, 10th Edition, 2014.
 James F. Kurose, Keith W. Ross, "Computer Networking: A Top–Down Approach Featuring the Internet", Pearson Education, 6th Edition, 2013.

5.	"Lab Manual", Department of ECE, SRM Institute of Science and Technology	

Learning As	sessment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Eventination	(EQQ(uninhters)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#	Final Examination	n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	100	0 %	10	0 %	100) %	100) %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Ms. T. Ramya, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

B. Tech in Electronics and Communication Engineering

2018 Regulations

ProfessionalElective Courses (E)

Department of Electronics and Communication Engineering SRM Institute of Science and Technology SRM Nagar, Kattankulathur – 603203, Kancheepuram District, Tamilnadu

Course	18ECE201J	Course		PYTHO	AND SCIENTIFIC PYTHON	Course	F	Professional Elective	L	Т	Ρ	С		
Code	102022010	Name		11110		Category	-		2	0	2	3		
Pre-requis Courses		Nil		Co-requisite Courses	Nil	Progressiv Courses	e	Nil						
Course Offe	ering Department	Ele	ectronics and Co	ommunication Eng	neering Data Book / Codes/Standards			Nil						
Course Lear	rning Rationale (CLF	R): The put	rpose of learnin	g this course is to:		Learning	arning Program Learning Outcomes (PLO)							

Course L	earning Rationale (CLR): I he purpose of learning this course is to:	L	earnii	ng					Progr	ram L	.earnı	ing O	utcon	nes (P	'LO)			
CLR-1 :	Understanding the python language construct and apply them for scientific computation	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15
CLR-2 :	Apply python vector ,list and plot concept to solve curve fitting										٢							ent ch
CLR-3 :	Applying Dictionary concept to model Polynomials	Ê	(%	_				arch			Sustainability							sear
CLR-4 :	Create insights to difference equation based system model and solving them with python	8	\sim	t (%)	Knowledge		ent	see			aina		Work		e		_	lanag & Res
CLR-5 :	Analyze Monte Carlo Simulation for computing Probabilities	B)	ency	nen	wle	s	E d	Å,	age	Ð	Sust		≤ E		Finance	ę	5	2 0
CLR-6 :	Create insights to the concepts and programming of SciPy, numpy, matplotlib to solve scientific problem	hinking	ofici	Attainme	Knc	alysi	velo	sign,	S	Culture	~		Tea	tion	S E	eaming	essi	oject alyze
		Thin	Å Å	d Att	ering	Ana	, Dev	De	00	& င၊	Jent		حە	icat	Mgt.		Prof	2: Pro ques 3: An
	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected	Expected	Engineer	Problem	Design &	Analysis,	Modern	Society 8	Environn	Ethics	Individual	Commun	Project N	Life Long	PSO-1: I Achiever	- in
CLO-1 :	Apply python language construct to compute formula and scientific problem	2	80	70	Н	Н	-	-	-	-	-	-	-	-	-	-	-	- H
CLO-2 :	Analyze Mathematical Models system using f Difference Equations and solving	2	85	75	Н	Н	-	-	-	-	-	-	-	-	-	-	-	- H
CLO-3 :	Apply time sequence concept for generation and processing of audio signal by python	2	75	70	Н	-	-	Н	-	-	-	-	-	-	-	-	Н	
CLO-4 :	Apply python language construct to solvePolynomials	2	85	80	Н	Н	-	-	-	-	-	-	-	-	-	-	-	- H
CLO-5 :	Apply python language construct to compute probability byMonte Carlo Simulation ,game design and dynamic random motion creation	2	85	75	Н	-	Н	-	-	-	-	-	-	-	-	-	-	- H
CLO-6 :	ApplySciPy, numpy, matplotlib to statistical analysis, correlation coefficient analysis, Solving equations-Linear least squares solutions and signal processing	2	80	70	-	-	-	-	-	-	-	-	-	-	-	-	-	- H

	ration	Solving Simple Formula And Scientific Problem	Plots, Array and Difference Equation Modelling	File I/O, Polynomials and Web Programming	Random Process and Game Programming	SciPy ,Numpy and Signal Processing
u,	nour)	12	12	12	12	12
S-1	SLO-1	Computing with Formulas- Using a Program as a Calculator	Vectors, Mathematical Operations on Vectors, Vector Arithmetics and Vector Function	Reading Data from File-Line by Line,Reading a Mixture of Text and Numbers	Drawing Random Numbers-Uniformly Distributed Random Numbers	SciPy, numpy,matplotlib
•	SLO-2		Arrays in Python Programs-Using Lists for Collecting Function Data	Making Dictionaries		Basic array methods in numpy,Changing the shape of an array
	SLO-1	Celsius-Fahrenheit Conversion,	Curve Plotting-The SciTools and Easyviz Packages	Dictionary Operations	The Gaussian or Normal Distribution- Drawing a Random Element from a List	Maximum and minimum values
S-2	SLO-2		Plotting a Single Curve,Decorating the Plot,Plotting Multiple Curves,Controlling Line Styles	Polynomials as Dictionaries,File Data in Dictionaries,File Data in Nested Dictionaries	Drawing random interger	Reading and writing an array to a fle
S-3-4	SLO-1	Lab 1:programming on formula and Standard Mathematical Functions-	Lab 4: Curve Plotting	Lab 7:reading student marks file into a dictionary data with the student name	Lab 10: real card games	Lab 13: numpy file reading and data
5-5-4	SLO-2	Evaluate a Gaussian function,Compute the air resistance on a football	Lab 4. Curve Protuing	as key and computing the average grades	Lab 10. real card games	analysis
	SLO-1	Complex Numbers, Complex Arithmetic's in Python	Numerical Python Arrays manipulations	Strings-Common Operations on Strings	Computing Probabilities-Principles of Monte Carlo Simulation	Statistical methods in numpy
S-5	SLO-2 Input Data-Reading Keyboard Input- Reading from the Command Line		Higher-Dimensional Arrays-Two- Dimensional Numerical Python Arrays	Reading Coordinates	Throwing Dice,Rolling Two Dice game	Statistical methods in numpy

S-7-8 SL SL S-9	SLO-1 SLO-2	Using Modules Lab 2: program on Making Modules and using them while loops and for loops	Lab 5:Animating a Function- temperature on earth	Lab 8:reading web temperature text file into Dictionaries and computing average Temperature	Simple Games- Guessing a Number Lab 11: Simple Games	Solving equations-Linear least squares solutions-Beer-Lambert Law Lab 14: the correlation coefficient between pressure and temperature
S-7-8 SL S-9	SLO-2	using them	temperature on earth	into Dictionaries and computing average Temperature	Lab 11: Simple Games	
SL SL S-9	5LU-2	-	•	average Temperature		between pressure and temperature
S-9	SLO-1	while loops and for loops	the Factorial as a Difference Equation			
			'	Extracting Data from an HTML Page	Random Walk in One Space Dimension	One-Dimensional Fast Fourier Transforms
52	SLO-2	Lists and list manipulation	Growth of a Population, Payback of a Loan, Making a Living from a Fortune	Writing a Table to File, Reading and Writing	Basic Implementation, visualization and Computing Statistics of the Particle Positions	Matplotlib basics-Plotting on a single axes object, scatter plot,Bar charts and pie charts
S-10	SLO-1		Logistic Growth,Programming with Sound Writing Sound to File,Reading Sound from File,	Representing a Function as a Class and manipulation	Random Walk in Two Space Dimensions	Choosing the Length of the DFT
	SLO-2	Tuples,Functions,Lambda Functions,If Tests	Plaving Many Notes	Bank Accounts as class, A Class for	Basic Implementation, visualization and Computing Statistics of the Particle Positions	Filters in Signal Processing
S-11- SL	SLO-1	I sh 2: Programming on list and loops	Lab 6: Sound generated by formula and	Lab 9: Programming on class	Lab 12:Random Walk in One Space	Lab 15: Numpu signal processing
¹² SL	SLO-2	Lab 3: Programming on list and loops	difference equation	Lab 9: Programming on class	Dimension or Two Space Dimensions	Lab 15: Numpy signal processing

Learning	1.	Hans Petter Langtangen," A Primer on Scientific Programming with Python", Springer, 2000.	З.	Juan Nunez-Iglesias, Stéfan van der Walt, and Harriet Dashnow Elegant SciPy Te Art of Scientific Python,
Resources	2.	Christian Hill, "Learning Scientific Programming with Python", Cambridge University Press, 2015.		O'Reilly Media, 2017.

Learning Asse	essment													
	Bloom's		Continuous Learning Assessment (50% weightage)											
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	¥ (10%)#		n (50% weightage)			
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%			
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%			
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%			
	Total	100	0 %	100) %	10	0 %	10	0 %	10	0 %			

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Course Code	18ECE202T	Course Name	Micro- and	Nano-Fabrication Technologies	Course Category	E	Professional Elective	L 3	Т 0	P 0	C 3
Pre-requis Courses		Nil	Co-requisite Courses	Nil	Progressiv Courses	e	Nil				
Course Offer	ring Department	Electro	onics and Communication Enginee	ring Data Book / Codes/Standards			Nil				

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ıg]					Progr	am L	earni	ing O	utcon	nes (F	PLO)				
CLR-1 :	Provide learners a systematic overview of micro and nano fabrication processes	1	2	3	1	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Gain understanding of lithography, etching and ion implantation methods to fabricate, structure and modify the layer	(n	(%)	(%)		е		-						ķ						
CLR-3 :	Understand thin film fabrication techniques including PVD and CVD and to apply the knowledge to film formation	(Bloom)				edg		nent		an an				Work		inance			ues	
CLR-4 :	Apply the knowledge of microfabrication technology to the fields of general microelectronics systems	j) Bi	roficiency	me		owl	sis	udo	Ľ.	sage	ഉ			eam	_	Fina	ing	na	ect Techniques	~
CLR-5 :	Learn the significant advances in molecular engineering	Thinking	rofic	Attainment		g Kn	Analysis	Jevelopme	lesign,	Tool Usage	Cultur	ent & ilitv			ation	∞ŏ	earning	essional	Tect	lyze
CLR-6 :	Embark on building micro/ nano structures applicable to their needs.	Τ	Ъ	-		ering	١An		□ _	Toc	م ە			al &	nice	Mgt		Profe	Proje	Ane (
		el of	Expected	Expected		Engineering Knowledge	Problem	ign	Analysis, Research	Modern	Society	nvironment ustainabilit	S	ndividual	ommunication	Project Mgt.	Long	-1: F	- 2: ager	- 3: arct
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level	Ц Ш Ш	Ě		Eng	Prol	Design	Ana Res	Moc	Soc	Env Sus	Ethics	lndi	g	Proj	Life	PSO Achi	PSC Man	PSC Rese
CLO-1 :	Understand the various layering Technologies	2	80	70	1	Н	-	-	-	-	-	-	-	-	-	-	-	М	-	
CLO-2 :	Realize how the pattern generation is done using Lithography Techniques	2	85	75	1	Н	-	М	-	-	-	-	-	-	-	-	-	М	-	
CLO-3 :	Gain knowledge on particle sources, Optics and Interaction	2	75	70	1	Н	-	М	-	-	-	-	-	-	-	-	-	-	-	Н
CLO-4 :	Learn the device and circuit fabrication Techniques	2	85	80	1	Н		М	-	-	-	-	-	-	-	-	-	-	-	Н
CLO-5 :	Learn about new and advances in fabrication Technologies	2	85	75		Н	-		Н	-	-	-	М	-	-	-	М	-	-	Н
CLO-6 :	Know the limitations and tools of micro, nanofabrication.	2	80	70	1	Н	М	-	-	М	-	-	-	-	-	-	L	М	-	Н

Du	ration	Crystal Growth, Epitaxy, Oxidation	Lithographic Processes	Deposition, Diffusion, Ion implantation	Device Circuit Fabrication	Molecular Nanotechnology
1)	nour)	9	9	9	9	9
S-1	SLO-1	Starting Materials	Photoreactive Materials	Vaccum Evaporation	Isolation	Directed Self Assembly
5-1	SLO-2	Growth from Melt (Czochralski Technique)	Image Reversal	Sputter Deposition	Self Alignment	Device Assembly
	SLO-1 Considerations for Paper Crystal Growth		Pattern Generation	Chemical Vapour Deposition	Local Oxidation-Trench Technique	Electrostatic
S-2	SLO-2	Cystal Orientation, Crystal hardening Techniques	Mask Making	Growth Habit	Planarization	Templated self assembly
S-3	SLO-1	Doping, Dislocation	Pattern Transfer	Fims for protection & Masks	Metalization	Colloids & Nanoparticles
3-3	SLO-2	Molecular Beam Epitaxy	Optical Printing	Self-aligned Masks	Gettering	Block Copolymers
S-4	SLO-1	Gas Source MBE	Advanced Techniques	Films for Doping	NIOS-based Micro Circuits	DNA Nanostructures
5-4	SLO-2	Vapour Phase Epitaxy	Short Wave lengths	Dopart Sources	P,n Channel Transistors	Scanning probe lithography Techniques
S 5-6	SLO-1	VPE Process to Silicon	Multilayer Resists	Films for Ohmic contacts	Complementary Transistors	Local Anodic Oxidation

	SLO-2	VPE Process for GaAs	Phase Shifting Masks	Wet Chemical etching	Memory Devices	Scribing
S-7	SLO-1	Liquid Phase Epitaxy	Electron Beam Techniques	Anisotropic Effects	SOI Devices	Atomic Manipulation
3-1	SLO-2	LPE System	Lon-Beam Techniques	Dry Physical Etching	BJT based Silicon Micro Circuits	SPM Scanning Probe Microscopy
S-8	SLO-1	Thermal Oxidation of Silicon	X Ray Printing	Dry Chemical Etching	The buried layer	Erasable Electrostatic Lithography
3-0	SLO-2	Kinetics of Oxide Growth	Problem areas- defects	Reactive Lon Etching	p-n-p Transistor	Limits to Nano Fabrication
	SLO-1	Oxidation System	Feature size control & anisotropic Etch Mechanism	Penetration range &Transverse effects	Field Effect Transistor	Limits to MSO Devices
S-9	SLO-2	Halogenic Oxidation	Lift off Techniques	Annealing	BICMOS Integrated Circuits	Limits for Pattern Generation
		Anodix Oxidation Plasma Processes	Plasma reactor Relative Plasma etching Technique	lon Implantation systems High energy, high current Inplants	Self Aligned Technology The Hetero junction Bipolar Transistor	Nanofabrication Tools

Learning Resources 1. Sorab. K. Gandhi, "VLSI Fabrication and Principles", McGraw Hill, 2005

- 2. Sami Franssila, "Introduction to Microfabrication", Wiley Publications, 2010
- Richard C.Jaeger, "Introduction to Microelectronic Fabrication", Prentice hall, 2002
 Ivor Brodie & Julius J. Muray, "The Physics of Micro/Nano-Fabrication" Springer, 1992
- 5. Bo Cui, "Recent advances in Nanofabrication Techniques and Applications", InTech Publisher, 2011
- 6. A G Davies and J M T Thompson, "Advances in Nanoengineering Electronics, Materials and
- Assembly", Imperial College Press, 2007
 Michael Pycraft Hughes, "Nanoelectromechanics in Engineering and Biology", by CRC Press LLC, 2003

Learning Asses	ssment										
	Diamaia			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examination	n (50% weightage)
	Bloom's Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#	FINALEXAMINATION	r (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 4	Remember	40%		40%		40%		40%		40%	
Level 1	Understand	40%	-	40%	-	40%	-	40%	-	40%	-
Level 2	Apply	40%		40%		40%		40%		40%	
Level 2	Analyze	40%	-	40%	-	40%	-	40%	-	40%	-
Level 3	Evaluate	20%		20%		20%		20%		20%	
Level 5	Create	20%	-	20%	-	20%	-	20%	-	20%	-
	Total	100) %	10	0 %	10	0 %	10	0 %	100 %	

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Course Code	18ECE203T	Course Name		SEMICONDUC	TOR DEVICE MODELI	NIC-	ourse tegory	,	Е			Profe	essiona	al Ele	ctive					L 3	T P 0 0	C 3
Pre-requ Cours		18ECC102J		Co-requisite Courses		Nil	Co	gress ourse							Ni	I						
Course Of	fering Department	Electro	nics and Comr	munication Engineerii	ng Data Book	/ Codes/Standards	Nil															
Course Le	arning Rationale (CLR): The pur	pose of learnir	ng this course is to:			L	earniı	ng				Prog	ram L	earni	ng O	utcon	nes (P	PLO)			
	Utilize the propertie						1	2	3	1 3	2 3	4	5	6	7	8	9	10	11	12	13 14	4 15
	Utilize the mechani																				rent	
	Utilize the characte											E			ity						rent	LC-
	Utilize the modeling						Ê	(%)	(%	ъ		Research			Sustainability		×				chie gen	sea
	Identify the effects of						300	cy (°	t (eod	nen	ses	a,		stain		Work		nce		al A lana	Å R
	Understand the fun fields.	damental physica	al processes o	f semiconductor devi	ces to meet the challen	ge of these dynamic	Thinking (Bloom)	ficien	Attainment (%)	MOL .	elopr	Design, F	Usagi	ture	& Sus		Team	Б	& Finance	rning	: Professional Achievement 2: Project Management	ques 3: Analyze & Research
L I	nondo.						hink	Pro	Atte	- Bu	Dev	Des	00	Cul			& T	icatio	gt. 8	Lea	Proj	es Ana
	•	. ,		rse, learners will be a	ble to:		Level of 1	Expected Proficiency	Expected /		Design & Development	Analysis,	Modem Tool Usage	Society & Culture	Environment	Ethics	Individual &	Communication	Project Mgt.	Life Long Learning	PSO-1: F PSO - 2:	Techniques PSO – 3: An
				arious applications			2	80	70		1 -	-	-	-	-	-	-	-	-	-		-
	Interpret the charac						3	85	75		- 1	Н	-	-	-	-	-	-	-	-		-
	Modify and model t						3	75	70		- 1	Н	-	-	-	-	-	-	-	-		
	Evaluate and optim			T			3	85	80		- 1	Н	-	-	-	-	-	-	-	-		· H
	Build new devices w						3	85	75	-		Н	-	-	-	-	-	-	-	-		
	Explain the equation device characteristi				ving a model with spec	ified properties, for a genera	/ 3	80	70		- 1	Н	-	-	-	-	-	-	-	-	- -	H
	-					-																
Duration (hour) 9 9 9												9							9			

Durat	ion (hour)	9	9	9	9	9
S-1	SLO-1	Electron, Hole Densities In Equilibrium: Distribution of quantum states in energy band	PN Junction under thermal equilibrium: Built in potential, concept of space charge layer	Current components, Basic BJT parameters,	MOS diode	Scaling of MOSFETS
	SLO-2	Fermi – Dirac Statistics	Problem Solving	Limitations on the junction voltage	Operation of Ideal MOS diode (at VGB >0)	Effect of Gate voltage on carrier mobility
S-2	SLO-1	Electron concentration conduction band	Distribution of electric filed and potential within the space charge layer for abrupt junctions at Zero bias	Capacitances in a BJT,	Operation of ideal MOS diode (at VGB <0)	Effect of Drain voltage on carrier mobility
5-2	SLO-2	Hole concentration Valence band	Distribution of electric filed and potential within the space charge layer for abrupt junctions at Zero bias	Switching of BJT	Operation of ideal MOS diode with and without oxide charge	Effect of Drain voltage on carrier mobility
S-3	SI ()-1	Carrier concentration in intrinsic semiconductors	Distribution of electric filed and potential within the space charge layer for linearly graded junctions at Zero bias	Ebers-Moll model	Effects of mobile lonic charges	Channel length modulation
5-5	SLO-2	Position of Fermi level in extrinsic semiconductors	Distribution of electric filed and potential within the space charge layer for linearly graded junctions at Zero bias	Problem Solving	Problem Solving	Breakdown and punch through
S-4		lonization of impurities, Equilibrium electron and hole concentration	PN Junction under applied bias: Depletion layer capacitance in an abrupt PN junctions	Early effect (CB & CE)	Oxide charges and Interface states	Sub threshold current
	SLO-2	Problem Solving	Problem Solving	Operation of BJT at high frequencies: Charge control model	C-V Characteristics	Sub threshold current

S-5	SLO-1	Fermi level at thermal equilibrium	Depletion layer capacitance with arbitrary doping profiles	Small signal equivalent circuit,	Problem Solving	Short channel effects
5-0	SLO-2	Problem Solving	Static current voltage characteristics of PN junction,	Problem Solving	Threshold voltage of MOSFET	Short channel effects
S-6	SLO-1	Excess Carriers: Generation and recombination of carriers	Current-voltage relationship in an infinitely long diode,	Design of high frequency transistors	Bulk charge model	Meyer's model
3-0	SLO-2	Mobility of carriers	Quasi Fermi level under bias condition	Problem Solving	Problem Solving	Small signal model
S-7	SLO-1	Charge transport in semiconductors: Drift current	Current –voltage relation in practical diodes having finite lengths	Second order effects in BJT: Non-uniform doping in the base	square law method (Level 1 in SPICE	MOSFET scaling
0-1	SLO-2	Hall effect	Ideality factor	Non-uniform doping in the base	square law method (Level 1 in SPICE	Non-uniform doping in channel
S-8	SLO-1	Diffusion current	Transient analysis: Time variation of stored charge	Variation of β with collector current	Level 3 model in SPICE	SOI MOSFET
0-0	SLO-2	Problem Solving	Problem Solving	High injection in collector	BSIM Models	SOI MOSFET
S-9	SLO-1	Current density equations	Reverse recovery of a diode, charge storage capacitance	Heavy doping effects in the emitter	Comparison of Models	Buried channel MOSFET
0-9	SLO-2	Current density equations	Problem Solving	emitter crowding in bipolar transistors	Comparison of Models	Fin FET
Loarni	20	1. Nandita Das Gupta, Amitava Das Gup	ota, Semiconductor devices, modeling and Te	chnology, Prentice Hall of 3. S.M. Sz	e, Semiconductor Devices-Physics and Tech	nology, John Wiley and Sons, 1985.

Learning	1.	Nandita Das Gupta, Amitava Das Gupta, Semiconductor devices, modeling and Technology, Prentice Hall of	3.	S.M. Sze, Semiconductor Devices-Physics and Technology, John Wiley and Sons, 1985.	
Learning		India, 2004	4.	Kiat Seng Yeo, Samir R.Rofail, Wang-Ling Gob, CMOS/BiCMOS VLSI-Low Voltage, Low Powe	₽r,
Resources	2.	Philip. E. Allen Douglas, R. Hoberg, CMOS Analog circuit Design, 2nd ed., Oxford Press, 2002		Pearson 2003	

Learning Asse	essment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weigl	ntage)			Final Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4	(10%)#		r (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	30 %		30 %		30 %		30 %		30%	
Level I	Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %		40 %		40 %		40%	
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate	30 %		30 %		30 %		30 %		30%	
Level 5	Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100) %	100) %	100) %	10	0 %

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Course	18ECE204J	Course		ARM-based	Embedded System Design				Cou			E		Profe	ssiona	l Electi	VA	L	Т	F	, C
Code	102022040	Name		Altin-buscu	Embedded Gystein Design				Cate	ory		-		1010	5510110			2	0	2	2 3
Pre-requ Cours		18ECC203J		Co-requisite Courses	Nil						gressi ourses					18EC	E305J	, 18EC	CE306J		
Course Of	fering Department	Electro	onics and Communi	cation Engineering	Data Book / Codes/	Standa	ards									Nil					
Course Le	arning Rationale (CLR):	The purpose of	learning this cours	e is to:		L	earn	ing					Progra	ım Le	earnin	g Outo	omes	(PLO))		
CLR-1 :	Understand fast software de	evelopment tools	s of ARM processo			1	2	3	1	2	3	4	5	6	7	8 9	10	11	12	13	14 15
	Understand I/O programmir											ſ			ty					4	Irch
	Understand the usage of tir					Ê	(%	(%	æ			Research			Sustainability	÷				3	r ruject management ss Analyze & Research
	Know effective use of mem			et and wireless protoc	col supports	(Bloom)	Proficiency (%)	Attainment (%)	eda		Development	Sese	m		stain	Toam Mork		Finance	-	a	S R
	Make application for audio					je pe	cien	ame	Mot	SI2.	lopr	л, Г	sag	e		E		Fina	ing .	SIO	Ze ?
CLR-6 :	Develop ARM Cortex-M bas	sed embedded s	systems for network	ing and signal proces	sing applications.	Thinking	rofi	vttaii	, A	, aly	eve	esic	ol C	Iff	nt &	E E	ation	∞ŏ	earr	Professional ment	nal)
Course Le	earning Outcomes (CLO):	At the end of th	nis course, learners	will be able to:		Level of Th		Expected	Enaineerina Knowledae	Problem Analysis	Design & D	Analysis, Design,	Modern Tool Usage	Society & Culture	Environment &	Ethics Individual &	Communication	Project Mgt.	ů,	PSO-1: Professional Achievement	Techniques PSO – 3: An
CLO-1 :	Learns about "mbed" softwa	are and C langua	age application for J	ARM Cortex-M proces	sors.	2	80	70	L											Н	
CLO-2 :	Learns using mbed softwar					2	80	70	L	М	М		М							Н	
	Learns to program System				D display.	2	80	70												Н	
	Learns to use memory effect	, , ,		<i>се.</i>		3	80	60		М	М								\square	М	
	Learns to do audio signal p					3	80	60		М	М	М	М						\square	М	
CLO-6 :	Use of "mbed" software pac	ck on ARM Corte	ex-M processor for I	networking and simple	signal processing.	3	80	60	L	М	М	М	М							М	

Duratio	on (hour)	Learning Unit / Module 1 Cortex-M processor	Learning Unit / Module 2 Peripheral Interfacing-I	Learning Unit / Module 3 Peripheral Interfacing-II	Learning Unit / Module 4 Network Interfacing	Learning Unit / Module 5 Audio Signal Processing
Duraut	n (nour)	15	15	15	15	15
S-1	SLO-1	Introducing embedded systems and mbed	Starting to Program Digital Input and Output	Introducing Synchronous Serial Communication	Memory organization	An Introduction to Digital Audio
3-1	SLO-2	Introducing embedded systems and mbed	Voltages as Logic Values	I2C bus	Memory organization	USB MIDI on the mbed
S-2	SLO-1	ARM Cortex assembly language basics.	Introducing Analog output Data Conversion	Communicating With I2C-Enabled Sensors	Using Data Files With the mbed	Digital Audio Processing
5-2	SLO-2	ARM Cortex assembly language basics.	Digital Output on the mbed	Asynchronous Serial Data Communication	Example mbed Data File Access	Digital Audio Filtering Example
S	SLO-1	Lab-1:Assembly language program,	Lab 4: A/D conversion program	Lab 8: Multinode I2C Bus	Lab 10: Data logging	Lab 13: Audio signal generation
3-4	SLO-2	simulation -1	Lab 4. A/D conversion program	Lab 6. Mulunode 120 Bus	Lab IV. Data logging	Lab 13. Audio signal generation
S-5	SLO-1	Cortex-M processor architecture and Basics : Programming exercises	Digital Input and Output.	LCD interfacing	Using External SD Card Memory With the mbed	Delay/Echo Effect
3-5	SLO-2	Cortex-M processor architecture and Basics : Programming exercises	Digital Input and Output.	Using the mbed TextLCD Library	Using External USB Flash Memory With the mbed	Working With Wave Audio Files

S-6	SLO-1	Development Environment using the mbed	Switching Larger DC Loads	Time and Tasks in Embedded Systems	Introduction to Internet Communication	High-Fidelity Digital Audio With the mbed
3-0	SLO-2	Development Environment using the mbed	Switching Larger DC Loads	Responding to External Events	The Ethernet Communication Protocol	High-Fidelity Digital Audio With the mbed
S	SLO-1	Lab 2: Assembly language	Lab 5: Mini Project: Letter Counter	Lab 8: A/D output on LCD	Lab 11: Ethernet communication	Lab 14: Model lab examination
7-8	SLO-2	program, simulation-2	Lab 5. mini roject. Letter Gounter			
S-9	SLO-1	Keil IDE and Debugging tools	Another Form of Analog Output: Pulse Width Modulation	An Introduction to Timers	Introducing Wireless Data Communication	Summary on Digital Audio and Digital Signal Processing
3-5	SLO-2	Keil IDE and Debugging tools	Pulse Width Modulation on the mbed	Using the mbed Timer	Wireless Data Communication : Bluetooth and Zigbee	Summary on Digital Audio and Digital Signal Processing
S-10	SLO-1	C- language review	Design of PWM problem	Using the mbed Timeout and Ticker	Local Area Network Communications With the mbed	Review and discussions
5-10	SLO-2	Embedded C, introduction	Design of PWM problem	The Real-Time Clock	Using RPC	Review and discussions
s	SLO-1	Lab 3: Parallel port programming,	Lab 6: PWM waveform generation	Lab 9: ExperimentingInterrupts,	Lab 12: RPC communication through	Lab 15: Final lab examination
11-12	SLO-2	simulation	Lab o: P vvin wavelorm generation	Timers	ethernet	

	1.	Tim Wilmshurst, "Fast and effective embedded system design, Applying the ARM mbed", ARM	3.	Theory/Lab teaching materials, "Efficient embedded system design kit", ARM
Learning Resources	2	Education Media, 2018. Andrew Sloss, Dominic Symes, Chris Wright, "ARM System Designers Guide: Designing and optimizing		Education media.
	i	System Software", The Morgan Kaufmann Series in Computer Architecture and Design, 2004.		

Learning As	sessment										
	Bloom's			Conti	nuous Learning Asse	essment (50% weig	htage)			Final Examinatio	n (50% weightage)
	Level of	CLA – 1	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		
	Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level I	Understand	20%	20%	15%	13%	13%	15%	10%	15%	13%	13%
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 2	Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
1	Evaluate	400/	400/	450/	450/	450/	450/	450/	450/	450/	450/
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Total 100 % 100 % 100 % -											

Course Designers			
Experts from Industry	Experts from Higher Technical Institutions		Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annau	<u>ıniv.edu</u>	1.Mr. Nivash. S, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in		2. Prof. V. Natarajan, SRMIST

Course Code	1	18ECE205J	Course Name		FPGA-based E	Embedded	System Design	-	ourse tegory	1	Е				Ρ	rofes	siona	l Elec	ctive					L 2	T 0		C 3
Pre-requ Cours		11	BECC203	I	Co-requisite Courses		Nil			gress ourse									Ni	il							
Course Of	ffering [Department	E	ectronics and C	ommunication Engin	neering	Data Book / Codes/Stand	ards			·	Nil															
Course Le	earning	Rationale (CLR):	The pu	rpose of learnin	g this course is to:				L	earni	ng					P	Progra	am L	earni	ing O	utcor	nes (PLO)				
CLR-1 :	Know w	vhy many high vol	ume embe	dded systems r	need to be function s	pecific			1	2	3	_	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Design	circuits using FPC	GA knowle	dge of using its	resources like, clock	k, cells, dei	vice modules, etc.																		ut		
CLR-3 :	study X	(ilinx FPGA IDE ar	nd design	practice					Ē		(_			Å						veme	ent	5
CLR-4 :	Unders	tand platform FPG	GAs						loor	%) %	nt (%)		е		Ŧ	Research			Sustainability		¥				\chie	Project Management	Research
CLR-5 :	Unders	tand FPGA syster	n design a	nd practical issu	ues				B B	cienc	mer		vledg		men	Rese	ge		ustain		IN VOI		ance	b	onal ⊿	Mana	& R
CLR-6 :	Develop	p designs using Fl	PGAs/PSc	Cs for specific e	embedded modules	and low-po	ower designs		inkir	Profic	Attainment		Knov	alysis	& Development	sign,	Usa	Culture			& Team Work	ion	& Fir	amin	essic	oject	alyze
Course Le	earning	Outcomes (CLO)	: At the	end of this cours	se, learners will be a	able to:			Level of Thinking (Bloom)	Expected Proficiency (%)	Expected /		Engineering Knowledge	Problem Analysis	Design & De	Analysis, Design,	Modern Tool Usage	Society & Cu	Environment &	Ethics	Individual &	Communication	Project Mgt. & Finance	Life Long Leaming	PSO-1: Professional Achievement	- 2: nique	PSO – 3: Analyze &
CLO-1 :	Explain	Micro controller s	ubsystem	S					2	80	70		М												Н		
CLO-2 :	Make s	ystem design with	n PSoCs						3	80	70		L	Н	Н		М								Н		
CLO-3 :	Unders	tanding of Platforr	n FPGAs						2	80	70														Н		
CLO-4 :	Unders	tanding of FPGA a	architectur	e design					3	80	60			Н	Н										Н		
CLO-5 :	Designi	ing with Platform F	PGAs (ex	amples)					3	80	60		Τ	Н	Н	М	М								Н		
CLO-6 :	Design	simple FPGA bas	ed syster	IS					3	80	60		L	Н	Н	М	М								Н		

Durati	on (hour)	Learning Unit / Module 1 Basics of Peripherals	Learning Unit / Module 2 PSoC Design	Learning Unit / Module 3 Xilinx Virtex 5 IDE	Learning Unit / Module 4 Platform FPGA Designs	Learning Unit / Module 5 Designing Simple FPGA based Systems
		15	15	15	15	15
S-1	SLO-1	Embedded systems performance criteria - Interrupts	PSoC3/5 architecture overview	Design challenges, life cycle	Design quality: correctness, reliability, resilience.	Communication: Coprocessor model
5-1	SLO-2	Embedded systems performance criteria - Interrupts	PSoC3 architecture details and 8051 instructions	Metrics: measures of success	Modules and interfaces	Network on chip model
S-2	SLO-1	Embedded systems performance criteria - DMA	Interrupts and interrupt lines	Spectrometer example using Xilinx IDE	Abstraction and state,	Transfer of state
3-2	SLO-2	Latency and its problems	Interrupt priority and nesting	Spectrometer example using Xilinx IDE	Cohesion and coupling and control flow graph	Practical issues: profiling issues
S	SLO-1	Lab 1: Embedded sensors and	Lah A DeaC Dealing A	Lab 7: VHDL, Verilog Practice	Lab 10: Sample design	Lab 13: On-chip memory access,
3-4	SLO-2	sensing -1	Lab 4: PSoC Design -1	session -1	implementation	FIFOs
S-5	SLO-1	Embedded system subsystems: A/D conversion	The concept of memory and its connectivity to CPU	Xilinx Virtex 5 IDE	Origin of Platform FPGA Designs	Spatial design: Principles of parallelism
0-0	SLO-2	Digital ports & its current capacity	Different DMA modes	Xilinx Virtex 5 IDE	Platform FPGA components	Granularity, degree of parallelism

S-6	SLO-1	Introduction to other digital interfaces	Clocking system: Internal master oscillator	PLD basics	Adding to platform FPGA systems	Spatial organizations
3-0	SLO-2	Introduction to other digital interfaces	IMO, and sleep/wake up modes	FPGA configurations	Assembling custom compute cores	Spatial organizations
S	SLO-1	Lab 2: Embedded sensors and	Lab 5: PSoC Design -2	Lab 8: VHDL, Verilog Practice	Lab 11: Building base systems	Lab 14: Model lab examination
7-8	SLO-2	sensing - 2	245 617 666 266igir 2	session -2	Las III Ballang sadd dydallo	
S-9	SLO-1	Sensors and sensing principles. Optical, capacitive sensors	Clock distribution	Various slices in Virtex 5	Software design :root file system, cross- developmental tools	Managing bandwidth: Balancing
00	SLO-2	Magnetic, RF sensors	Power management: Internal regulators	Various slices in Virtex 5	Monitors and boot loaders	Khan process network
0.40	SLO-1	Processing: Mathematical views.	Types of reset	Bit stream	Overview of partitioning platform	Platform FPGA bandwidth techniques
S-10	SLO-2	Programmable logic and mixed signal design fundamentals	Intro to PSoC creator IDE	Programming FPGA	Analytical solution to partitioning	On-chip, off-chip memory
S	SLO-1	Lab 3: Programmable logic		Lab 9: Sample design		
11-12	SLO-2	design	Lab 6: PSoC Design - 3	implementation	Lab 12: Creating IP core	Lab 15: Final lab examination

Learning	1.	. Robert Ashby, "Designers guide to the Cypress PSoC", Cypress Semiconductors, 2005.	3.	Sass and Shmidt, "Embedded system design with Platform FPGAs", Morgan Kaufmann, 2010.	
Resources	2	2. Edward H. Currie and David Van Ess, " PSoC3/5 Reference Book", Cypress Semiconductor, 2010.	4.	Theory/Lab Session Teaching Materials, ARM Educational Media.	

Learning Ass	sessment										
	Disemial availat			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examinatio	- (EOO/
	Bloom'sLevel of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#	Final Examinatio	n (50% weightage)
	ininking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	100) %) %		-					

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Prof. V. Natarajan, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@ici.com</u>	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2.

Course Code	18ECE206J	Course Name	ADVANCED D	DIGITAL SYSTEM DESIG	GN	Course Categor		Е				Profe	ssiona	al Elec	ctive					L	•	P C 2 3
Pre-requ Cours		18ECC103J	Co-requisite Courses		Nil		ogres: Course								Ni	1						
Course Of	fering Department	Electro	nics and Communication Engineer	ing Data Book	/ Codes/Standards									Nil								
Course Le	arning Rationale (CL	R): The pur	pose of learning this course is to:			I	earni	ng					Progr	am L	earni	ng O	utcon	nes (P	LO)			
CLR-1 :	Understand advanced	Boolean theo	rems for logic simplification and im	plementation		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15
			or the analysis and design of synch																			
ULK-J.	sequential logic circuit	's using them.	le Devices (PROM, PLA, PAL, CF												£.						vement	ent Ich
	Adopt systematic appl and systems	roach with the	use of ASM chart ASMD chart, R1	TL representation for the	design of digital circuits	Thinking (Bloom)	(%)/	t (%)	ac	2	ent	Research			Sustainability		/ork		е		I Achie	2: Project Management jues 3: Analyze & Research
CLR-5 :	Use VHDL as a design	n-entry langua	ge for FPGA in electronic design a	automation of digital circu	uits	BI (BI	ency	nen		s	ŭ	Å	age	Ð	Sust		× ¤		Finance	g	iona	t Ma
			ts for more advanced design proje			lking	ofici	Attainment	kno	aly si	velo	Design, I	I Usi	Culture	۰ð		Team Work	lion	∞ŏ	arni	ess	ojec
Course Le	arning Outcomes (C	L O): At the e	and of this course, learners will be	able to:		Level of Thir	je j	Expected	Encineering Knowledge		Design & Development	Analysis, De	Modem Tool Usage	Society & Ct	Environment	Ethics	Individual &	Communication	Project Mgt.	Life Long Learning	PSO-1: Professional Achievement	PSU – 2: Project N Techniques PSO – 3: Analyze
CLO-1:	Apply advanced theor	ems to simplify	the design aspects of various practice	actical circuits		3	80	75	N	1												
	Analyze and design sy					3	80	70		М	М											
			ign Asynchronous sequential circu	iits		3	75	70		М	М											
CLO-4 :	Implement various dig	ital circuits usi	ing Programmable Logic Devices			3	80	75		М	М											
	Design and implemen					3	80	70		Н	Н	Н	Н			L	Н	М				L
CLO-6 :	Perform experiments	in the laborato	ry with hardware and as well with s	software (VHDL) to simu	late and verify the desig	n 3	80	70												Н	Н	L
_					1																	

Durati	on (hour)	12	12	12	12	12
S-1	SLO-1	Shannon's Expansion theorem	state reduction	Analyze asynchronous sequential circuit	Dynamic hazards	Xilinx 3000 series FPGA
5-1	SLO-2	Shannon's Expansion theorem application	state reduction	flow table reduction	Essential hazards	Xilinx 3000 series FPGA
S-2	SLO-1 Shannon's Expansion theorem and its application state assignment			races-state assignment	Programming logic device families	Xilinx 4000 series FPGA
5-2	SLO-2	Consensus theorem	state assignment	Variables Signals, Constants, Sequential statements VHDL processes	Designing synchronous sequential circuit using PROM	Xilinx 4000 series FPGA
S 3-4		Lab 1: Implement six-variable function using four-variable function generators	Lab 4: Implement hazard-free circuits	Lab 7: VHDL Programming Practice	Lab 10: Construct multiplexers, de- multiplexers in VHDL	Lab13: Implement BCD adder, comparator in VHDL
S-5	SLO-1	Reed-Muller Expansion technique	Design of synchronous sequential circuits	races-state assignment	Designing synchronous sequential circuit using PROM	Design of sequential circuits (using VHDL)
3-5	SLO-2	Reed-Muller Expansion technique	Design of synchronous sequential circuits	Transition table and problems in transition table	Designing synchronous sequential circuit using PROM	Design of sequential circuits (using VHDL)
S-6	SLO-1	Multiplayar loaic as function apparators	Introduction to VHDL, Entity and Architecture description	Transition table and problems in transition table	Programmable Array Logic (PAL)	Design of sequential circuits (using VHDL)
3-0	SLO-2	Implementation of Multiple output logic functions	VHDL Data types and Operators	Design of asynchronous sequential circuit	Programmable Array Logic (PAL)	Design of sequential circuits (using VHDL)
S 7-8		Lab 2: Implement Reed-Muller expressions using logic gates.		0 0	Lab 11: Construct code converters, 4-bit binary adders in VHDL	Lab 14: Mini Project Work

S-9	SLO-1	Mealy and Moore machines	ASM chart and realization using ASM	Design of asynchronous sequential circuit	Programmable Logic Array (PLA)	Additional circuit designs using VHDL
2-9	SLO-2	Clocked synchronous sequential circuit design procedure	ASM chart and realization using ASM	Design of asynchronous sequential circuit	Programmable Logic Array (PLA)	Additional circuit designs using VHDL
S-10	SLO-1	State diagrams	Concurrent, Sequential Assignment Statements, Types of Modeling in VHDL	Static hazards	FPGA-Xilinx FPGA	Additional system designs using VHDL
5-10	SLO-2	State table	Behavioral, dataflow and structural modeling	Static hazards	FPGA-Xilinx FPGA	Additional system designs using VHDL
S 11-12		Lab 3: Implementation of Sequence detector circuit.	Lab 6: VHDL Programming Practice		Sequential circuits (lising VHDL)	Lab 15: End-Semester Practical Examinations
Learni Resou	•	ed., Cengage Learning, 2012	of Texas at Austin. Larry L. Kinney, Fundar e, Fundamentals of digital and computer des	4. Charles.	, Bhasker, A VHDL Primer, 3 rd ed., Prentice F H. Roth, Jr, Digital Systems Design using VI Iano M, Michael D. Ciletti, Digital Design with , 2014	HDL, CENGAGE Learning, 2010

Learning Assess	ment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Einal Examination	n (50% weightage)
	Level of Thinking	CLA – 1	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#		r (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level I	Understand	20%	20%	13%	13%	13%	13%	13%	13%	13%	13%
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level Z	Analyze	2070	2076	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Level 5	Create	10%	10%	10%	15%	15%	15%	10%	13%	13%	13%
	Total	100) %	100	0 %	100	0 %	100) %		-

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. B. Viswanathan, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2.

Course	195052071	Course	Pool Time Operating Systems	Course	F	Professional Elective	L	Т	Ρ	С
Code	18ECE207J	Name	Real Time Operating Systems	Category	E	Professional Elective	2	0	2	3

Pre-requisite Courses		18CSS101J	Co-requisite Courses		Nil	Progressive Courses	Nil
Course Offering I	Department	Electronics and Comm	nunication Enginee	ring	Data Book / Codes/Standards		Nil

Course Le	Course Learning Rationale (CLR): The purpose of learning this course is to:			L	earni	ng	Program Learning Outcomes (PLO)													
CLR-1 :	Develop application program	n in ARM based hardware, we need to know, C and assembly programming and IDE.		1	2	3	1	2 3	3 4	5	6	7	8	9	10	11	12	13	14 15	1
CLR-2 :	Interface pheripherals, and	hence gain the knowledge of programming, need to be known.		(c	()	(~						to of	ch ent	1
CLR-3 :	Study RTOS principles			loc	(%)	(%)			4			bilit						80	en	
CLR-4 :	Study RTOS principles of va	arious types		B	nc)	ent	dge	1	Research			aina		Work		e	_	_	Management & Research	
CLR-5 :	Develop application program	nming of sample projects		ing	icie	inm	a la	s	a a	ade	ω	Sustainability		≥ E		Finance	ĝ	SSIUIIAI	e &	
CLR-6 :	Study and understand, how	OS on ARM processor can be implemented and used.		Thinking (Bloom)	Proficiency	Attainment	Y Y	Analysis	Design Rese	n SU	Culture	~ð		Team	ы.	δ	Leaming		Project N es Analyze	
					P P		ing .		α Πα	Tool Usage	د م	nent		∞	icatior	Mgt.	Le			
Course Le	earning Outcomes (CLO):	At the end of this course, learners will be able to:		Level of	Expected	Expected	Engineering Knowledge		Analvsis	Modern 7	Society 8	Environment	Ethics	Individual	Commur	Project N	Life Long		PSO – 2: Techniqu PSO – 3:	
CLO-1:	Read and understand many	microprocessor instruction sets and their use.		2	80	50	M											Н		1
CLO-2 :	Implement and write code in	n assembly and C for embedded applications.		3	99	70		H F	1	М				М				Н		1
CLO-3 :	Understand the concepts an	nd requirements of RTOS, in general basic OS principles.		2	80	70				М								Н		1
CLO-4 :	CLO-4: Implement and use RTOS for embedded programs				90	70		ŀ	H M	М				М				Н		1
CLO-5 :	CLO-5: Apply the knowledge in related sample use cases.				90	85	L	L		L									М	
CLO-6 :	:LO-6 : Design processor based embedded systems along with OS implementation. (Specifically RTOS)				90	70	LI	Иŀ	H M	М				М				Н	L	

Durati	on (hour)	Learning Unit / Module 1 Cortex-M processor & 'C'	Learning Unit / Module 2 Peripheral Programming in 'C'	Learning Unit / Module 3 Concepts of RTOS	Learning Unit / Module 4 RTOS Implementation	Learning Unit / Module 5 RTOS Applications
		15	15	15	15	15
S-1	SLO-1	Cortex-M processor architecture	Parallel I/O programming	Introduction to RTOS	Process management	Real time systems: Data acquisition system
5-1	SLO-2	Cortex-M processor architecture	Sample programs	Introduction to RTOS	Dynamic linking and loading	Real time systems: Data acquisition system
S-2	SLO-1	ARM Cortex assembly language – part1	Interrupt processing basics	Concurrent programming	Spin-lock semaphore, cooperative scheduling	Performance metrics
5-2	SLO-2	ARM Cortex assembly language – part2	System tick; periodic interrupts	Thread fundamentals	Thread rendezvous	Examples and discussions
S	SLO-1	Lab 1: Arm Assembly language	Lab 4: Interrupts and timers in C and	Lab 7: Simple thread programming in	Lab 10: Semaphore implementation	Lab 13: Any application program
3-4	SLO-2	programming	assembly	RTOS – Wave form simulation	experiment in RTOS	using RTOS.
S-5	SLO-1	ARM Cortex microcontroller interface standards	UART programming	Shared resources and Critical sections	FIFO & Little's theorem	Solid state disk
3-5	SLO-2	IDE software tools	UART programming	Consumer producer problem	Three semaphore implementation	Flash device driver
S-6	SLO-1	Pointers in C	Digital signal time measurement	Switching threads	Thread sleeping	SD card interface

	SLO-2	Arrays, structures and unions, Linked lists	Use of timers and compare, capture registers.	Profiling the OS	Deadlocks, monitors	Communication systems with Ethernet
S 7-8	SLO-1 SLO-2	Lab 2: C & assembly programming using Keil IDE and kit	Lab 5: Debugging hardware with target board – UART interface programming	Lab 8: Multi threaded application in RTOS – LED blinking with multi threads	Lab 11: Multi threaded application with communication -1	Lab 14: Model lab examination
S-9	SLO-1	Embedded debugging tools in Keil IDE	SSI interface	Semaphores and implementation	Fixed scheduling	Application layer protocols for embedded systems
5-9	SLO-2	Embedded debugging example with simulation	SSI programming with interrupt	Operations on semaphores	Fixed scheduling	CoAP, MQTT
S-10	SLO-1	Memory management -1	Analog I/O; A/D converter interfacing	Resource sharing	Kahn process networks	Discussions & Reviews
5-10	SLO-2	Memory management -2	OS considerations of I/O devices	Thread communications	Review	Discussions & Reviews
S	SLO-1	Lab 3: Practice: C & assembly	Lab 6: Debugging hardware with	Lab 9 : Multi threaded application in	Lab 12: Multi threaded application	
11-12	SLO-2	programming using Keil IDE and kit	target board – Analog I/O programming	RTOS, with semaphores	with communication -2	Lab 15: Final Lab Examination

			3.	Quing Li, "Real time techniques for embedded systems", CMP Books, 2003.
	1.	Jonathan Valvano, "Real time operating systems for ARM Cortex-M Microcontrollers, Embedded		
Learning Resources		systems - Volume 3", ARM Educational Media, 2017.	4.	K.C. Wang, "Embedded and Real time operating systems", Springer, 2017.
Resources	2.	Andrew Slosset all, "ARM system developers guide", Elsevier, 2004.		
			5.	Theory/Lab Session teaching materials, " RTOS kit", ARM Educational media

Learning Asse	essment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examinatio	n (EO9/ weightens)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA – 3 (15%)		CLA – 4	l (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	10	0 %	10	0 %	10	0 %	100) %		-

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Prof. V. Natarajan, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2.

Course Code	18ECE301J	Course Name	СМС	S ANALOG IC DESIGN		ourse		•			Pro	ofessic	onal E	lectiv	е			L 2	T 0	P 2	C 3
Pre-requ Cours		18ECC206J	Co-requisite Courses	Nil		gress ourse								Ni	il	tcomes (PLO) 9 10 11 12 13 amining tessional tessional					
Course Of	fering Department	Ele	ectronics and Communication Engin	Data Book / Codes/Standards									Nil								
	arning Rationale (CL		rpose of learning this course is to:			earnir						. .	am L	earni	<u> </u>						
	Identify Analog IC De Analyze the operation		ow and IC biasing y response of CMOS single stage a	mplifiers		2	3		2	3	4	5	6	7	8	9	10 1'	12	13	14 ਸ਼ੁ	15 ទ
CLR-3 :		d frequency re	sponse of the Differential amplifiers		Thinking (Bloom)	cy (%	nt (%)		D.	Ħ	Research			Sustainability		¥	a	,		Project Management	Research
CLR-5 :			d switched capacitor circuits		ting (F	ficien	Attainment		inaliwo si	pmer		age	e	Sustai		m Wc	- Cueri	<u> </u>	ional	t Man	ze & R
CLR-6 :						d Pro	d Atta	:	Analys	Devel	Desig	ool Us	& Culture				cation	ਗ ਹ	Profess	Projectes	Analyze &
Course Le	arning Outcomes (C	LO): At the e	end of this course, learners will be a	ble to:	Level of	Expected Proficiency (%)	Expected ,		Englineening Milowleuge Problem Analysis	Design & Development	Analysis, Design,	Modern Tool Usage	Society &	Environment &	Ethics	Individual &	Communi Pmiact M	Life Long	PSO-1: F	PSO – 2: PI	PSO – 3:
CLO-1 :	Identify IC Biasing col	ncepts			2	80	70		I H	-	-	Н	-	-	-	-		-	-	-	-
	Analyze Single stage				2	85	75			-	-	Н	-	-	-	-		-	-	-	-
	Analyze Differential A		Op-amp		2	75	70			-	Н	Н	-	-	-	-		-	-	-	-
	Identify the noises in J				2	85	80				-	Н	-	-	-	-		-	-	-	-
	Identify oscillators an	d switched ca	pacitors circuits		2	85	75		1 -	Н	-	Н	-	-	-	-		-	-	-	-
CLO-6:																					

Du	ration					
(h	iour)	12	12	12	12	12
S-1	SLO-1	IC Design Philosophy : Introduction to MOSFET scaling	CMOS Single stage Amplifiers: Analog Design Octagon	Differential Amplifier: MOS Differential Pair- Operation with Common mode input	Noise in Amplifiers: Statistical characteristics of noise	Oscillators: General Considerations
3-1	SLO-2	Analog IC design process flow, Typical values of IC MOSFET parameters	Common Source stage with resistive load	Operation with differential input	Statistical characteristics of noise-contn	Ring oscillators
S-2	SLO-1	IC Biasing: MOSFET current source	CS stage with diode connected load	Small signal operation of MOS differential pair- Differential gain	Types of Noises- Thermal Noise, flicker noise	LC oscillators
3-2	SLO-2	Effect of the output resistance of the current source load	CS stage with current source load	Common mode gain, CMRR	Noise Model- MOSFET, Resistor	Cross coupled oscillators
S	SLO-1		Lab 4: Common source amplifier with	Lab 7: Differential amplifier	Lab 10: Noise analysis and a measure of	Lab 13: Switched capacitor circuits
3-4	SLO-2	switch & Inverter using HSPICE	resistive load and diode connect load		noise figure in CS, CG and CD amplifier	
S-5	SLO-1	Basic MOSFET current mirror-operation	CS stage with triode load	Differential amplifier with current source load	Representation of noise in circuits	One port oscillators
3-0	SLO-2	Study on the effects which deviates performance of the current mirror	CS stage with source degeneration	Cascode Differential amplifier	Representation of noise in circuits-Contn	Colpitt oscillator
S-6	SLO-1	Cascode Current mirror	Source Follower	Frequency response of the differential amplifier	Noise Analysis of CS stage	Voltage Controlled oscillators
3-0	SLO-2	Cascode Current mirror- contn. and problem solving	Common gate stage	Frequency response of the differential amplifier- contn	Noise Analysis of CD stage	Voltage Controlled oscillators-contn

S 7-8		Lab 2: Basic MOS current mirror, Current mirror circuit to overcome the channel length modulation effect	Lab 5: Common gate amplifier and Source follower	Lab 8: One stage op-amp		Lab 14: Pre and Post layout simulation of CMOS inverter using Cadence EDA (Virtuoso tool)
S-9	SLO-1	Wilson MOS current mirror		Multistage Amplifiers: Performance parameters of Op-Amp	Noise Analysis of CG stage	Switched Capacitors circuits: Basic principles
3-9	SLO-2	MOS current steering circuits	Folded Cascode amplifier	One stage op-amp	Noise Analysis of Cascode stage	Sampling switches
	SLO-1	Band gap reference circuits	Frequency response of CS amplifier	Two stage op-amp	Noise Analysis of Differential amplifier	Switched capacitor amplifier
S-10	SLO-2	Band gap reference circuits-contn.	Frequency response of CS amplifier - Contn	Two stage op-amp with gain boosting	Noise Bandwidth, Noise Figure Concepts	Switched capacitor integrator
S	SLO-1	Lab 3: Cascode current mirror, Wilson	Lab 6: Cascode amplifier	Lab 9: Two stage op-amp		Lab 15: Pre and Post layout simulation of CMOS Amplifier using Cadence EDA
11-12	SLO-2	current mirror				(Virtuoso tool)

Learning Resources

 Adel S. Sedra, Kenneth C.Smith, "Microelectronic Circuits-Theory and Applications "– 6th Edition, Oxford University Press, 2011.
 Behzad Razavi, "Design of Analog CMOS Integrated Circuits", Mc Graw Hill, 2001 Allen Holberg, "CMOS Analog Circuit Design", Oxford University Press, 2004
 Gray, Meyer, Lewis, Hurst, "Analysis and Design of Analog Integrated Circuits", 4th edition, Willey International, 2002.

Learning Asse	essment											
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Einal Examinatio	n (50% weightage)	
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		ii (50 % weiginage)	
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	10	0 %	10	0 %	10	0 %	10	0 %	100 %		

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Course	18ECE302T	Course	М	EMS TECHNOLOGIES	Course	Е	PROFESSIONAL ELECTIVE	L	Т	Ρ	С
Code		Name			Category			3	0	0	3
Pre-requisi Courses		Nil	Co-requisite Courses	Nil	Progressiv Courses		Nil				
Course Offer	ing Department	Electro	nics and Communication Enginee	Pring Data Book / Codes/Standards			Nil				

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng]					Prog	ram L	earn	ing O)utcoi	mes (PLO)			-
CLR-1 :	Identify the characteristics and various technology adopted in MEMS fabrication	1	2	3	1	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15
CLR-2 :	Understand the electrical and mechanical phenomenon used in MEMS design				1														
CLR-3 :	Analyze how to apply electrostatic and thermal principles in MEMS components design	1																ent	
CLR-4 :	Study the application of piezoresistive, piezoelectric principle and the design of microfluidic devices	Ê										2						/em	g er
CLR-5 :	Classify the application of polymer in MEMS application, also to explore the principle and application of optical, and RF MEMS devices	Thinking (Bloom)	ncy (%	ent (%)		dge		ent	search			Sustainability		Work		ce		Il Achievement	Management & Research
CLR-6 :	Study the mechanics of miniaturization, learning various micro fabrication technologies and the application of mechanisms used in MEMS sensor and Actuators design.	inking	roficie	Attainment		Knowle	Analysis	& Development	Design, Re	Tool Usage	Culture	∞ŏ		Team V	ion	& Finance	Learning	essional	roject Ma s nalyze &
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	evel of Th	Expected Proficiency (%)	Expected /		Engineering Knowledge	^{>} roblem Ana	Design & De	Analysis, De	Modem Tool	Society & Cu	Environment	Ethics	Individual & .	Communication	Project Mgt.	life Long Le	SO-1: Profe	⊃SO – 2: Prr Techniques ⊃SO – 3: An
CLO-1 :	Acquire the knowledge of MEMS devices principles and microfabrication techniques	2	75	60	1	H	-	-	-	-	-	-	-	-	-	-	-	M	LH
CLO-2 :	Understand' the essential electrical and mechanical concepts of MEMS.	2	75	60	1	Н	Н	Н	-	-	-	-	-	-	-	-	-	Μ	LH
CLO-3 :	Familiarize about electrostatic and thermal sensing principles and actuating technique.	2	75	60	1	Н	Н	Н	-	-	-	-	-	-	-	-	-	Μ	LH
CLO-4 :	Attain the knowledge of piezoresistive, piezoelectric and magnetic sensing and actuating technique and microfluidic devices	2	75	60		Н	Н	-	-	-	1	-	-	1	-	-	-	М	L H
CLO-5 :	Be familiar with the polymers material used in MEMS, design and exposure on optical and RF MEMS.	2	75	60	1	Н	-	-	-	-	-	-	-	-	-	-	-	Μ	LH
CLO-6 :	Understand the mechanics of miniaturization, familiar with various micro fabrication technologies and able to design MEMS sensor and Actuators based on the required application.	2	75	60		Н	М	Н										М	L H

	iration hour)	INTRODUCTION TO MEMS AND MICRO FABRICATION	ELECTRICAL AND MECHANICAL CONCEPTS OF MEMS	ELECTROSTATIC AND THERMAL PRINCIPLE SENSING AND ACTUATION	PIEZORESISTIVE, PIEZOELECTRIC AND MAGNETIC PRINCIPLE SENSORS AND ACTUATOR	POLYMER, OPTICAL, RF MEMS AND ITS APPLICATION
	,	9	9	9	9	9
S-1	SLO-1	History of MEMS Development	L'ODOUCTIVITY OF SEMICODOUCTORS	Electrostatic sensing - Parallel plate capacitor	Piezoresistive sensors -piezoresistive	Polymers in MEMS- polymide,SU-8, Liquid
3-1	SLO-2		Problems on conductivity of semiconductors	Problems on electrostatic sensing	sensor material	crystal polymer (LCP)
S-2	SLO-1	Characteristics of MEMS – Miniaturization,	Crystal plane and orientation- Single crystal Si (FCC, Miller Indices and notation,	Electrostatic actuation Parallel plate capacitor	Stress in flexural cantilever and membrane	Polymers in MEMS- PDMS, PMMA ,
3-2	SLO-2	Microelectronics integration - Mass fabrication with precision	crystal planes & characteristics, flats & wafer identification)	Problems on electrostatic actuation		Parylene, Fluorocarbon
	SLO-1	Miniaturization and scaling	Strass and strain definition Delationship	Electrostatic sensing and actuation-	Discolatric consists and actuation	Optical MEMS, passive MEMS, optical
S-3		Sensors and Actuators- Energy domains and example devices for each	Stress and strain - definition , Relationship between tensile stress and strain			Optical MEMS-passive MEMS optical components-lenses-mirrors

				n.		
S-4	SLO-1	Micro fabrication process - Bulk and Surface Micromachining	Stress and strain - definition , Relationship	Electrostatic sensing and actuation- Application - parallel plate actuator comb	Quartz - PZT-	Actuation for active optical MEMS.
3-4	SLO-2		between tensile stress and strain	drive	PVDF -ZnO -Applications	
S-5	SLO-1	Silicon based MEMS processes- processing anisotropic wet etching	Flexural beam bending analysis under single loading condition	Problems on electrostatic sensing and actuation	Magnetic actuation- Principles- Deposition of magnetic materials	RF MEMS: Switches
	SLO-2	Isotropic wet etching	Single loading condition		of magnetic materials	
S-6	SLO-1	Dry etching (plasma etching, ion milling, RIE, DRIE)	Types of beam, longitudinal strain under	Thermal sensing and Actuations- sensors	Design and fabrication of magnetic coil	RF MEMS - Filters, oscillators
0-0	SLO-2	Photolithography,	pure bending	and actuators based on thermal expansion	Design and rabilitation of magnetic con	
6.7		Thin film deposition -sputtering, evaporation,	Deflection of beam- Spring constant	Thermocouples	Missefluidias Concerts of fluid reachanies	
S-7		Thin film deposition - LPCVD, PECVD	Problems: Deflection of beam- Spring constant	Thermal resistors	 Microfluidics – Concepts of fluid mechanics 	MEMS Packaging
S-8	SLO-1	Thin film deposition - sputtering, evaporation, LPCVD, PECVD	Torsional deflection, intrinsic stress	Application of thermal sensors – Inertial,	Microfluidics – Application: Channels,	MEMS Testing
0-0	SLO-2	Thin film deposition - plating, spin-on		Flow, Infrared.	valves	MENO resurg
S-9	SLO-1	New material and fabrication processing techniques	Decemence and quality factor	Problems on thermal sensing and	Misrofluidica Application values	Deliability issues in MEMC peekseing
5-5	SLO-2	Points of consideration for processing structural and sacrificial material.	Resonance and quality factor	actuation	Microfluidics – Application valves	Reliability issues in MEMS packaging
Learni Resou	•	1 st Edition, 2015.	Design and Manufacturing, McGraw Hill Ec	6. Julian W.Gardner ar Sons. 2010. 2013.	d Frank J. Owens, "Introduction to Nanotechr nd Vijay K Varadhan, "Microsensors, MEMS a crofabrication - by M. Madou; Publisher: CRC	nd Smart Devices", John Wiley & sons,

Learning Ass	essment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Einal Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	¥ (10%)#		i (50 % weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40 %		30 %		30 %	-	30 %	-	30%	
Level I	Understand	40 /0	-	30 78	-	50 78	-	50 78	-	3078	-
Level 2	Apply	40 %	_	40 %	_	40 %	-	40 %	_	40%	_
	Analyze	40 70	-	40 70	-	40 70	-	40 70	-	4070	-
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%	
Level 3	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100) %	100) %	100	0 %	100 %	

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@ici.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE303T		urse me	Nanoelee	tronic Devices and Circuits		Е	Professional Elective	L 3	T 0	P 0	C 3
Pre-requisi Courses		18EC	CC102J Co-requisite Nil Progressive Courses		Nil							
Course Offer	ing Department		Electronics and Comm	nunication Enginee	ring Data Book / Codes/Standards	Nil						

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng]					Progr	am L	.earni	ng O	utcon	nes (F	PLO)				
CLR-1 :	Identify the need and effects of device miniaturization	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Understand the principles of nano devices	(m	(%)	(%)					ų			lity								
CLR-3 :	Learn about new devices at nano scale	(Bloom)				ge		Ŧ	earch			Sustainability		¥		æ			ect Techniques	
CLR-4 :	Create insights to the concepts of nano CMOS circuits	J) Br	roficiency	Attainment		vledç		mer	Rese	g		ıstai		Nork		Finance	0	nal	hnig	~ð
CLR-5 :						Engineering Knowledge	Analysis	Development	sign,	Tool Usage	Culture	∞ŏ		Team	ы	& Fir	Leaming	Professional ment	Tect	alyze
CLR-6 :	Utilize the design procedure in circuits	Thinking	ЧÞ	Αþ		ing l	Anal		å	00	Cul	lent		∞ŏ	icati		Lea	Profe	Projec	Ans
		el of	ecte	ecte		neer	lem	gn &	Analysis,	ern]	ety &	Environment	ŝ	ndividual	Communication	^o roject Mgt.	Ű.) – 2: Proje agement T	- 3: earct
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level	Expected	Expected		Engi	Problem ,	Design	Anal	Modern ⁻	Society	Ē	Ethics	Indiv	Com	Proje	Life L	PSO Achie	PSC Man	PSO Rese
CLO-1 :	Realize the importance of scaling of devices.	2	80	70	1	Н	М	-	М	-	-	-	-	-	-	-	-	-	-	М
CLO-2 :	Identify the difference of nano devices from conventional devices.	2	85	75	1	Н	-	-	М	-	-	-	-	-	-	-	-	М	-	
CLO-3 :	Analyze the performance measures of various devices	2	75	70	1	Н	-	М	Н	Н	-	-	-	-	-	-	-	-	-	Н
CLO-4 :	Choose appropriate application of the device	2	85	80	1	Н	Н	-	-	-	Н	L	Н	-	-	-	-	-	L	Н
CLO-5 :	: Understand the design considerations of nano circuits		85	75	1	Н	-	Н	Н	М	-	-	-	-	-	-	-	-	-	М
CLO-6 :	D-6 : Apply the design concepts of nano circuits in real time applications		80	70	1	Н	М	-	-	-	Н	L	Н	-	-	-	-	М	-	М

	ration	Introduction to Nano Devices	Silicon MOSFETs- Novel Materials and Alternative Concepts	Nano Devices – Principles and Techniques	Nano- CMOS scaling Problems and Implications	Mixed Signal Circuit Design
(r	nour)	9	9	9	9	9
S-1	SI 0-1	MOS transistor- A First Glance at the Device	SOI MOSFET, partially depleted		Design Methodology in the Nano-CMOS Era	Design Considerations – Device Modeling
3-1	SLO-2	The MOS Transistor under Static Condition	fully depleted SOI		Innovations needed to continue performance scaling -	Passive Components
S-2	SLU-1	MOS Transistor Capacitances- Channel Capacitance	Strained channel MOSFET,		Sub-100-nm Scaling Challenges- Back- End-of-Line Challenges (Metallization)-	Design Using Thin Oxide Devices – Design Using Thick Oxide Devices
3-2	SLO-2	Junction Capacitance	Hi-k gate dielectric, Metal gate electrode	Single Electron Transistor	Interconnect scaling-copper wire technology	Low-Voltage Techniques
S-3	SLO-1	The Actual MOS Transistor—Some Secondary Effect	Double gate MOSFET		Low –k dielectric challenges-future global interconnect technologies	Design Procedures
3-3	SLO-2	Challenges in Nanoscale MOSFETs	FinFET		Front-End-of-Line Challenges (Transistors)-Quantum effects model	Electrostatic Discharge Protection
	SLO-1	Scaling of transistor dimensions	Tunnel Effect	Carbon Nano Tube(CNT)	Polysilicon gate , Metal gate electrodes,	Multiple-Supply Concerns
S-4	SLO-2	Moore's law	Tunneling through a potential barrier	Electronic properties of UNI	Direct tunneling gate leakage-Parasitic capacitance	Noise Isolation
S 5-6		Short Channel Effects (SCE) : Sub- threshold Conduction,	Potential energy profiles for material interfaces	Geometrical structure, Electronic structure of CNT Transport properties	Reliability concerns	Guard Ring Structures Isolated NMOS Devices

S-7	SLO-1	Drain Induced Barrier Lowering	Metal -insulator, metal -semiconductor	CNTFET, comparison of Si MOSFET with CNTFET	Process Control Reliability	Epitaxial Material versus Bulk Silicon –
	SLO-2	Velocity Saturation, Hot electrons	Metal –insulator -metal junctions	FeFET	Lithographic Issues	Decoupling
S-8	SLO-1	Emergence of new materials,	Tunneling Diode	Principle of Spintronics	Mask Data Explosion	Power Busing
3-0	SLO-2	Hi-k materials and its issues	Resonant Tunneling diode	Spin valves, SpinFET	New Breed of Circuit	Integration Problems
S-9	SLO-1	metal gate, copper interconnect and	Three-terminal resonant tunneling devices	Magnetic Tunnel Junctions	- Physical Design - Modeling Challenges	Corner Regions
3-9	SLO-2	low-k interlayer dielectric	inverter and logic OR gates based on RTD	MRAM	Need for Design Methodology Changes	Neighboring Circuitry

Learning Resources	 Rainer Waser (Ed.), "Nanoelectronics and Information Technology", Wiley-VCH, Third, Complet Revised and Enlarged Edition, 2012. Jan M. Rabaey, Anantha Chandrakasan, and Borivoje Nikolic," Digital Integrated Circuits 2nd edition", Pearson, 2000. Ban P. Wong, Anurag Mittal, YuCao, Gren Starr, "Nano- CMOS Circuit and Physical Design", John Willey and sons Publication, 2005 	 George W. Hanson, "Fundamentals of Nanoelectronics", Prentice Hall, 20073.Karl Goser, Peter GlÖsekötter, Jan Dienstuhl, "Nanoelectronics and Nanosystems", Springer, 2004 Vladimir V. Mitin, Viatcheslav A. Kochelap, Michael A. Stroscio, "Introduction to Nanoelectronics: Science, Nanotechnology, Engineering, and Applications", Cambridge University Press, 2012
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Learning As	ssessment										
	Diamin			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examinatio	n (50% weightage)
	Bloom's Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA – 3 (15%)		CLA – 4	4 (10%)#		n (50% weightage)
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40%	-	40%	-	40%	-	40%	-	40%	-
Level 2	Apply Analyze	40%	-	40%	-	40%	-	40%	-	40%	-
Level 3	Evaluate Create	20%	-	20%	-	20%	-	20%	-	20%	-
	Total	10	0 %	10	0 %	100	0 %	10	0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. P. Aruna Priya, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE304T		urse ame	Micro	wave Integ	grated Circuits	-	ourse tegor	F	Professional Elective			-	L 3	T 0	P 0	C 3						
Pre-requisite 18ECC105T Co-requisite Nil Courses 18ECC105T Courses Nil					Nil		gress		 					Ν	Vil								
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Stan						Data Book / Codes/Standards			·					Nil									
Course Learning Rationale (CLR): The purpose of learning this course is to:					L	earnii	ng				Pro	gram	Learr	ning C	Outcor	nes (PLO)						
CLR-1:	Create the insights of I	nicrow	ave circuits				1	2	3	1	2 3	3 4	5	6	7	8	9	10	11	12	13	14	15
CLR-2: Analyze matching networks and filter design CLR-3: Identify and implement amplifiers and oscillators CLR-4: Layout the types of mixers and control circuits CLR-5: Utilize techniques to fabricate and measurement of MICs CLR-6: Analyze and realize microwave circuits and its techniques				of Thinking (Bloom)	Expected Proficiency (%)	cted Attainment (%)	Engineering Knowledge	2 0	Design & Development. Analvsis: Design Research	Modern Tool Usage	ty & Culture	Environment & Sustainability		tual & Team Work	Communication	Project Mgt. & Finance	ong Leaming	 Professional vement 	 - 2: Project Management liques 	- 3: Analyze & Research			
Course Lea	arning Outcomes (CL	.0): /	At the end of this cours	e, learners will be al	ole to:		Level		8 Expected		Proble	Analveis	Moder	Society &	Enviro	Ethics	Individual	Comm	Projec	Life Long	PSO- Achiev	PSO - Techn	- OS
CLO-1:	LO-1: Understand the different types of MICs, different MIC devices and parameters to be used in MICs				be used in MICs	2	75		H		-	-	-	-	-	-	-	-	-	-	-	Μ	
	CLO-2: Explore the concept of frequency parameters, ZY smith chart and its interpretation in the analysis and design of matching circuits			2	75	60	Н	H F	ł M	-	-	-	-	-	-	-	-	-	-	М			
CLO-3 :	CLO-3 : Understand the design of Amplifiers and Oscillators				2	75	60	Н	H H	I h	-	-	-	-	-	-	-	-	-	-	Н		
CLO-4 :				2	75	60	Н	Η·	· M	-	-	-	-	-	-	-	-	-	-	М			
CLO-5 :	CLO-5: Understand Micro fabrication of MIC devices and measurement techniques of MICs			2	75	60	Н		· N	-	-	-	-	-	-	-	М	-	-	Η			
CLO-6:					2	75	60	Н	Ηŀ	I h	-	-	-	-	-	-	-	М	М	-	М		

Du	ration	Introduction to MIC	Matching Circuits	Microwave Amplifiers and Oscillators	Mixers and Microwave Diodes	MIC Measurement Techniques
(h	iour)	9	9	9	9	9
S-1	SLO-1	Introduction to MICs	Circuit Representation of two port	Circuit Representation of two port Introduction to amplifiers		Microwave Integrated Circuits :
3-1	SLO-2		RF/Microwave Networks	Stability considerations in active networks	Introduction to Mixers	Introduction to SOC, SOP
S-2	SLO-1	Frequency Bands Low Frequency Parameters		Caia Canaidantian in Annlifian		MIC Materials.
3-2	SLO-2	Lumped versus Distributed Circuits	High Frequency Parameters	Gain Consideration in Amplifiers	Mixer Types	MIC Materials.
S-3	SLO-1	Behavior of finite length transmission lines	Transmission Matrix	Noise Consideration in active networks	Conversion Loss	Hybrid versus Monolithic MICs
3-3	SLO-2				Conversion Loss	
S-4	SLO-1	General Characteristics of PC Boards	ZY Smith Chart	Broadband Amplifier design	SSB Mixers	Multichip Module Technology
3-4	SLO-2	General Characteristics of PC Boards		Low Noise Amplifier Design	DSB Mixers	Multichip Module Technology
	SLO-1	T				
S-5	SLO-2	Transmission Lines on PC Boards	ZY Smith Chart	Introduction to oscillators	Design of Mixers: Single Ended Mixers	Fabrication Techniques
	SLO-1		Design of Matching Circuits using			
S-6	SLO-2	Passives made from Transmission Lines	Lumped Elements	Oscillator versus Amplifier Design	Single Balanced Mixers	Miniaturization techniques
S-7	SLO-1	Resonators	Design of Matching Circuits using Lumped Elements		Sub Harmonic Diode Mixers	Test fixture measurements
	SLO-2		Matching Network Design using Distributed			

			Elements			
S-8	SLO-1	Combiners and Splitters	Matching Network Design using Distributed	Design and stability considerations of	Microwave Diodes	probe station measurements
3-0	SLO-2	Combiners and Spinters	Elements	Microwave Transistor Oscillators.	Microwave Diodes	
S-9	SLO-1	Couplers	Filter design	Design and stability considerations of	Phase Shifters and PIN	thermal and cryogenic measurements
3-9	SLO-2	Couplers		Microwave Transistor Oscillators.	Diode Attenuators	

Learning Resources	2002 3. Guillermo Gonzalez, "Microwave Transistor Amplifiers – Analysis and Design", Il Edition, Prentice Hall, New Jersy.	 Hoffman R.K. "Handbook of Microwave Integrated Circuits", Artech House, Boston, 1987. Ulrich L. Rohde and David P.N., " RF / Microwave Circuit Design for Wireless Applications", John Wiley, 2000. C. Gentili, "Microwave Amplifiers and Oscillators", North Oxford Academic, 1986. Samuel. Y. Liao, "Microwave Circuit Analysis and Amplifier Design", Prentice Hall. Inc., 1987.
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Learning As	sessment											
	Bloom's		Final Examinatio	n (E0%) weightenn)								
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total 100 % 100 %) %	100	0 %	0 %	100 %			

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. T. Ramarao, SRMIST

Course	18ECE3051	Course	ARM -SoC	Course	F	Drafassianal Electiva	L	Т	Ρ	С
Code	10ECE305J	Name	ARIVI -SOC	Category	E	Proiessional Elective	2	0	2	3

Pre-requisite Courses	18E	CE204J	Co-requisite Courses		Nil	Progressive Courses	Nil
Course Offering I	Department	Electronics and Co	ommunication Engi	neering	Data Book / Codes/Standards		Nil

Course Lea	rning Rationale (CLR): The purpose of learning this course is to:	L	earnir	ng					I	Progr	ram L	.earni	ng O	utcon	nes (l	PLO)				
CLR-1 : S	Study the hardware architecture of ARM Cortex-M core	1	2	3	F	1	2	3	4	5	6	7	8	9	10	11	12	13	14 1	15
CLR-2 : S	Study the AHB (high speed bus) and peripherals	ĉ										Y						1	ent	earch
CLR-3 : S	Study the AHB (high speed bus) and peripherals	(Bloom)	(%) /	(%)					search			bilit						1	letti	sear
CLR-4 : P	Program high speed peripherals	Ē	l S	lent		dge		ent	esee			aina		Work		ce		_	Managemer	Res
CLR-5 : S	Study APB bus and peripherals. Designing applications with CMSIS	Thinking	Proficiency	Attainment		wle	s	md	ı, Re	age	e	Sustainability		≥ E		inar	Ð.	ional	, Ri	e S
CLR-6 : U	Inderstand and learn to use ARM Cortex-M processor architecture, and deice level programming.	ink	p l	∖tta		Кло	Analysis	velo	Design,	Us	Culture	∞ŏ		Team	ion	ъ К	ami	t		Analyze
						ing	Ana	& Development	Ê.	Tool Usage	& C	neni		∞ŏ	ication	Agt	ong Leaming		τ̃ ອີ	An
Course Lea	rning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected	Expected		Engineering Knowledge	Problem	Design 8	Analysis, I	Modem .	Society 8	Environment	Ethics	Individual	Commun	Project Mgt. & Finance	Life Long		i i i i i	PSO – 3:
CLO-1 : E	xplain hardware and register architecture of ARM Cortex-M based processors	2	80	70														Н		
	Explain AHB and its signals, and program high speed peripherals	3	80	70			М	Н	L	Н								Н	1	Μ
CLO-3 : U							Н	Н	L	Н								Н	1	Μ
CLO-4 : P							М	Н		Н								М		
CLO-5 : P	0-5: Program device driver and create libraries.						М	Н		М								М		
CLO-6 : P							М	Н	L	Н								М		

Durati	on (hour)	12	12	12	12	12
Durati	on (hour)	ARM Cortex-M architecture	ARM Internal bus	ARM pheripherals	SOC programming	Case studies
S-1	SLO-1	Introduction to Programmable SoC	AMBA 3 AHB Lite architecture	AHB UART peripheral	Programming an SOC using C language	Graphics LCD interfacing
3-1	SLO-2	Introduction to Programmable SoC	AMBA 3 AHB Lite architecture	AHB UART peripheral	Programming an SOC using C language	Graphics LCD interfacing
S-2	SLO-1	ARM architecture	AMBA 3 AHB Lite architecture	AHB UART peripheral	APB Bus	Board support package
3-2	SLO-2	ARM architecture	AMBA 3 AHB Lite architecture	AHB UART peripheral	APB Bus	Board support package
S	SLO-1	Lab-1:ARM Keil IDE usage –	Lab 4: Study of AHB peripheral	Lab 7: Multinode I2C Bus	Lab 10: Making a device driver	Lab 13: Case study – 2
3-4	SLO-2	sample ARM program.	Lab 4: Study of AHB peripheral		Lab To: Making a device driver	Lab 13: Case study – 2
S-5	SLO-1	ARM Register architecture	AHB SRAM controller	AHB timer	ARM CMSIS	Ethernet interfacing
5-5	SLO-2	ARM Register architecture	AHB SRAM controller	AHB timer	ARM CMSIS	Ethernet interfacing
S-6	SLO-1	ARM assembly language	AHB SRAM controller	AHB-APB bridge	Device drivers	Ethernet interfacing
3-0	SLO-2	ARM assembly language	Review and discussions	AHB-APB bridge	Device drivers	Ethernet interfacing
S	SLO-1	Lab 2: Assembly language	Lab 5: ARM memory management	Lab 8: Application of timers	Lab 11: Using CMSIS	Lab 14: Model lab examination

7-8	SLO-2	programming of ARM processor using Keil IDE				
S-9	SLO-1	ARM Cortex-M Architecture -1	AHB VGA peripheral	Fast GPIO programming	Application programming	Student Seminar / discussions
5-9	SLO-2	ARM Cortex-M Architecture -1	AHB VGA peripheral	Fast GPIO programming	Application programming	Student Seminar / discussions
S-10	SLO-1	ARM Cortex-M Architecture -2 (pipelines)	AHB VGA peripheral	Interrupt mechanism of ARM	Case study - 1	Student Seminar / discussions
	SLO-2	ARM Cortex-M Architecture -2	AHB VGA peripheral	Interrupt mechanism of ARM	Case study - 2	Student Seminar / discussions
S	SLO-1	Lab 3: Parallel port programming	Lab 6: Graphics application	Lab 9: Experimenting Interrupts,	Lab 12: Study of USB interface	Lab 15: Final lab examination
11-12	SLO-2		Las o. Graphics application	Timers		

Learning Resources 1. Steve Furber, "ARM System on a Chip Architecture – 2 nd Edition", Pearson Education, 2000. 2. "AMBA -3 APB Protocol", ARM Limited, 2003. 3. "AMBA -3 AHB Lite Protocol", ARM Limited, 2003. 4. Theory/Lab teaching materials, "Introduction to SoC kit", ARM

Learning As	sessment											
	Bloom's		Continuous Learning Assessment (50% weightage)									
		CLA – 1 (10%)		CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#	Final Examination (50% weightage		
	Level of Thinking Theory Practice Theory Practice Theory Practice			Theory Practice		Theory	Practice					
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100 %			100 %		0 %	10) %	-		

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Course	185053061	Course	ARM based Digital Signals Processing	Course		Professional Elective	L	Т	Ρ	С
Code	105053003	Name	Artivi based Digital Signals Processing	Category	L	Professional Elective	2	0	2	3

Pre-requisite Courses	18ECE204J	Co-requisite Courses		Nil	Progressive Courses	Nil
Course Offering Department	Electronics and Co	ommunication Engir	neering	Data Book / Codes/Standards		Nil

Course L	earning Rationale (CLR):	The purpose of learning this course is to:	L	earni	ng						Progi	ram L	_earn	ing O	utcor	nes (PLO)				
CLR-1 :	Understand the concepts of	DSP, discrete time signals and its properties.	1	2	3	ľ	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Understand applications of	transforms in solving digital signal processing	Ê										y							ent	с-
CLR-3 :	Study FIR filters		(Bloom)	(%)	(%)					arch			Sustainability							Management	search
CLR-4 :	Study IIT filters		Ē	l Co	lent		dge		ent	Se			aina		Work		ce			anaç	Res
CLR-5 :	Understand the usage of a	laptive filter techniques	ing	icie	inπ		wle	s	mdo	n, Re	Usage	e	Sust		۳V		inar		ion	t	e &
CLR-6 :	Understand the usage of D	SP in embedded ARM Cortex-M processor platform	Thinking	Proficiency	Attainment		g Kno	Analysis	& Development	Design, I	Tool Us	Culture	~ð		& Team	ation	Mgt & Finance		Professi nent	Project I es	Analyze
Course L		At the end of this course, learners will be able to:	Level of	Expected	Expected		Engineering Knowledge	Problem /	Design &	Analysis, I	Modem To	Society &	Environment	Ethics	Individual	Communication	Project M	Life Long	D-1: F ever	PSO-2: Technique	
CLO-1 :	Apply theory and application	on of discrete time signals	3	80	70			М	-	-	М	-	-	-	-	-	-	-	Н	-	L
CLO-2 :	Apply theory and implemen	ting methods of Z-transforms, DFT and FFT.	3	80	70			Н	-	-	М	-	-	-	-	-	-	-	Н	-	L
CLO-3 :	Apply FiR filter theory and	processor implementation in C.	3	80	70	ľ		Н	-	-	М	-	-	-	-	-	-	-	Н	-	L
CLO-4 :	Apply IIR filter theory and p	processor implementation in C	3	80	60			Н	-	-	М	-	-	-	-	-	-	-	Н	-	L
CLO-5 :	Implement adaptive filter de	sign theory, methods and its uses.	2	80	60			Н	-	-	L	-	-	-	-	-	-	-	Н	-	L
CLO-6 :	Apply the theory and implei	nentation aspects of DSP in ARM Cortex-M based processor platform.	3	80	60			Н	-	-	М	-	-	-	-	-	-	-	Н	-	L

	iration hour)	Learning Unit / Module 1 Basics of digital signals	Learning Unit / Module 2 Transforms for DSP	Learning Unit / Module 3 FIR filters	Learning Unit / Module 4 IIR filters	Learning Unit / Module 5 DSP applications
,,	iour)	12	12	12	12	12
S-1	SLO-1	DT Signals-basics properties &Operations on DT signals	Z-Transform Properties	Design of Finite Impulse Response Filters- Symmetric and Antisymmetric FIR filters	Frequency Response and Characteristics of Analog Filters	Introduction-Steepest Descent Method- Least Mean Squares Method
3-1	SLO-2	on DT signals 2-1 ransform Properties Symmetric and Antisymmetric FIR filters of Analog Filters L		Introduction-Steepest Descent Method- Least Mean Squares Method		
S-2	SLO-1	DT systems-Properties of DT Systems – LTI system	Inverse Z-Transform-solving Difference Equation	Design of Linear- Phase FIR filters Using window methods	IIR Filter Design by Impulse Invariance	Adaptive Filters: Prediction and System Identification
5-2	SLO-2	DT systems-Properties of DT Systems – LTI system	Inverse Z-Transform-solving Difference Equation	Design of Linear- Phase FIR filters Using window methods	THR FILTER Design by Impulse Invariance	Adaptive Filters: Prediction and System Identification
S 3-4	SLO-1 SLO-2	Lab 1: Introduction- Keil MDK-ARM application development Environment.	Lab 4: LTI System Implementation	Lab 7: Filter Structures in the CMSIS- DSP Library		Lab 13: CMSIS Implementation of the LMS and Normalized LMS methods
S-5	SLO-1	Convolution and Correlation	DFT-review; problems	Design of Linear- Phase FIR filters Using window methods	Design of Butterworth filter using Bilinear Transformation	Adaptive Filters: Equalization and Noise Cancellation
3-3	SLO-2	Convolution and Correlation	DFT-review; problems	Design of Linear- Phase FIR filters Using window methods	Design of Butterworth filter using Bilinear Transformation	Adaptive Filters: Equalization and Noise Cancellation

11-12	SLO-2	the Sampling Frequency	Domain	Different Structures	Different Structures	Lab 15: University practicals
S	SLO-1	Lab 3: A-D & D-A conversion-Changing	Lab 6: Filtering in the Frequency	Lab 9: Implementing a FIR Filter using	Lab 12: Implementing a Filter using	l ab 45. University prestingle
3-10	SLO-2	Reconstruction in the Frequency Domain & time Domain	Filtering in the FD-Linear Convolution	Filter Design using Software	Filter Design using Software	Review, Problems and Discussions
S-10	SLO-1	Reconstruction in the Frequency Domain & time Domain	Filtering in the FD-Linear Convolution	Filter Design using Software	Filter Design using Software	Review, Problems and Discussions
S-9	SLO-2		Filtering in the FD-Circular & Convolution	Design of Optimum Equiripple Linear- Phase FIR filters	Chebyshev Filter Designs based on the Impulse Invariance	Review, Problems and Discussions
e 0	SI O-1	Sampling Theorem in the Frequency Domain-Aliasing	Filtering in the FD-Circular & Convolution	Design of Optimum Equiripple Linear- Phase FIR filters	Chebyshev Filter Designs based on the Impulse Invariance	Review, Problems and Discussions
S 7-8	SLO-2	Digital Signals	Lab 5: Calculating the DFT-FFT	Lab 8: FIR Filter Design	Lab 11: IIR Filter Design	Lab 14: Model Practicals
	SL0-2	CT-to DT Conversion Sampling Theorem in the Time Domain	DIT-FFT Radix 2 butterfly derivation - problems	Design of Optimum Equiripple Linear- Phase FIR filters	Chebyshev Filter Designs based on the Bilinear Transformation	Adaptive Filters: Adaptive FIR Filter
S-6	SL0-1	in the Time Domain	DIT-FFT Radix 2 butterfly derivation - problems	Design of Optimum Equiripple Linear- Phase FIR filters	Bilinear Transformation	Adaptive Filters: Adaptive FIR Filter

Learning Resources	 Cem Unsalan, M. Yerkin Yuccel, H. Deniz Gurham, "Digital Signal Processing Using ARM Cortex-M based microcontrollers, Theory and Practice", ARM Education Media, 2018. 	2. Theory/Lab teaching materials, ARM Educational Media.
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Learning Assess	sment											
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Einal Examination	n (50% weightage)	
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA – S	3 (15%)	CLA – 4	¥ (10%)#		i (50 % weightage)	
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level I	Understand	2070	2076	1370	1370	1370	1370	1370	1370	1370	1570	
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level Z	Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
1	Evaluate	400/	400/	450/	450/	450/	450/	450/	450/	450/	450/	
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100	0 %	100 %		100	0 %	10	0 %	-		

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Course Code	18ECE307J	Course Name	Арр	blied Machine Learning	Course Category	Professional Elective	L T P C 2 0 2 3
Pre-requis Courses		Nil	Co-requisite Courses	Nil	Progressive Courses	Nil	
Course Offe	Course Offering Department Electronics and C			neering Data Book / Codes/Standards		Nil	

Course Lo	earning Rationale (CLR): The purpose of learning this course is to:	L	earniı	۱g	[Prog	ram L	earni	ing O	utcor	nes (F	PLO)				
CLR-1 :	Understanding the Machine Learning concept and types	1	2	3	ĺ	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Analyze the ML algorithm performance byLearning Curve and error	Ē	-		ſ							>							ent	÷
CLR-3 :	Applying ML algorithm for solving practical problems	(moc	(%)	(%)					arch			Sustainability							nageme	Research
CLR-4 :	Create insights to the concepts and programming of supervised and unsupervised ML methods	(Blo	roficiency	Attainment		Knowledge		ant	se			aina		Work		e		_	nag	
CLR-5 :	Analyze and understand the working principle and model development of Evolutionary Learning	hinking	cie	E		Me	6	Development	, Re	Usage		usta		۲		Finance	p	siona	Ma	oo o
CLR-6 :	Create insights to the concepts and programming of Reinforcement learning	inki	lo	ttai		Kno	lysi	velo	Design,	Use	Culture	∞ŏ		Team	o	& Fi	Learning	essi	roject	Analyze
			р Ц			ing	Analysis	De	De	Tool	G	lent		∞ŏ	unication	Mgt	Le	Prof	es Pro	An
Course Lo	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected	Expected		Engineering I	Problem	Design &	Analysis,	Modem 1	Society &	Environment	Ethics	Individual	Commun	Project N	Life Long	PSO-1: I Achieven	PSO-2: Techniqu	PSO-3:
CLO-1 :	Apply Genetic Algorithm for evaluationary learning	2	80	70	ſ	H	H	H	-	-	-	-	-	-	-	-	-	-	-	H
CLO-2 :	Analyze Reinforcement learning	2	85	75	ſ	Н	Н	-	Н	Н	-	-	-	-	-	-	-	-	-	Н
CLO-3 :	Apply linear model of linear regression and SVM for classification problem	2	75	70	ľ	Н	Н	Н	Н	-	-	-	-	-	-	-	-	Н	-	Н
CLO-4 :	Apply neural network and CNN for classification problem	2	85	80	ſ	Н	Н	Н	Н	Н	-	-	-	-	-	-	-	Н	-	Н
CLO-5 :	Apply Decision Trees , clustering For classification problem	2	85	75		Н	Н	Н	Н	Н	-	-	-	-	-	-	-	Н	-	Н
CLO-6 :	Apply probability model of Bayesian decision theory and HMM for classification problem	2	80	70		Н	Н	Н	Н	Н	-	-	-	-	-	-	-	Н	-	Н

	ration	Introduction to Machine Learning and Linear Model	Multiplayer Perceptrons and Decision Tree	Clustering, SOM and HMM	Bayes Network, Reinforcement Learning and CNN	Genetic Algorithm and Application of ML
(II	iour)	12	12	12	12	12
S-1	SLO-1	Introduction to Machine learning: Types of Machine Learning - Supervised Learning – Unsupervised, Learning	Multiplayer, Perceptrons	Clustering	Bayesian decision theory	The Genetic Algorithm
	SLO-2	reinforcement learning , The Curse of dimensionality	Multiplayer, Perceptrons	K-Means clustering	Bayesian decision theory	The Genetic Algorithm
	SLO-1	Bias and Variance, Learning Curve	Multiplayer, Perceptrons	Hierarchical clustering	Bayesian estimation	Facial Expression Recognition
S-2	SLO-2	Classification, Error and noise,linear regression	Multiplayer, Perceptrons	Agglomerative clustering	Bayes network	Human Emotion Research
S-3,4	SLO-1 SLO-2	Lab 1: Linear Regression	Lab 4: Multiplayer, Perceptrons	Lab 7: K-Means clustering	Lab 10: Bayes Network	Lab 13: Genetic Algorithm
0.5	SLO-1	Support Vector Machines	example of using MLP	Vector Quantization	Reinforcement learning	Facial Expression Recognition System
S-5	SLO-2	Support Vector Machines	example of using MLP	Vector Quantization	Reinforcement learning	Facial Expression Recognition System
	SLO-1	Support Vector Machines	example of using MLP	The Self-Organizing Feature Map	Reinforcement learning	Speech Emotion Recognition
S-6	SLO-2	Support Vector Machines	example of using MLP	The Self-Organizing Feature Map	Reinforcement learning	Speech Emotion Recognition
S-7,8	SLO-1 SLO-2	Lab 2: Support Vector Machines	Lab 5: MLP application	Lab 8: SOFM	Lab 11: Reinforcement learning	Lab 14: Speech Emotion Recognition Basic classification

• •	SLO-1	basics of neural network	Decision Trees- classification	НММ		Neural Network Multi-Layer Perceptron
S-9	SLO-2	Perceptrons	regression tree,	НММ		Modeling For Surface Quality Prediction in Laser Machining
S-10	SLO-1	LINEAR SEPARABILITY	pruning, rule from tree and data	НММ	CNN Building Blocks	Machine Learning in Cybersecurity- Supervised Learning for Misuse/Signature Detection
3-10	SLO-2	Perceptrons and introduction to Multiplayer, Perceptrons	multivariate tree	НММ	CNN Building Blocks	Machine Learning in Cybersecurity- Supervised Learning for Misuse/Signature Detection
S-	SLO-1	Lab 3: Perceptrons	Lab 6: Decision Trees	Lab 9: HMM	Lab 12: CNN	Lab 15:Mini project
11,12	SLO-2					

Learning Resources	3. Sumeet Dua and Xian Du , "Data Mining and Machine Learning in Cybersecurity", CRC Press, 2011.	 Yagang Zhang, "Application of Machine Learning", Published by In-Tech, 2010. Starter Bundle, "Deep Learning: A Practitioner's Approach", O'Reilly Media, 2017. Dr. Adrian Rosebrock, "Deep Learning for Computer Vision with Python", Packt Publisher, 2018. Ankur A Patel, "Hands-On Unsupervised Learning Using Python: How to Build Applied Machine Learning Solutions from Unlabeled Data", O'Reilly media, 2019.
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Learning As	sessment"										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examinatio	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total 100 %			10	0 %	100) %	100	0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. P. Vijayakumar, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE220T	Cours Name		Advanced Mobile	Communication Systems		ourse egor		E				Profe	ssional	Elect	ive				L 3	T 0	P 0	C 3
Pre-requisite 18ECC301T Co-requisite Nil Progres Courses 18ECC301T Courses Nil Courses							•		Nil														
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards							Nil																
Course Lear	ning Rationale (CLF	R): The	e purpose of learnin	g this course is to:			earni	ng					Pr	ogram	Lear	ning (Dutco	mes (PLO)				
CLR-1: Ga	ain knowledge about	the latest	t Standards from 30	G to 5G systems.		1	2	3		1	2	3	4	56	7	8	9	10	11	12	13	14	15
CLR-3 : Stu CLR-4 : Un CLR-5 : Stu CLR-6 : Ap	nderstand the techniq udy the techniques o ply the knowledge ga	MO system ques of cc f Millimet ained to v	m and the impact of ognitive radio spectr er wave communica various Advanced M	f different channel models rum sensing and sharing	stems	evel of Thinking (Bloom)	Expected Proficiency (%)	Attainment		Engineering Knowledge	Problem Analysis		Analysis, Design, Research	Modern Tool Usage Society & Culture	Environment & Sustainability		Individual & Team Work	Communication	Project Mgt. & Finance	Long	0-1: P	PSO – Z: Project Management Techniques	⊃SO – 3: Analyze & Research
	oly the architecture a	and functi	ionalities of 3G and	1G systems		2.3		<u>نن</u> 80		ш Н	<u>-</u>	<u> </u>	Ā M	<u> </u>	j ū	<u> </u>	-	ŏ	- -	-	Act Do H	Ϋ́́́́	ă M
CLO-1 : Apply the architecture and functionalities of 3G and 4G systems CLO-2 : Understand the concepts of OFDM and it issues					2,3	80	85		M	-	H	M		-	1-	-	-	-	-	M	-	H	
CLO-3 : Understand the MIMO communication systems						2,3	85	80		Н	-		H		-	-	-	-	-	-	M	-	Н
CLO-4: Understand the principle of Cognitive Radio Techniques						2,3	80	75		М	-	-	Μ		-	-	-	-	-	-	М	-	Н
CLO-5: Acquire the concept of millimeter wave communication					2,3	85	80		М	-	М	Н		-	-	-	-	-	-	М	-	Н	
CLO-6 : Ab	le to analyze the Adv	vance Mo	bile communication	n systems																	М		Н

Duration (hour)		Advanced cellular mobile communication systems	Multicarrier modulation technique- OFDM	MIMO systems	Cognitive Spectrum management	Millimeter wave communication	
		9	9	9	9	9	
S-1	SLO-1	Overview of the legacy 3GPP cellular systems	Introduction to OFDM	Introduction to MIMO	Cognitive transceiver Introduction	Millimeter Wave Characteristics	
3-1	SLO-2	Overview of the legacy 3GPP cellular systems	Multicarrier Modulation Introduction	Introduction to MIMO Channel Capacity	Cognitive transceiver architecture	Introduction to Channel Performance at Mm wave communication	
S-2	SLO-1	WiMAX systems: Introduction	Multicarrier Modulation	MIMO Channel Estimation	Interweaving	Channel Performance at Mm wave communication	
3-2	SLO-2	WiMAX systems: Architecture	Cyclic Prefix	MIMO Channel Estimation	Principle of interweaving	Modulation for Millimeter Wave communication	
S-3	SLO-1	WiMAX systems: Architecture	Channel model	MIMO Spatial Multiplexing	Principle of interweaving	Modulation for Millimeter Wave communication	
3-3	SLO-2	WiMAX systems : Frame structure	SNR	MIMO Spatial Multiplexing	Introduction to Spectrums	Millimeter wave transmitter	
S-4	SLO-1	WiMAX systems : Frame structure	SNR Performance	V- BLAST 2	Types of Spectrum	Millimeter wave Receiver	
0-4	SLO-2	WiMAX systems : Applications	SNR Problems	V- BLAST 2	Spectrum sensing	Millimeter wave Antenna	
S 5-6	SLO-1 SLO-2	LTE systems: Introduction	OFDM Introduction	MIMO Diversity	Advantages of Spectrum sensing	Introduction Mm wave Communications	
S-7	SLO-1	LTE systems: Architecture	OFDM Issues	MIMO Diversity	Disadvantages of Spectrum sensing	Emerging applications of Mm wave Communications	

	SLO-2	LTE systems: Architecture	OFDM Issues	Alamouti		Emerging applications of Mm wave Communications
S-8	SLO-1	LTE systems: Frame structure	PAPR	Alamouti	Spectrum Management	Millimeter Wave Standards.
3-0	SLO-2	LTE systems: Frame structure	Frequency and timing	OSTBC	Spectrum Management	Introduction to Millimeter Wave Standards.
S-9	SLO-1	LTE systems: applications	Frequency offset issues.	MIMO :OFDM system Introduction	Shectrum Management	Development of Millimeter Wave Standards.
2-9	SLO-2	LTE systems: applications	Timing offset issues.	MIMO :OFDM system	Shectrum Management	Development of Millimeter Wave Standards.

Learning Resources	 Andrea Molisch, "Wireless Communication", Cambridge University Press, 2nd edition, 2013. Theodre Rappaport, "Wireless Communication: Principle and Practice", Prentice Hall, 2nd edition, 2014. Kao-Cheng Huang, Zhaocheng Wang, "Millimeter Wave Communication System", Wiley-IEEE Press, 2nd edition, 2011. EzioBigleri, "MIMO Wireless Communications", Cambridge University Press, 1st edition, 2007. 	 Arslan, Hüseyin, ed. Cognitive radio, software defined radio, and adaptive wireless systems. Springer Science & Business Media, <u>2007 (263-284</u>) Thomas W.Rondeau, Charles W. Bostain, "Artificial Intelligence in Wireless communication", ARTECH HOUSE .2009 {pp1-51] Andrew Goldsmith, Wireless Communications, Cambridge University Press, 2005. Mischa Dohler, Jose F. Monserrat Afif Osseiran " 5G Mobile and Wireless Communication Technology", Cambridge University Press 2016.
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Learning Asse	essment										
	Bloom's				Final Examination	n (50% weightage)					
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100	0 %	10	0 %	100	0 %	10	0 %

CLA – 4 can be from any combination of these: Assignments, Seminars, Tech Talks, Mini-Projects, Case-Studies, Self-Study, MOOCs, Certifications, Conf. Paper etc.,

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. Sabitha Gauni, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

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Course Code	18ECE221T Name RADAR AND NAVIGATIONAL AIDS					ourse egory	, E			F	rofess	ional E	Electiv	e				L 3	<u> </u>	P 0	C 3
Pre-requ Cours		18ECC205J	Co-requisite Courses	Nil		gress ourse							N	il							
Course Of	fering Department	Electro	nics and Communication Engineering	Data Book / Codes/Standards								Nil									
	arning Rationale (CL		L	earnir					Prog		Learni			nes (F	PLO)						
-	CLR-1: Get introduced to basics of Radar System						3	1	2	3 4	5	6	7	8	9	10	11	12		14	15
CLR-1: Get Introduced to basics or Radar System CLR-2: Impart the knowledge of different types of Radar CLR-3: Analyze the various detection schemes CLR-4: Understand the Radar transmitters and Receivers CLR-5: Understand the fundamentals of navigation system CLR-6: Acquire knowledge on theoretical concepts and analysis techniques related to different types of Radar and variou navigational aids					Thinking (Bloom)	Expected Proficiency (%)	Attainment (%)	Engineering Knowledge	walysis	& Development s Design Research	Modem Tool Usage	& Culture	ent & Sustainability		ndividual & Team Work	cation	Project Mgt. & Finance	ife Long Leaming	Professional Achievement	a la	3: Analyze & Research
Course Le	earning Outcomes (C	LO): At the e	le to:	Level of T	Expected	Expected	Engineeri	Problem Analysis	Design & I Analvsis I	Modem To	Society &	Environment	Ethics	Individual	Communication	Project Mę	Life Long	<u>н</u>	- 2: nique	PSO-3:,	
CLO-1 :	CLO-1 : Gain knowledge about Radar theory and Range equation					80	70	Н	Μ		-	-	-	-	-	-	-	-	-	-	-
CLO-2: Apply Doppler principle to Radars and hence understand the working principle of different types of Radar					2	85	75	-	-		-	-	-	-	-	Н	-	-	М	-	-
CLO-3 :					2	75	70		Н		-	-	-	I	-	-	-	-	М	-	-
CLO-4 :					2	85	80		Н		-	-	-	-	-	-	-	-	-	-	Н
CLO-5 :					2	85	75		М		-	-	-	-	-	М	-	-	М	-	-
CLO-6 :					2	85	75	Н	Н		-	-	-	-	-	Н	-	-	-	-	Н

Du	ration	Introduction To Radar Equation	MTI And Pulse Doppler Radar	Detection Of Signals In Noise	Radar Transmitter And Receiver	Radio Navigation
ł)	nour)	9	9	9	9	9
S-1	SLO-1	Introduction-Basic Radar	Introduction to Doppler Radar	Detection of Signals in Noise -Detection Criteria	Radar Transmitters and Receivers.	Introduction - Four methods of Navigation Positioning- Errors in Direction Finding
0-1	SLO-2	Radar Frequencies -Applications of Radar	Introduction to MTI Radar	Probabilities of Detection and False Alarm	Linear Beam Power Tubes-Reflex Klystron	Line of sight Distance measurement
S-2	SLO-1	The Simple form of Radar Equation	Delay –Line Cancellers	Matched Filter Receiver	Linear Beam Power Tubes-TWT	Terrestrial Radio Navigation systems
5-2	SLO-2	Tutorials	Delay –Line Cancellers	Derivation of Matched filter frequency response	Solid State RF Power Sources	Radio transmission and Reception
S-3	SLO-1	Radar Block Diagram	Doppler Filter Banks	Automatic Detector	Magnetron - Crossed Field Amplifiers	System design considerations-System Performance Parameters
3-3	SLO-2	Receiver Noise	Digital MTI Processing	Constant-False-Alarm Rate Receivers	Magnetron - Crossed Field Amplifiers	The Loop Antenna - Adcock Direction Finders
	SLO-1	Signal-to-Noise Ratio	Block Diagram of Digital MTI Doppler Signal Processor	Signal Management	Other RF Power Sources	Direction Finding at Very High Frequencies - Automatic Direction Finders
S-4	SLO-2	Integration of Radar Pulses	Moving Target Detector - Limitations to MTI Performance	Propagation Radar Waves- Atmospheric Refraction	Other aspects of Radar Transmitter	VHF Omni Directional Range(VOR) - VOR Receiving Equipment - Range and Accuracy of VOR
S-5	SLO-1	Radar Cross Section of Targets-Simple Targets	Pulse Doppler Radar	Standard propagation	The Radar Receiver	Hyperbolic Systems of Navigation-Loran
3-0	SLO-2	Radar Cross Section of Targets-Complex Targets Transmitter Power	High, Medium and Low prf Doppler	Nonstandard Propagation	Receiver noise Figure	Loran-C

S-6	SLO-1	Radar cross Section Fluctuations	Other Doppler Radar Topics	Ambiguity Diagram	Receiver noise Figure	The Decca Navigation System -Decca Receivers
0-0	SLO-2	Swerling Target Model	Tracking with Radar	Ambiguity Diagram	Super heterodyne Receiver	Range and Accuracy of Decca
	SLO-1	Transmitter Power	Mono pulse Tracking	Pulse compression	LNA and Mixers	TACAN
S-7	SLO-2	Pulse Repetition Frequency	Two Coordinate amplitude comparison monopulse tracking	Linear FM pulse compression	Duplexers	TACAN Equipment
S-8	SLO-1	Antenna Parameters	Conical Scan and Sequential Lobing	Binary Phase Coded pulse compression	Receiver Protectors	Case study on Airborne Tactial networks- Instrument Landing System
3-0		System losses-Microwave plumbing loss, Antenna loss, Signal Processing loss	Limitations to Tracking Accuracy	Questionnaire	Receiver Protectors	Case study on Airborne Tactial networks- Instrument Landing System
S-9	SLO-1	System losses-Doppler processing, Collapsing, Operator loss, propagation Effects	Case study on weather radars	Introduction to clutter	Radar Displays	Introduction to satellite Radio Navigation-
	SLO-2	Other Radar Equation Considerations	Case study on weather radars	Surface Clutter Radar equation	Surprise Test	Navstar Global Positioning System (GPS)

Learning Resources	1. 2. 3.	Merrill I. Skolnik," Introduction to Radar Systems", 3rd Edition Tata Mc Graw-Hill 2008 R.B. Underdown and David Cockburn, "Ground Studies for Pilots: Radio Aids", sixth Edition, Blackwell Publishing, 2011. Myron Kayton, Walter R.Fried, "Avionics Navigation Systems", second Edition, Wiley- India Edition, 2010.	5. 6. 7.	N E J F E V
	4.	N.S.Nagaraja, "Elements of Electronic Navigation Systems", 2nd Edition, TMH, 2000.		V

- Mark, Richards.A, "Fundamentals of radar signal processing", Mc-Graw Hill, Electronic Engineering, 1st Edition, 2005.
- Jenny L. Reed, Aaron D. Lanterman, John M. Trostel," Tutorial: Weather Radar: Operation and Phenomenology", IEEE Aerospace and Electronic Systems Magazine, Vol: 32, 7, 2017.
 Bow-Nan Cheng, Frederick J. Block, B. Russ Hamilton, David Ripplinger, Chayil Timmerman, Leonid
- Z. Bow-Nan Cheng, Frederick J. Block, B. Russ Hamilton, David Ripplinger, Chayil Timmerman, Leonid Veytser, and Aradhana Narula-Tam," Design Considerations for Next-Generation Airborne Tactical Networks, IEEE Communications Magazine, May 2014.

Learning As	sessment														
	Bloom's		Continuous Learning Assessment (50% weightage)												
		CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	(10%)#		n (50% weightage)				
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice				
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-				
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-				
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-				
	Total	100	0 %	10	100 % 100 %			100) %	100 %					

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Course Code	18ECE222T	Course Name	ADHOC A	ND SENSOR NETWORKS	-	ourse tegory	Е		Professional Elective					Professional Elective						L 3	T 0	P 0	C 3
Pre-requisi Courses	NII		Co-requisite Courses	Nil		Cou	ressive Irses	Nil															
Course Offer	ing Department	Electronics a	and Communication Engine	ering Data Book / Codes/Standar	ds	Nil																	
Course Learn	ning Rationale (CL	R): The purpose	of learning this course is to	:		Lea	rning				Р	ogran	n Learn	ing O	utcome	s (PLC))						
CLR-1 : Uti	lize the Ad hoc Netw	vorks and its variou	is routing protocols			1	2 3	1	2	3	4	5 6	i 7	8	9 1) 11	12	13	14	15			
CLR-2 : Lea	arn the MAC Layer a	and the concept of	Quality of Service										ý						ent	с <mark>н</mark>			
CLR-3 : An	alyze energy manag	gement in Ad hoc N	letworks			Ê	<u> </u>				arch		Sustainability						lem	Research			
CLR-4 : Ide	ntify insights of Sen	sor network				(Bloom)	Proficiency (%) Attainment (%)	000	þ	ent	Rese		aina		Work	e		_	anag				
CLR-5: An	alyze various aspec	ts Hybrid networks	and routing configuration			9 (B	enc		s	evelopment	Å.	age	Sust		2	Finance	<u> </u>	one	t Mana	e Se			
CLR-6 : Ex	pose to the differen	t types of adhoc n	etwork routing protocols ar	nd sensor networks		kinę	Proficiend Attainmen	K l	lysi	velo	Design, I	ool Us	2 ~~		Team	2 ~	i ie	ess	oject	Analyze			
			**			Thinking	d Pri		Analysis	& De	De	⊢ a	ment		~ ×	Mat Mat	d Le:	Professional	. Prc	3: An			
Course Learr	ning Outcomes (CL	O): At the end of	f this course. learners will b	e able to:		el of	pected	inconina Knowlada o	blem	sign 8	alysis,		vironment	ics	ividual & Tea	piect Mat	Fong	0-1: iever	0 – 2: Proj chniques	0-3			

		F	-	9	÷	<	~~	-	Ĕ	∞ ŏ	Ë		m	·≅	~~		αĔ.	. e	
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expecter	Expecter	Enginee	Problem	Design 8	Analysis	Modem .	Society &	Environr	Ethics	Individua	Commur	Project N	Life Lonç	PSO-1: Achiever	PSU – z Techniqi	PSO - 3
CLO-1 :	Acquire knowledge about Ad hoc Networks and various routing protocols used in Ad hoc networks	3	80	75	Н	М	L	М	-	-	Н	-	-	-	-	М	-	-	Н
CLO-2 :	Analyze the various functional areas such as MAC Layer and QOS	3	80	70	Н	М	-	М	-	-	Н	-	-	-	-	М	М	-	Н
CLO-3 :	Identify energy management in Ad hoc Networks	3	75	70	L	Н	-	М	-	-	М	-	-	-	-	Н	-	-	L
CLO-4 :	Analyze the Sensor network	3	80	75	Н	L	-	М	-	-	-	-	-	-	-	-	-	М	Н
CLO-5 :	Identify Hybrid networks and routing configuration	3	80	70	-	-	Н	М	-	-	М	-	-	-	-	-	-	-	-
CLO-6 :	Understand the various types of adhoc networks and sensor networks	3	80	70	Н	М	-	L	-	-	Н	-	-	-	-	М	-	-	Н

Durati	on (hour)	9	9	9	9	9
S-1	SLO-1	Cellular and Ad hoc Wireless Networks	Quality of service in Ad hoc wireless networks, Real-Time Traffic support	Energy Management-Needs	Sensor Networks, Applications. Comparison with Ad hoc network,	Hybrid wireless network, Introduction, classification
5-1	SLO-2	Applications of Ad hoc Wireless Networks	Issues and challenges in providing QoS	Classifications of Energy Management Schemes	Issues, challenges in designing sensor network Sensor Network Architecture	Multi-hop cellular network (MCN) Architecture
	SLO-1	Issues in Ad hoc Wireless Networks	Classifications of QoS solutions	Battery Management Scheme-Overview,	Layered Architecture, Clustered Architecture	Mobile assisted data forwarding (MADF) Architecture
S-2	SLO-2	MAC Protocol for Ad hoc Networks Issues in Designing and Design Goals	MAC Layer solution-cluster TDMA, IEEE 802.11e, DBASE	Data link layer solution-Lazy packet scheduling scheme,	Data Dissemination, Flooding, Gossiping, Rumor Routing, Sequential Assignment Routing	Hybrid wireless Network (HWN) Architecture
S-3		Classifications of MAC protocols-Floor Acquition Multiple Access protocols	Network Layer solution-QOS routing protocols,	Battery Aware MAC protocol	Cost field approach	Routing in Hybrid wireless network Base assisted ad hoc routing (BAAR)
3-3	SLO-2	Collision Avoidance Time Allocated Protocol	Ticket Based QOS Routing protocols,	Network Layer solution	Data Gathering, Direct Transmission, Binary scheme	Operation of BAAR protocol
S-4		Routing Protocol for Ad hoc wireless network-Classification	Predictive location based QOS routing	Transmission Power Management Schemes-Data link layer solution	Chain Based Three level scheme	Base driven multi-hop bridging protocol (BMBP)-Message used
3-4	SLO-2	Table driven Routing Protocols-Wireless Routing Protocol	QOS frame work	Dynamic power adjustments policies, Distribute topology control Algorithm	MAC protocols for sensor Networks-Self organizing MAC, CSMA Based MAC	BMBP procedure
S-5		On demand routing protocols-Dynamic Source Routing protocol	QOS models	Construct distributed power control loop, Centralized Topology control Algorithm	Location discovery-Indoor and sensor network localization	Issues in pricing Multi-Hop wireless networks
3-0	SLO-2	Multicast Routing Architecture Reference model	QOS Resource Reservation Signaling	Network layer solution-common power protocol	Quality of Sensor Networks-coverage,	Pricing in Multi-Hop wireless WANs
S-6	SLO-1	Tree Based Routing	INSIGNIA-QOS framework	Minimum power consumption Technique	Exposure	Pricing in Ad hoc Wireless Networks

	SLO-2	Mesh Based Routing	Operation of INSIGNIA framework, Advantages and disadvantages	Minimum battery cost Routing	Recent Trends in Sensor Networks-Energy	Power control scheme in Hybrid Wireless Networks, Issues in using variable power in IEEE 802.11
0.7	SLO-1	Energy Efficient Multicasting-Routing protocols	INORA-Coarse feedback scheme,	Higher Layer solution	Transport Layer Issue	Power optimization scheme
S-7	SLO-2	Cluster Adaptation of Multicast protocols	Class based fine feedback scheme	System power management scheme, Processor power management	Security-Localized Encryption and Authentication protocols (LEAP)	Load Balancing in Hybrid Wireless Networks
S-8	SLO-1	Multicast with QOS Guarantees-Real Time Multicasting Protocols	SWAN-Model	Power saving Mode Power Aware Multi-Access Signaling	Intrusion Tolerant Routing in Wireless Sensor Network (INSENS)	Preferred Ring Based Routing Scheme
3-0	SLO-2	Priority Scheduling Protocols	Advantages and Disadvantages	Addition of separate signaling scheme	Real –Time communication	Preferred inner Routing Scheme(PIRS)
S-9	SLO-1	Application Dependent Multi Cast Routing- Role Based,	Proactive RTMAC framework	Device power Management Scheme-Low Power Design of Hardware	SPEED Protocol	Preferred outer Ring Routing Scheme (PORS)
3-9	SLO-2	Content Based, Location Based	Advantages and Disadvantages	Hard Disk Drive (HDD) power consumption	RAP protocols	Preferred Destination/Source Ring Based Routing Scheme

Learning 2004 Resources 2. Feng Zhao, LeonidasGuibas, Wireless Sensor Networks, 1st ed., Morgan Kaufman Publishers, 2004 C.K.Toh, Ad hoc Mobile Wireless Networks, 7th ed., Pearson, 2002
 Thomas Brag, Sebastin Buettrich, Wireless Mesh Networking, 3rd ed., O'Reilly Publishers, 2007

Learning Ass	sessment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weigl	ntage)			Einal Examination	n (50% weightage)
	Level of Thinking	CLA – 1	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		i (50 % weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	30 %		30 %		30 %	_	30 %	-	30%	
Level I	Understand	50 78	-	50 78	-	50 78	-	50 78	-	50%	-
Level 2	Apply	40 %	_	40 %	_	40 %	_	40 %	_	40%	_
	Analyze	40 70	-	40.70	_	40 70	_	40 70	_	4070	_
Level 3	Evaluate	30 %		30 %		30 %		30 %		30%	
Level 5	Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
Total 100 % 100 % 100 %) %	100) %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mrs. S. T. Aarthy, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE223T	Course Name	Satellite Co	mmunication and Broadcasting	Course Category	Е	Professional Elective	L 3	Т 0	P 0	C 3
Pre-requisite Courses		18ECC205J	Co-requisite Courses	Nil	Progressiv Courses	e	Nil				
Course Offer	ing Department	Electron	ics and Communication Engineer	ing Data Book / Codes/Standards			Nil				

Course Lo	earning Rationale (CLR):	The purpose of learning this course is to:		earni	ng						Prog	ram L	earn	ing O	utcor	nes (F	PLO)				
CLR-1 :	Understand the orbital and	functional principles of satellite communication systems	1	2	3	Ī	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Architect, interpret, and sel	ect appropriate technologies for implementation of specified satellite communication systems																	ent		
CLR-3 :	Analyze and evaluate a sat	ellite link and suggest enhancements to improve the link performance											/						veme	iii ii	÷
CLR-4 :	4: Select an appropriate modulation, multiplexing, coding and multiple access schemes for a given satellite communication link		(Bloom)	(%)	Attainment (%)		dge		ant	search			Sustainability		ork	1	90		e.	manageme	Research
CLR-5 :	Specify, design, prototype a	and test analog and digital satellite communication systems as per given specifications	8	, ancy	Jent		vleo		Development	Rese	age	0	uste		Team Work		Finance	g	sional	Μa	<u>م</u>
CLR-6 :	Utilize the concepts in optic	al communication for the understanding of engineering and technology	, ind	Proficienc	ainn		, No	ysis	/elo	Design,	S	Culture			ear	Б	ъ М	Leaming	essi SSi	ect	Analyze
Course L	earning Outcomes (CLO):	At the end of this course, learners will be able to:	el of Thinking	cted	Expected Att		Engineering Knowledge	^o roblem Analysis	Design & De	nalysis, De	odern Tool	Society & Cu	Environment &	S	Individual & ⁻	ommunication	Project Mgt.	ong	D-1: Profes	- z: nigue	0 – 3: An
OULSC E	curning outcomes (oro).		lev l	Expec	Exp		Еŋ	Pa	Des	Ana	Mo	Soc	Env	Ethics	Indi	Ğ	6 Q	Life L	PSO 1	ν	PS(
CLO-1 :	Understand the principles,	concepts and operation of satellite communication systems	2	80	70		Н	Н	М	Н	Н	L	L	L	М	Н	L	Μ	Н	-	-
CLO-2 :	Gain the knowledge of Sat	ellite orbits and launching, link design, link availability and perform interference calculations	2	85	75		Н	Н	М	Н	Н	L	Г	L	М	Н	L	Μ	Н	-	-
CLO-3 :	Analyze the concepts of Sa	tellite systems in relation to other terrestrial systems	2	75	70	Ī	Н	Н	М	Н	Н	L	L	L	L	Н	L	Μ	Н	-	-
CLO-4 :	Evaluate the performance of	f various channel access schemes	2	85	80		Н	Н	М	Н	Н	L	L	L	L	Н	L	Μ	Н	-	-
CLO-5 :	Familiarize with application	s of satellites and compression standards	2	85	75		Н	Н	М	Н	Н	L	L	L	L	Н	L	Μ	Н	-	-
CLO-6 :	Apply their idea in Satellite	communication module	2	80	70		Н	Н	М	Н	Н	L	L	L	М	Н	L	Μ	Н	-	-

	iration	Satellite Orbit	Link Design	Space and Earth Segment	Multiple Access Techniques for Satellite Communication	Broadcast and Services
U,	nour)	9	9	9	9	9
S-1	SLO-1	Satellite Orbit	Link Design	Space Segment	Concepts of Multiple Access techniques, types	Concept of Broadcasting satellites
5-1	SLO-2	Kepler's law	EIRP	Basic concept of space segmen	Single Access	Direct Broadcasting Satellite
S-2	SLO-1	Earth - Orbiting satellites terms	Transmission Losses	Power Supply	Pre assigned FDMA	Orbital Spacing
3-2	SLO-2	Types of satellites	Link Power Budget equation	Altitude control	Demand Assigned FDMA	Power ratings
S-3	SLO-1	Orbital elements	System Noise	Station keeping	SPADE system	Frequency and polarization
3-3	SLO-2	Orbit Perturbations	Carrier to noise ratio	Thermal Control	TWT amplifier operation	Transponder Capacity
S-4	SLO-1	Inclined Orbits	Types of FEC	TT&C Subsystems	Downlink analysis	Bit rate
3-4	SLO-2	Sun synchronous orbits	Computer-Aided Design	Antenna subsystem	ТДМА	MPEG

S-5	SLO-1	Constellation:Geo stationary satellites	Uplink	Transponders	Reference bursts	Forward Error Correction
3-0	SLO-2	Non geostationary constellation	saturation flux density, input backoff	Wideband Receiver	Preamble, Postamble	Outdoor Unit
S-6	SLO-1	Launching of Geostationary satellites	Down Link	Earth Segment	Carrier recovery	Indoor Unit
3-0	SLO-2	Launch vehicle Types	output backoff, TWTA output	Basic concept of Earth segment	Network synchronization	Downlink Analysis
S-7	SLO-1	Antenna Look angles	Effects of rain	Receive only home TV system	Pre assigned TDMA	Uplink Analysis
3-1	SLO-2	Sun transit outage	Inter modulation Noise	Community antenna TV system	Demand assigned TDMA	Satellite Mobile services
S-8	SLO-1	Solving Problems	Solving Problems	Solving Problems	CDMA	VSAT
5-8	SLO-2	Solving Problems	Solving Problems	Solving Problems	Direct Sequence Spread Spectrum , CDMA throughput	GPS
S-9	SLO-1	Solving Problems	Solving Problems	Solving Problems	Solving Problems	Solving Problems
3-9	SLO-2	Solving Problems	Solving Problems	Solving Problems	Solving Problems	Solving Problems

Learning Resources

 Dennis Roddy, "Satellite Communications", Tata Mc-Graw Hill Publications, 4th Edition, 13th Reprint, 2014
 TIMOTHY PRATT, CHARLES BOSTIAN JERMEY ALLNUTT, Satellite Communications, John Wiley, Singapore, 2nd Edition, reprint 2013.

8. MadhavendraRichharia, Leslie David, "Satellite Systems for Personal Applications Concepts and

Technology", Wiley-Blackwell, 1st Edition, 2010.
Louis J. IppolitoJr, "Satellite Communications Systems Engineering", John Wiley and Sons , Ltd, Publication, 1st Edition, 2008

Learning Ass	sessment													
	Bloom's		Continuous Learning Assessment (50% weightage)											
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#		n (50% weightage)			
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-			
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-			
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-			
	Total	Total 100 % 100 % 100 %) %	100) %	10	0 %			

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE224T	Course Name	CRYPTOGRAPHY AND NETWORK SECURITY	Course Category	Е	Professional Elective	L 3	T 0	P 0	C 3
Pre-requisit Courses	Nil		Co-requisite Courses	Progre Cour		Nil				
Course Offer	ing Department	Electron	ics and Communication Engineering Data Book / Codes/Standards	Nil						

Course L	earning Rationale (CLR):	The purpose of learning this course is to:	L	earni	ng						Prog	ram L	earni	ing O	utcor	nes (PLO)			
CLR-1 :	Utilize classical and moder	n encryption methods	1	2	3	-	1	2	3	4	5	6	7	8	9	10	11	12	13 14	15
CLR-2 :	Utilize the different key gen	eration standards											y						ent	ch
CLR-3 :		es in authentication of information	Ê		-					arch			bilit						lem	sear
CLR-4 :	Analyze the aspects in netw	vork security	(Bloom)	(%) <i>k</i>	t (%)		dge		ent	Se			Sustainability		Work		ce		onal Management	Research
CLR-5 :	Identify the effect of various	malwares and counter measures	9 (B	enc	nen		wle	s	m d	, Re	age	Ð	Sust		m M		Finance	g	essional t ject Man	e Se
CLR-6 :	Understand various conver	ntional and modern cryptography techniques with its added security features	Thinking	Proficiency	Attainment		Knc	alysi	Development	Design,	Usi	Culture	∞ŏ		Team	io	&F	aming	essic t oject	es Analyze & I
			Thir		i Ati		ing	Ana	De	De	[ool	ہ م	iner			icat	Mgt.	Le	Profe	Ies An
Course L	earning Outcomes (CLO):	At the end of this course, learners will be able to:	Level of	Expected	Expected		Engineering Knowledge	Problem Analysis	Design &	Analysis,	Modern Tool Usage	Society 8	Environment	Ethics	Individual &	Communication	Project N	Life Long	PSO-1: Achiever PSO - 2	PSO - 3:
CLO-1 :	Identify the methods of class	sical and modern Encryption	3	80	75		-	-	М	L	-	-	-	-	-	-	-	Н		Н
CLO-2 :	Identify the concepts of Nul	nber theory, Key generation and distribution standards	3	80	70		L	Н	М	1	-	-	-	-	1	-	I.	-	- H	-
CLO-3 :	Analyze Message authentic	ation and Digital Signature algorithm.	3	75	70		-	М	L	I	-	-	-	-	I	-	I	Н		М
CLO-4 :	Obtain information about va	arious forms of network security	3	80	75		Н	М	L	-	-	-	-	-	-	-	-	-		М
CLO-5 :	Analyze the effects of intrus	sion, viruses, firewalls and various levels of system security	3	80	70		L	-	-	-	-	-	-	-	-	-	-	М	- M	-
CLO-6 :	Obtain the knowledge abou	t various encryption techniques, standards and security aspects	3	80	70		М	-	-	L	-	-	-	-	-	-	-	-		М

Durati	on (hour)	9	9	9	9	9
	SLO-1	Security Services Mechanisms	Number Theory	Basics of Message authentication codes	IP Security	Intruders
S-1	SLO-2	Attacks	Basics of Modulo operations, additive and multiplicative inverse	Basics of Message authentication codes	Overview of techniques	Intrusion
• •	SLO-1 Network Security Model		Euclidean algorithm	Requirements of MAC	Architecture	Intrusion Detection
3-2	S-2 Block cipher, stream cipher, symmetric an Assymetric		Extended Euclidean algorithm	MAC logic	Authentication Header	Techniques
S-3	SLO-1	Conventional Encryption techniques	Fermet's theorem	MD5 Logic, MD5 Compression Function,	Authentication Protocols	Password Management
3-3	SLO-2	Substitution and transposition techniques	Euler's theorem	MD4, Strength of MD5	Mututal authentication, one way authentication	Techniques
S-4	SLO-1	Steganography	Key cryptography	Requirements for a Hash Function, simple Hash Function,	Encapsulating Security Payload	Viruses
3-4	SLO-2	Basics of LSB, Histogram,DE techniques	Key cryptography	Birthday Attacks, Block Chaining Techniques	Encapsulating Security Payload	Worms
S-5	SLO-1	DES	RSA	Securities	Security Associations	Advanced Security
3-5	SLO-2	Algorithm and examples	Algorithms and examples	HASH - MAC	Techniques overview	OS Security
S-6	SLO-1 SDES		Key distribution	Birthday Attack	Kerbros V4, V5 certificate	WLAN Security
3-0	SLO-2	Block cipher modes operation	Algorithms	SHA	Authentication Procedure	Ad hoc Network Security

S-7	SLO-1	Overview of IDEA	Key Management	Digital Signature standard		PGP	GSM Security					
3-1	SLO-2	Overview of Blowfish	Algorithms	Overview of blocks		Email Security	E-commerce Security					
S-8	SLO-1	Overview of RC5	Diffie Hellman key exchange	Digital Signature Algorithms		Web security requirements	Cloud Computing Security					
3-0	SLO-2	Overview of CAST-128	Diffie Hellman key exchange	Examples		SSL -TLS - SET	Introduction to Firewall					
S-9	SLO-1	Characteristics of advanced symmetric Block ciphers	Elliptic curve cryptography	Basics of proof		Port Scanning	Firewall-Types, configurations					
9-9		Characteristics of advanced symmetric Block ciphers	Elliptic curve cryptography	Proof of DSS Message Authen Codes.	tication	Port Knocking	Trusted System					
Learn Resou	•	 William Stallings, Cryptography & Netu Bruce Schneier, Applied Cryptography Eric Maiwald,Fundamentals of Networ 	r, 2 nd ed., 2015		Tata Mc0	BehrouzA.Forouzan, Debdeep Mukhopadhyay, Cryptography and Network Security, 2 ⁿ Tata McGraw Hill, 2010 Bernard Menezes, Network Security and Cryptography, Cengage Learning, 2010						

	Dia angle		Continuous Learning Assessment (50% weightage)											
	Bloom's	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – 3	3 (15%)	CLA – 4	(10%)#	Final Examinatio	n (50% weightage)			
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-			
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-			
Level 3	Evaluate Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-			
	Total	100) %	100) %	100	1%	100) %	10	0 %			

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Course Cor	Course Code 18ECE225T Course Name Information Theory an										Cours	e	F	Е		fassia	nal Ele	octivo		L	Т	Р	С
000136 000	uc		Jourse Maine		and obting					(Catego	ry	L		110	103310		01110		3	0	0	3
Pre-requ Cours			18MAB203T	Co-requisite Courses		Ni	il					Progr Cou	essive rses	•					Nil				
Course Offe	ering Dep	artment	Electr	onics and Communication Engineering	Data Bool	k / Cod	des/ S	Standard	s								Nil						
Course Lea	rning Rat	tionale (CLR):	The purpose of	learning this course is to:		L	.earni	ng					Pro	gram	Learn	ing Ou	utcom	es (PL	0)				
CLR-1: In	ntroduce s	ource coding in info	ormation theory			1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 : //	mpart the	fundamentals of err	or control coding tecl	hniques and their applications		Ê	(%	(%				сŀ			ī₹								
		e noisy channel coo				0	, ,	it (°	Φ		÷	earc			lidbil		¥					les	
	lssess the ituations	performance of bo	th block and convolu	tional coding schemes in different practical		Thinking (Bloom)	ficienc	Attainment (%)	owledg	si.	Jesign & Development	Analysis, Design, Resear	age	е	Sustainability		ndividual & Team Work		Project Mgt. & Finance	ing	-1: Professional vement	st echniqu	ze &
CLR-5 : D	Derive Sha	nnon's fundamenta	l channel capacity re	sults		hin	P D	Atta	۲ ۲	alys	evel	esig	ol Us	Culture	nt &		Teã	ation	<u>م</u>	eam	nt se	2: Project ement Tec	naly
CLR-6 : K	lnow abou	it channel and impa	irments channel and	how to mitigate them		L T	ed	eq	, un	١٩n	8 D	õ	Toc	80	mer		ଞ	nice	Mgt	٦ و	Pro	2: Pl	بة R
Course Lea	rning Ou	tcomes (CLO):	At the end of th	is course, learners will be able to:		Level of .	Expected Proficiency (%)	02 Expected /	Engineering Knowledge	Problem Analysis	Design	Analysis	Modern Tool Usage	Society & I	Environment &	Ethics	Individu	Communication	Project	Life Long Leaming	PSO-1: Profe Achievement	PSO – 2 Manage	PSO – 3: Analyze & Research
CLO-1 : C	Comprehe	nd various source c	oding schemes			2	80	70	-	Н	-	-	-	-	-	-	-	-	-	-	-	-	Н
CLO-2: A	pply varia	ble length codes fo	r source coding			2	85	75	-	Н	-	-	-	-	-	-	-	-	-	-	-	-	Н
			ror detection and cor			2	75	70	-	Н	-	-	-	-	-	-	-	-	-	-	-	Η	-
				& cyclic codes for error detection and correction	on.	2	85	80	-	Н	-	-	-	-	-	-	-	-	-	-	-	Н	-
			ce using Information			2	85	75	-	Н	-	-	-	-	-	-	-	-	-	-	-	-	Η
CLO-6 : A	nalyse ar	y type of channel a	nd select coding tec	hniques to improve channel performance					-	Н	-	-	-	-	-	-	-	-	-	-	-	-	Η

Duratio	n (hour)	Source coding	Variable-Length Codes	Error Detecting and Error Correcting Codes	Convolutional Codes	Entropy and Channel Capacity
	()	9	9	9	9	9
	SLO-1	Introduction to Information theory	Unique decoding	Hamming codes Generation	Convolutional codes introduction	Entropy
S-1	SLO-2	Model of signaling system	Rules and construction of Unique decoding	Hamming code checking	Convolutional codes generation	Mathematical properties
	SLO-1	Block Diagram	Instantaneous codes	Hamming weight	Convolutional encoder	Entropy and coding
S-2	SLO-2	Mathematical models for information sources	Construction of Instantaneous codes	Hamming distance	Encoder for different rates	System entropies
	SLO-1	Encoding a source alphabet	The Kraft's inequality	Minimum distance decoding	code tree formation	Mutual information
S-3	SLO-2	Source coding	Shortened block codes	Linear block codes Generator polynomial	code tree formation	Example Problem solving- Mutual information
S-4	SLO-1	ASCII code	The McMillan's Inequality	Linear block codes Generation	state diagram generation	Shannon-Fano coding
5-4	SLO-2	Code Formation for an information	Huffman codes	Linear block codes Decoding	state diagram generation for different rates	Example Problem solving- Shannon- Fano coding
S-5	SLO-1	Radix r code	Huffman codes -special cases	Example Problem solving- Linear block codes	trellis diagram for decoding convolutional codes	Classification of channels
3-0	SLO-2	Different examples for different 'r'	Extensions of a code	Cyclic codes Generator polynomial	trellis diagram for decoding convolutional codes	Channel Capacity
S-6	SLO-1	Simple parity checks – Generator	Huffman codes Radix r	Cyclic codes Generation	Maximum likelihood decoding of convolutional codes	Calculation of channel capacity

	SLO-2	Simple parity Checker	Example Problem solving in Huffman coding		Maximum likelihood decoding of convolutional codes	Types of channel
S-7	SLO-1	CRC codes-Generation	Example Problem solving in Huffman coding-special cases	Example Problem solving - UVCIIC CODES	Sequential decoding of convolutional codes-	Conditional mutual information
3-1	SLO-2	CRC codes-Checking	Noise in Huffman coding probabilities	Example Problem solving- Syndrome calculation	Sequential decoding of convolutional codes	Random encoding
S-8	SLO-1	Single parity checks	Use of Huffman codes	Block encoders	Applications of Viterbi decoding	Average random code
3-0	SLO-2	Double parity checks	Hamming coding	Block Decoders	Viterbi decoding	Fano bound
S-9	SLO-1	Miscellaneous codes	Example Problem solving in Hamming coding	Assignment Problems in Linear Block codes	Turbo codes	Converse of Shannon's theorem
3-9		Problems in source coding with different radix and parity	Assignment Problems in Huffman and Hamming coding	Assignment Problems in Cyclic codes	Assignment Problems in Convolutional codes	Assignment Problems in Channel capacity and mutual information

ſ			1. Kennedy, "Electronic Communication systems", McGraw Hill, 4th Ed., 1999		
	Learning	2.	Daniel Costello, and Shu Lin, "Error Control coding fundamentals and applications", Prentice Hall Inc, 1983		Proakis J. G., "Digital Communications", McGraw Hill Inc., 4th Edition, NY, 2001. Simon Haykin, "Communication System", Wiley, 2008
	Resources	3.	Hamming, Richard W, "Coding and Information Theory", Prentice Hall Inc., NJ, 1986.	5.	Sinon Haykin, Communication System, Wiley, 2006

Learning Assess	sment													
	Bloom's			Einal Examination	n (50% weightage)									
	Level of Thinking	CLA –	CLA – 1 (10%) CLA – 2 (15%) CLA – 3 (15%) CLA – 4 (10%)#											
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember	40 %		30 %		30 %		30 %		30%				
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-			
Level 2	Apply	40 %		40 %	_	40 %		40 %	_	40%				
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-			
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%				
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-			
Total 100 % 100 % 100 %										100 %				

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Course Code	18	BECE226T	Course Name	Optical Components, S	Systems and Networks		ourse tegor	- E				Profe	ession	al Ele	ctive				L 3	Т 0	P 0	C 3
Pre-req Cours			18ECC302J	Co-requisite Courses	Nil		ogress ourse								Nil							
Course O	ffering D	epartment	Electronics an	d Communication Engineering	Data Book / Codes/Standards								N	il								
Course L	earning F	Rationale (CLR	:): The purpose of	f learning this course is to:		L	.earniı	ıg				Р	rograi	n Lea	arning	Outco	omes	(PLO))			
CLR-1 :	Understa	and the basics	working principle of o	optical fibers, fiber modes configurati	ons and structures.	1	2	3	1	2	3	4	56	6	7 8	9	10	11	12	13	14	15
CLR-2 : CLR-3 :	network Acquire i receiver	components, s the basic know operation and	witches, EDFA, SOA ledge of fiber optical configuration.	A. receivers such as PIN APD diodes,	aser diodes. To learn the fiber optical noise performance in photo detector,	(Bloom)	ncy (%)	ent (%)	eb		nt	Research			sustainability	ork		e		Professional Achievement	Management	Research
CLR-4 :		v	, ,	modulators and other signal degrada	tion factors	cing (ficier	Attainment	owled	<u>.</u>	amqc	n, Re	age	D (SUSTA	Team Work		Finance	ing	ional	t Mar	∞ δ
CLR-5 : CLR-6 :			orking principle of W optical networks and			el of Thinking	Expected Proficiency (%)	Expected Atta	Engineering Knowledge	^o roblem Analysis	jn & Development	Analysis, Design,	⊢ o	w Cultu	nment &	ual &	<u>a</u>	Project Mgt. & F	ife Long Leaming	÷	– 2: Project I niques	– 3: Analyze
Course L	earning (Outcomes (CL	O): At the end of th	nis course, learners will be able to:		Level	БХ	Exp	Engi	Prob	Design	Anal	Modern	society -		ndiv	Com	Proje	Life [PSO	PSO - Techni	PSO
CLO-1 :	Familiari	ize with the fun	damentals of light tr	ansmission through fiber		2	80	70	H	M	-	-					-	-	-	Ĥ	-	H
CLO-2 :			degrades inside the connecterization	fiber due to physical effects and ext	ernally due to various factors like	2	85	75	Н	М	-	-				-	-	-	-	Н	-	Н
CLO-3 :	Understa	and the operat	ion of optical sources	s, amplifiers and detectors and there	by build transmitter and receiver circui	ts 2	75	70	Н	-	-	Н				-	-	-	-	Н	-	Н
CLO-4 :	Familiari	ize with optical	measurements for p	erformance analysis		2	85	80	Н	М	-	-	- A	1 1	И -	-	-	-	-	Н	-	-
CLO-5 :	Design a	a basic optical o	communication syste	em		2	85	75	Н	-	Н	-				-	-	-	-	Н	-	М
CLO-6 :	Acquire	fundamental co	ncepts on multichan	nnel system and related components		2	80	70	-	-	-	-	M			-	-	-	-	-	-	-

	ration	Optical Fibers and transmission characteristics	Optical Sources, Amplifier and Transmitter	Optical Detectors and receivers	Optical modulators, switches and OEICs	Optical communication systems
(r	lour)	9	9	9	9	9
	SLO-1	Elements of Optical fiber communication, Optical spectral bands	Introduction to Luminescence: Photo, electro, cathodo, injection luminiscence	Photo detection principle	Electro optic modulators	Point to point links
S-1	SLO-2	Optical fiber structure, Light Propagation in Optical fibers: Ray theory, Total Internal reflection, Skew rays, Fiber types: SI, GI, MM, SM	Plasma display, LCD	Photoconductor,		Digital and analog systems design considerations
S-2	SLO-1	Overview of Modes, Cutoff wavelength and V number,	LED: Choice of material,	Noise in photoconductors, SNR	Interferometry modulators	Digital link design,
5-2	SLO-2	Problems on v-number	LED Structures; Surface and Edge emitters,	Response time	Semiconductor optical amplifiers	Links power budget
S-3	SLO-1	Wave Equations for Step index fiber, Modal equation, Modes in SI fibers	Quantum efficiency and power, LED Characteristics	Problems on response time and SNR	Optical switching and logic devices	Rise time budget
3-3	SLO-2	Problems on V-number, modes	Problems on LED quantum efficiency	Problems on Photoconductor	Problems on modulators	Overview of analog links
S-4	SLO-1	Special Fibers introduction, Polarization Maintaining fibers,	Semiconductor Laser Diode, Operating principles,	Photodiode: PIN Photodiode	Optical switching	Radio over fibers

.

	SLO-2	Photonic Crystal fibers, Dispersion compensated fiber	Emission absorption and radiation	Avalanche photodiode	Logic devices	Key link parameters
	SLO-1	Attenuation Introduction	Population inversion	Detector performance parameters	Hybrid integration	Multichannel systems
S-5	SLO-2	Material Adsorption, Scattering, bending and core cladding losses	Optical feed- back, Threshold condition	Detectors for long wavelength operation	Monolithic integration	Need for multiplexing
S-6	SLO-1	Problems	External Quantum efficiency, LASER Characteristics	wavelength selective detection	Comparison of hybrid and monolithic	Operating principle of WDM
3-0	SLO-2	Overview of Signal dispersion in fibers	Problems on LASER quantum efficiency	Fundamental receiver operation	Slab waveguides	Operating principle of DWDM
S-7	SLO-1	Dispersion limitations, Intermodal dispersion Single mode Laser: VCSEL		Front end amplifier and decision circuit	Strip waveguides	WDM components
3-7	SLO-2	Intra-Modal dispersion: Material dispersion,	Introduction to Fiber Amplifiers	Functional block diagram of receiver circuit	Guided wave devices	Couplers/splitters
S-8	SLO-1	Waveguide dispersion and PMD	EDFA	Measurement standards, basic test equipment	Active filters	Isolators and circulators
3-0	SLO-2	Problems on Dispersion	SOA	Optical spectrum analyzer	Problems	Machzender interferometer
	SLO-1	Non linear effects : Non linear scattering, Kerr effects	Modulation characteristics and Driver circuits	Oprtical power meter	Integrated Transmitter	Fabry perot filters
S-9	SLO-2	Fiber alignment and Joint Loss, Fiber		OTDR	Integrated Receivers	Optical MEMS

Learning Resources	 Gerd Keiser, "Optical Fiber Communication" McGraw –Hill International, Singapore, 3rd edition, 2000 J. Wilson and JF B Hawkes "Optoelectronics – An Introduction" 3rd Edition PearsonEducation Taiwan Ltd 2010 Pallab Bhattachara "Semiconductors Optoelectronics Devices", 2nd Edition, Prentice Hall of India Pvt Ltd, New Delhi, 2009. Jasprit Singh " Optoelectronics- An Introduction to Materials and Devices", Mc Graw HillEducation India 2014. S C Gupta " Optoelectronics Devices and systems", 2nd Edition, Prentice Hall of India, 2015. 	 S O Kasap "Optoelectronics and Photonics: Principles and practices", 2nd Edition Person Education International, 2012. Rajiv Ramaswami, Kumar N. Sivaranjan, "Optical Networks A practical perspective", 2nd edition, Elsevier, 2004 Djafar K. Mynbaev, Lowell L. Scheiner, "Fiber-Optic Communications Technology", 1st edition, Pearson Education, 2001. John Powers, "An Introduction to Fiber optic Systems", 2nd edition, Irwin-McGraw Hill, 1999. J.Gowar, "Optical Communication System", 2nd edition, Prentice Hall of India, 2001.
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Learning Ass	sessment											
	Disamia			Conti	nuous Learning Asse	essment (50% weig	htage)			Final Eveninatio	- (EOO/ weighters)	
	Bloom's Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4 (10%)#		Final Examinatio	n (50% weightage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Lovel 1	Remember	40 %		30 %		30 %		30 %		30%		
Level 1	Understand	40 %	-	30 %	-	50 70	-	50 70	-	5078	-	
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%		
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	100	0 %	100	100 %		100 %)%	100 %		

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. B. Ramakrishna, SRMIST

2. Mr. Hari	harasudhan - Jo	nson Cor	ntrols, Pune	e, <u>hariharasudh</u>	nan.v@jci.com		2. Dr. Venkatesan, Sr. Scie	ntist, NIOT, Che	nnai,	venka	t@niot.i	<u>es.in</u>					2. L	Dr. Sh	nanth	i Princ	ce, Sł	RMIS	Т		
Course Code	18ECE32		Course Name		SOFTW	ARE DEFI	NED NETWORKS			ourse egory	, E	Prot	essio	nal Ele	ctive							_	L 3	T 0	P C 0 3
Pre-requ Cours	es		ECC303J		Co-requisite Courses		Nil		Progressive Nil																
Course Of	Burse Offering Department Electronics and Communication Engineering Data Book / Codes/St							ndards									lil								
Course Le	se Learning Rationale (CLR): The purpose of learning this course is to:								Le	earnin	g				I	Progra	m Le	arnir	ng Ou	utcom	nes (F	PLO)			
CLR-1 :								1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15	
CLR-3 : CLR-4 :	CLR-2: Understanding The Control Plane, Data Plane of SDN CLR-3: Analyze and understand various SDN controller CLR-4: Create insights to the standard OpenFlow for SDN CLR-5: understand the Network Programmability for SDN and SDN Open Source						l of Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment (%)	Engineering Knowledge	Problem Analysis	Design & Development	sis, Design, Research	m Tool Usage	ty & Culture	Environment & Sustainability		ndividual & Team Work	Communication	^o roject Mgt. & Finance	ong Learning	PSO–1: Professional Achievement	0 – 2: Project <u>nagement Techniques</u> 0 – 3: Analyze &		
Course Le	arning Outcom	s (CLO):	At the e	nd of this cours	e, learners will be	able to:			Level	Expe	Expe	Engir	Probl	Desiç	Analysis,	Modem	Society	Envir	Ethics	Indivi	Comr	Proje	Life Long	PSO- Achie	PSO PSO
CLO-1 :	understand the	DN archit	tecture and	l benefit					2	80	70	Н	-	-	-	Н	-	-	-	-	-	-	-	-	Н -
	Analyze and cor	pare avai	ilable SDN	controller					2	85	75	Н	-		Η	Н	-	-	-	-	-	-	-	-	- H
CLO-3 :	CLO-3 : Program the SDN elements						2	75	70	Н		Н	Η	-	-	-	-	-	-	-	-	Н			
							2	85	80	Н	Н	Н	Н	Н	-	-	-	-	-	-	-	-	- H		
								2	85	75	Н	-		Н	-	-	-	-	-	-	-	-	-		
CLO-6 :	-6: Understand standard OpenFlow							2	80	70	Н	-	Н	-	Н	-	-	-	-	-	-	-	-		

	iration	Basics of SDN	SDN Devices and Controller	OpenFlow, Programmability and the Management Interface	SDN Application and Use Case	SDN Implementation and Mobile Networks
()	hour)	9	9	9	9	9
S-1	SLO-1	Introduction to SDN-Evolution of Switches and Control Planes , Cost	How SDN Works-Fundamental Characteristics of SDN		SDN in the Data Center - Data Center Definition,Data Center Demands	SDN Open Source-Chapter-Specific Terminology ,Open Source Licensing Issues
	SLO-2	Introductionto SDN -SDN Implications for Research and Innovation	SDN Operation, SDN Devices	The OpenFlow Protocol,The OpenFlow Protocol	Tunneling Technologies for the Data Center.	Profiles of SDN Open Source Users ,OpenFlow Source Code,
S-2	SLO-1	need of SDN-Data Center Innovation,	SDN Controller		Path Technologies in the Data Cente Ethernet Fabrics in the Data Center	Switch Implementations , Controller Implementations SDN Applications
5-2	SLO-2	need of SDN- Data Center Needs	SDN Applications ,Alternate SDN Methods	Actions and Packet Forwarding,Messaging Between Controller and Switch		Simulation, Testing, and Tools, OpenStack, Example: Applying SDN Open Source .
S-3	SLO-1	Genesis of SDN-The Evolution of Networking Technology	General Concents of SDN Controller		Open SDN versus Overlays in the Data Center	SDN Futures-Current State of Affairs
	SLO-2	the Genesis of SDN- forerunners of SDN	VMware	OpenFlow 1.3 Additions and OpenFlow Limitations	Real-World Data Center Implementations	Potential Novel Applications of Open SDN
S-4	SLO-1	the Genesis of SDN- software Defined Networking is Born, Sustaining SDN Interoperability	Nicira	Introduction to Network Programmability and The Management Interface	SDN in Other Environments -Wide Area Networks. Service Provider and Carrier Networks	role of SDN in 5G- Drawback of hardware- based network functions.,Network Functions Virtualization (NFV) and Software Defined Networking (SDN) in

						5G
	SLO-2	Open Source Contributions, Legacy Mechanisms Evolve Toward SDN , Network Virtualization	VMware/Nicira	The Application-Network Divide	Campus Networks, Hospitality Networks	optimization models that aim at finding the optimal design for a mobile core network based on SDN and NFV
S-5	SLO-1	The Control Plane, Data Plane	OpenFlow-Related	Modern Programmatic Interfaces-Publish and Subscribe Interfaces, XMPP	Mobile Networks. In-Line Network Functions,	SDN and NFV Mobile Network Architectures
3-5	SLO-2	Moving Information Between Planes, Separation Importance	Mininet ,NOX/POX	Google's Protocol Buffers , Thrift ,JSON	Optical Networks	Dimensioning and Resource Allocation Problems
S-6		Distributed Control Planes-IP and MPLS, Creating the IP Underlay, Convergence Time	Trema, Ryu	I2RS 143 Modern Orchestration-OpenStack	SDN vs. P2P/Overlay Networks	Mobile Core Network Architecture
3-0	SLO-2	Load Balancing ,High Availability, Creating the MPLS Overlay,Replication	Big Switch Networks/Floodlight,	CloudStack, puppet	SDN Applications-reactive versus Proactive Applications ,Analyzing Simple SDN Applications ,	SDN Mobile Core Network Architecture
	SLO-1	Centralized Control Planes-Logical Versus Literal	Layer 3 Centric, L3VPN	Virtualization Virtualization and Data Plano	A Simple Reactive Java Application,Background on Controllers	NFV Mobile Core Network Architecture
S-7	SLO-2	ATM/LANE , Route Servers	Path Computation Element Server		Using the Floodlight Controller, Using the OpenDaylight Controller, Using the Cisco XNC Controller, Using the Hewlett-Packard Controller.	Data Plane Function Chains Analysis
S-8	SLO-1	Introduction to OpenFlow-Wire Protocol	Path Computation Element Server	Service Locations and Chaining	witch Considerations, Creating Network Virtualization Tunnels, Offloading Flows in the Data Center, Access Control for the Campus, Traffc Engineering for Service Providers	Control Plane Function Chains Analysis
	SLO-2	Replication ,FAWG (Forwarding Abstraction Workgroup)	Plexxi Plexxi Affinity	Non-ETSI NFV Work-Middlebox Studie	SDN Use Cases-Use Cases for Bandwidth Scheduling	requirements & challenges of SDN and NVF In 5G
S-9	SL0-1	Configuration and Extensibility, Architecture	Cisco OnePK	Embrane/LineRate	Big Data and Application Hyper- Virtualization for Instant CSPF	Existing Solutions
3-3	SLO-2	Hybrid Approaches , Ships in the Night ,Dual Function Switches	Relationship to the Idealized SDN Framework	Platform Virtualization	use Cases for Input Traffic Monitoring, Classification, and Triggered Action	future directions

Learning Resources	 Software Defined Networks: A Comprehensive Approach by Paul Goransson and Chuck Black, Morgan Kaufmann Publications, 2014 SDN - Software Defined Networks by Thomas D. Nadeau & Ken Gray, O'Reilly, 2013 Cho, Hsin-Hung, et al. "Integration of SDR and SDN for 5G." IEEE Access 2 (2014): 1196-1204. 	 Bouras, Christos, Anastasia Kollia, and Andreas Papazois. "SDN & NEV in 5G: Advancements and challenges." Innovations in Clouds, Internet and Networks (ICIN), 2017 20th Conference on. IEEE, 2017. Arsany Basta; Andreas Blenk; Klaus Hoffmann; Hans Jochen Morper; Marco Hoffmann; Wolfgang Kellerer, Towards a Cost Optimal Design for a 5G Mobile Core Network Based on SDN and NEV,,IEEE Transactions on Network and Service Management, 2017, Volume: 14, Issue: 4, Pages: 1061 - 1075
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Learning Asses	ssment											
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examination (50% weight		
	Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4 (10%)#				
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 4	Remember	10.0/		30 %		20.0/		20.0/		30%		
Level 1	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %		40%		
Level Z	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%		
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	100) %	100	100 %		100 %		0 %	100 %		

Learning As	sessment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weig	htage)			Final Evenination	(FOO/
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#	Final Examination	n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40 %		30 %		30 %		30 %		30%	
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %		40 %	_	40 %	-	40%	
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	4070	-
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%	
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100) %	10	0 %	10	0 %	10	0 %

Course Designers									
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts							
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. P. Vijayakumar, SRMIST							
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in								

Course Code	186(-321)		RF AND MICROWAVE SEM	EMICONDUCTOR DEVICES Course Category			Professional Elective	L 3	T 0	P 0	C 3
Pre-requisi Courses	18FCC1021		Co-requisite Courses		Progre Cour	ssive ses	Nil				
Course Offer	Course Offering Department		ics and Communication Engineering	Data Book / Codes/Standards	Nil						

Course Lo	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng					l	Progr	am L	earni	ng O	utcon	nes (F	PLO)				
CLR-1 :	Study microwave semiconductor materials and to understand the fundamental of electronic components under microwave signal	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14 1	15
CLR-2 :	Learn about microwave components and devices that are used in modern microwave radar and communication systems											Y							ent	earch
CLR-3 :	Know the characteristics and operation of microwave transistor.	Ê							arch			pilit							gem	sear
CLR-4 :	Know the fundamentals of RF power transistors and challenges	(Bloom)	y (%)	Attainment (%)		dge		ent	ese			Sustainability		Work		ce		7	anag	Rese
CLR-5 :	Discuss the main issues and challenges encountered in developing the products at microwave frequencies	8	enc	nen		Me	s	Development	, Re	Usage	Φ	Sust		ح ۲		Finance		iona		e S
CLR-6 :	Acquire deep understanding of development of RF and modern semiconductor devices	hinking	ofici	ainr		Хnc	Analysis	velo	Design,	S	Culture	~		Team	ation	∞ŏ	Leaming	ess	roject	alyze
		Thir	Å.			ing	Ana		De	Tool	& CL	Jent		જ	icat	Mgt.	Le	Prof	Pro	A
Course Lo	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of .	Expected	Expected		Engineering Knowledge	Problem	Design &	Analysis,	Modern 7	Society 8	Environment	Ethics	Individua	Commun	Project N	Life Long	PSO-1: Achiever	PSO – 2 Techniqu	PSO - 3
CLO-1 :	Understand the properties of Semiconductor Junction Diodes under microwave signals	3	80	75		Н	-	-	Н	-	-	-	-	-	-	-	-	Н	-	-
CLO-2 :	Analyze the development of negative resistance characteristics in tunnel diode and transit time devices	3	80	70		Н	-	-	М	-	-	-	-	-	-	-	-	Н	-	-
CLO-3 :	D-3 : Characterize the microwave components and circuits in terms of their performance parameters					Н	-	-	Н	-	-	-	-	-	-	-	-	Н	- 1	Н
CLO-4:						Н	-	-	М	-	-	-	-	-	-	-	-	М	-	-
CLO-5 :						Н	-	Н	-	-	-	-	-	-	-	-	-	Н	- 1	М
CLO-6 :	Understand the concepts of RF and semiconductor devices and apply in the design of electronic systems.	3	80	70		Н	Н	-	-	-	-	-	-	-	-	-		Н	- /	Н

		Semiconductor P-N Junction	Negative Resistance and Transit Time Devices	Microwave BJT Transistors	HEMT Transistors and RF Power Transistor	RF Package Design and Development
Durati	on (hour)	9	9	9	9	9
S-1	SLO-1	Review of properties of semiconductors	Negative Resistance Devices	Microwave Transistor	Introduction to HEMT	Introduction to RF Package
5-1	SLO-2	Review of properties of semiconductors	Negative Resistance Devices	High frequency limitations of BJT	Short channel effects	Introduction to RF Package
S-2	SLO-1	Transient and ac behavior of p-n junctions	Tunnel Diode, Tunneling process in p-n junction	Microwave bipolar transistors – introduction	Device operation	Thermal Management
0-2	SLO-2	Transient and ac behavior of p-n junctions	V-I characteristics and device performance	Microwave bipolar transistors – operation	Device operation	Thermal Management
S-3	SLO-1	Effect of doping profile on the capacitance of p-n junctions	MIS tunnel diodes	Hetero junction bipolar transistors	Device design	Mechanical Design
3-3	SLO-2	Effect of doping profile on the capacitance of p-n junctions	V-I characteristics and device performance	Basic principle of operation	Scaling issues	Mechanical Design
S-4	SLO-1	Noise in p-n junctions	Backward Diode	Kirk effect	Material Systems for HEMT Devices	Package electrical and electromagnetic Modeling
3-4	SLO-2	Noise in p-n junctions	V-I Characteristics	High frequency response	GaAs HEMT	Package electrical and electromagnetic Modeling
0.5	SLO-1	Varactor diode	Transferred Electron Devices	MESFET	InP HEMT	Design verification
S-5	SLO-2	Construction and Operation of Varactor Diode	Impact ionization	Principle of operation	Technology comparisons	Design verification

S-6	SLO-1	Applications of Varactor Diode		Properties of semiconductor materials used in MESFET	Technology comparisons	Materials testing
0-0	SLO-2	Schottky effect	Small-signal analysis of IMPATT diodes	MESFET Technology	Introduction of RF power transistor	Reliability testing
0.7	SLO-1	Schottky barrier diode	TRAPATT, BARITT Diodes	MESFET Modeling	Figure of Merit for RF Power Transistor	computer integrated Manufacturing
S-7	SLO-2	Applications of Schottky Diode	Two-valley model of compound semiconductors	I-V Characteristics	Common RF power devices	computer integrated Manufacturing
	SLO-1	Hetero junctions	vd-E characteristics	High frequency performance	Material properties	Thermal modeling
S-8	SLO-2	Hetero junctions	Gunn Effect, modes of operation	MISFET-Introduction	State-of-the-art-wide bandgap microwave transistor data	Thermal analysis of resistance networks
S-9	SLO-1	Construction and operation of microwave PIN diode	small-signal analysis of Gunn diode	Operating characteristics of MISFET	Challenges to production	Introduction to computer aided design
3-9	SLO-2	Applications	Power-frequency limit.	Operating characteristics of MISFET	IL nailendes to production	Benefits, limitations and applications of CAD

Learning

 Golio, M., "RF and Microwave Semiconductor Devices Handbook", CRC Press (2002).
 2Sze, S.M., and Ng, K.K., "Physics of Semiconductor Devices", 3rd Ed., Wiley-Interscience (2006). Resources

3. Glover, I.A., Pennoek, S.R. and Shepherd P.R., "Microwave Devices, Circuits and Sub-Systems", 4th Ed., John Wiley & Sons (2005)

4. Liao, S.Y., "Microwave Devices and Circuits", 4th Ed., Pearson Education (2002).

Learning Asse	ssment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weigl	ntage)			Einal Examinatio	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – S	3 (15%)	CLA – 4	l (10%)#		ii (50 % weigiilage)
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	30 %		30 %		30 %		30 %		30%	
Level I	Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %	-	40 %		40 %		40 %		40%	
Leverz	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate	30 %		30 %		30 %		30 %		30%	
Level 5	Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100) %	100) %	100) %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. E. Siva Kumar, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE322T	Course Name	OF	OPTOELECTRONICS		-	ourse tegor					Pro	ofessio	onal E	Electiv	е				L 3	T 0	P 0	C 3
Pre-requ Course		18ECC102J	Co-requisite Courses		Nil		gres								N	il							
Course Of	Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards													Nil									
Course Le	Course Learning Rationale (CLR): The purpose of learning this course is to:						earn	ing					Prog	ram l	Learn	ing O)utcor	nes (PLO)				
CLR-1 :	Identify the working a	nd nature of op	otical wave			1	2	3		2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	ldentify the working ar	nd nature of op	otical semiconductors			(ju	(%)	(%)				÷			lity								
CLR-3 :	Analyze the working p	principles of dif	ferent photonic sources			Thinking (Bloom)	C C	ut (p D	Ŧ	Research			Sustainability		¥		е			ues	
CLR-4 :	Analyze the working p	principles of dif	ferent photonic detectors			j) pr	Proficiency	Attainment		nen l	Development	Res	ge		ustai		n Work		Finance	5	onal	ct echniques	જ
CLR-5 :	Create knowledge abo	out various opt	oelectronic applications			inkii	Lofic	ttair		VSiS	/eloj	sign,	Usa	Culture	& SI		Team	u	& Fir	Leaming	^{>} rofessional nent	Project tent Tec	alyze
CLR-6 :	Familiarize the conce	ots of optoelec	tronic integrated circuits				P P P	A b€		Ana	Dev	Des	Tool	s cu	nent		ిన	iicati	Agt. å) Lee	Profe	: Pro nent	h An
						el of	e e	Expected		Engineering Miowedge Problem Analysis	Design &	Analysis, Design,	Modern Tool Usage	Society &	Environment	S	Individual	Comm unication	^o roject Mgt. &	Life Long)-1: Pr eveme) – 2 ager	PSO – 3: Analyze 8 Research
Course Le	arning Outcomes (C	LO): At the e	nd of this course, learners will be al	ole to:		Lev	Exp	Exp		Ping Dig	Des	Ana	Mod	Soci	Envi	Ethics	Indiv	Con	Proj	Life	PSO- Achie	PSC Man	PSC Res
CLO-1 :	Review the basics of o	optics, optical s	semiconductors			2	85	80	1	I H	-	-	-	-	-	-	-	-	-	М	-	-	-
CLO-2 :	Understand the working	ng principle of	different photonic sources			4	85	75	I	I H	Н	Н	-	-	-	-	-	-	-	М	L	I	Н
CLO-3 : Familiarize the principle and operation of various detectors				4	85	75	I	H H	Н	Н	-	-	-	-	-	-	-	М	L	i	Н		
CLO-4 :	CLO-4 : Acquire knowledge of various optoelectronic modulators and switches			4	80	70	1	I H	-	-	-	-	-	-	-	-	-	М	-	-	-		
CLO-5 :	CLO-5 : Explore the concepts of optoelectronic integrated circuits and components			4	80	70	I	1 -	Н	-	-	-	-	-	-	-	-	М	L	-	-		
CLO-6 :	CLO-6: Design and analyze the working of different components in optical system and use it for various applications.			4	80	70	1	I H	Н	Н	-	-	-	-	-	-	-	М	-	-	Н		

	iration	WAVE NATURE OF LIGHT AND SEMICONDUCTOR OPTICS	SEMICONDUCTOR PHOTON SOURCES AND DISPLAY DEVICES	SEMICONDUCTOR PHOTON DETECTORS	OPTOELECTRONIC MODULATORS, INTERCONNECTS AND SWITCHES	OPTOELECTRONIC INTEGRATED CIRCUITS (OEIC) AND APPLICATIONS
,	hour)	9	9	9	9	9
S-1		Light Waves In A Homogeneous Medium- Plane electromagnetic wave, Maxwell's wave equation	LED Principles- Homojunction LED, Heterostructure LED	Principle of Photo Detection	Electro-Optic Modulator: Principles, Electro optic effect	Introduction
3-1	SLO-2	Refractive Index And Dispersion- Sellmeier equation and diamond, Cauchy equation and diamond	Quantum Well High Intensity LEDs	The PIN Photodiode	Single waveguide electro optic modulators	Need For Integration
S-2	SLO-1	Polarization Of Light	LED Materials and Structures	Avalanche Photodiode- Principles, Structures	Dual channel waveguide electro optic modulator	Slab and stripe waveguides
3-2	SLO-2	Snell's law and Total internal reflection	LED Efficiencies and Luminous Flux	Responsivit, Efficiency	Electro optic modulator employing reflection or Diffraction	Basic IO structural elements
S-3	SLO-1	Reflection And Refraction	Manufacturing Process and Applications	Heterojunction Photodiodes	Integrated Optical Modulators: Phase and polarization modulation	IO devices: Optical disk read head
3-3	SLO-2	Solving problems	Solving Problems	Schottky Junction Photodetectors	Mach Zehnder modulator, Coupled waveguide modulator	OIC temperature sensor
S-4	SLO-1	Superposition And Interference Of Waves	LASER: Threshold Condition	Solving problems	Acousto-Optic Modulator: Principles, Acousto optic effect, Raman nath and Bragg type modulators	IO high voltage sensor

	SLO-2	Diffraction Principles- Fraunhofer diffraction, Diffraction Grating	Emission and Absorption of Radiation	Solving problems	Performance characteristics, Acousto optic frequency shifters	IO chemical sensor
S-5	SLO-1	Overview Of Semiconductors	Population Inversion	Metal-Semiconductor, Metal Photodiode	Solving proplems	IO wavelength meters and spectrum analyzers
3-5	SLO-2	Interaction of Photons With Charge Carriers	Principle of the Laser Diode	Phototransistors	Solving problems	RF Spectrum Analyzer
S-6	SLO-1	Hole Pair Formation And Recombination	Heterostructure Laser Diodes	Array Detectors	Faraday Rolalion	Monolithic Wavelength-Multiplexed Optical Source
	SLO-2	Absorption In Semiconductors	Device Fabrication	Photoconductive detectors	Optical Isolators	Analog-To-Digital Converter
S-7	SLO-1	Effect Of Electric Field On Absorption	Solving problems	Noise In Photodetectors	Nonlinear Optics	Integrated-Optic Doppler Velocimeter
•	SLO-2	Absorption In Quantum Wells	Display Device: Photo Luminescence	Noise In Photodetectors	Second Harmonic Generation	Guided Wave Devices
S-8	SLO-1	Radiation In Semiconductors	Cathode Luminescence, Electro Luminescence	Solving problems	Optical Interconnects	Guided Wave Devices
	SLO-2	Solving Problems	Injection Luminescence	Solving problems	Optical gates	OEIC: Transmitter
	SLO-1	Heterojunctions	Plasma Displays	Charge Coupled Devices (CCD)	Photonic Switches	OEIC: Receiver
S-9	SLO-2	Heterojunctions	LCD, Numeric Displays	Charge Coupled Devices (CCD)	Solving problems	OEIC phased array antenna driver

		1.	Kasap, "Optoelectronics & Photonics: Principles & Practices", 2nd edition, Pearson Education,	
L	earning	2	2013. Pallab Bhattacharva "Semiconductor Ontoelectronic Devices" 2nd Edition Prentice Hall of India	 Robert G. Hunsperger, "Integrated Optics- Theory And Technology", Springer, 2009 J. Wilson and J F B Hawkes "Optoelectronics- An Introduction", 3rd edition, Pearson Education Taiwan Ltd,
	Resources		Pvt. Ltd, New Delhi, 2009.	2010
		3.	B. E. A. Saleh and m.c. Teich, "Fundamentals Of Photonics," 2nd edition, John Wiley & Sons, Inc.	6. A Ghatak and K Thyagarajan, "Introduction to Fiber Optics", Cambridge University Press 2006.
			2007.	

Learning Asses	ssment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weigl	htage)			Einal Examination	o (50% woightago)
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA –	4 (10%)		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40%		40%		35%		35%		40%	
Level I	Understand	40%	-	40%	-	30%	-	30%	-	40%	-
Level 2	Apply	40%		40%	_	35%	_	35%		40%	
Level 2	Analyze	40%	-	40%	-	33%	-	3376	-	40%	-
Level 3	Evaluate	20%		20%		30%	-	30%	_	20%	
Level J	Create	2070	-	2070	-	50%	-	50%	-	2070	-
	Total	100) %	100) %	100) %	10	0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Ms. Ramya A, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@jci.com</u>	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE323T	Cours Name	-	Advanc	ced Optical (Communication	 ourse egory	Е	Professional Elective	L 3	T 0	P 0	C 3
Pre-requisite Courses		18ECC3	02J	Co-requisite Courses		Nil	gressiv ourses	e	Nil				
Course Offering Department Electronics and Co			ectronics and Comn	nunication Enginee	ring	Data Book / Codes/Standards			Nil				

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng					Prog	ram L	earni	ng O	utcor	nes (l	PLO)				
CLR-1 :	Introduce the advanced features of Fibers and light wave system	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Illustrate the basics of light wave system and multichannel system	(c	()								y							Ille	ç
CLR-3 :	Understand the various dispersion compensation techniques	(Bloom)	(%)	(%)				arch			Sustainability							ē	Research
CLR-4 :	Gain the information on advanced RoF Systems	Ē	nc)	ent	dge		ent	sse			aina		Work		8		al	201	Re
CLR-5 :	Improve the knowledge about the characterization of the Visible Light Communication	Thinking	roficiency	Attainment	wle	s	Development	Å	age	e	Sust		≥ E		Finance	ĝ	iona 4	Project manag	e Se
CLR-6: Utilize the concepts in optical communication for the understanding of engineering and technology					Kno	lysi	velc	Design,	Us	Culture	o∕ð		Team	ion	⊗ ⊥	Leaming	ession t)a(Analyze
		Ē	Ъ		ing	Analysis	De	De	00	& CL	Jent		ବ	icat	Mgt.	Le	Profes	les les	
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected	Expected	Engineering Knowledge	Problem.	Design &	Analysis,	Modern Tool Usage	Society 8	Environment	Ethics	Individual	Comm unication	Project M	Life Long	PSO-1: I Achieven	ı .ğ	PSO – 3:
CLO-1 :	Explain the concept of wave propagation and dispersion in single-mode fibers, loss and nonlinear of fiber and fiber design and fabrication.	2	80	70	Н	Н	М	Н	Н	-	L	-	-	Н	-	-	Н	М	Н
CLO-2 :	Apply the concept of optical transmitter and receiver in single-mode semiconductor lasers, light-emitting diodes, transmitter design and receiver design	2	85	75	Н	Н	М	Н	Н	-	L	-	-	Н	-	-	Н	М	Н
CLO-3 :	Demonstrate the concept of long-haul systems, computer-aided design, WDM light wave systems, WDM Components, time-division, subcarrier and code division multiplexing	2	75	70	Н	Н	М	Н	Н	-	L	-	-	Н	-	-	Н	М	Н
CLO-4 :	Explain the loss and dispersion managements in EDFA - Raman amplifiers, dispersion compensating fibers Fiber Bragg gratings, dispersion-equalizing filters and optical phase conjugation	2	85	80	н	Н	М	Н	Н	-	L	-	-	Н	-	-	н	М	н
CLO-5 :	Apply the concept of advanced light wave system in demodulation schemes sensitivity degradation mechanisms and impact of nonlinear effects	2	85	75	н	н	М	Н	Н	-	L	-	-	Н	-	-	н	М	Н
CLO-6 :	.0-6 : Apply their idea in Optical communication module				Н	Н	М	Н	Н	-	L	-	-	Н	-	-	Н	М	Н

	ration iour)	OPTICAL FIBERS and LIGHTWAVE SYSTEMS	LIGHTWAVE SYSTEMS AND MULTICHANNEL SYSTEMS	LOSS MANAGEMENT AND DISPERSION MANAGEMENT	RADIO OVER FIBER SYSTEMS	OPTICAL WIRELESS COMMUNICATION
ų	ioury	9	9	9	9	9
	SLO-1	Geometrical-Optics Description	System Architectures	Compensation of Fiber Losses	Trends in Wireless Communications	Free-space optical wireless communication
S-1	SLO-2	Wave Propagation	Working Principles	Erbium-Doped Fiber Amplifiers les	Basic Transmission problems and solutions	Free-space optical OFDM communication
S-2	SLO-1	Dispersion in Single-Mode Fibers	Design Guidelines	Raman Amplifiers	Regulation	Wireless optical CDMA communication systems
3-2	SLO-2	Dispersion Induced Limitations	Long-Haul Systems	Optical Signal-To-Noise Ratio	Standardization	Comparison of Free-space optical OFDM & CDMA communication
S-3	SLO-1	Fiber Losses	r Losses Sources of Power Penalty Electrical Signal-To-Noise Ratio of signals		System concepts for the central processing of signals	Indoor wireless optical communication
3-3	SLO-2	Nonlinear Optical Effects	Forward Error Correction	Receiver Sensitivity and Q Factor	Wireless Trends	outdoor wireless optical communication

S-4	SL0-1	Fiber Design and Fabrication	Types of FEC	role of Dispersive and Nonlinear Effects	Architecture options,	Heterogeneous optical networks (HONs)
0-4	SLO-2	multicore fibers	Computer-Aided Design	Periodically Amplified Lightwave Systems	global centralized Architecture	System Performance
S-5	SLO-1 SLO-2	multiclad fibers advantages and its applications	WDM DWDM	-1	FUTON scenarios Optical Infrastructure	VLC System Model Advantages and its applications
S-6		Advanced Modulation Formats	Light wave Systems	Dispersion-Compensating Fibers	Concepts of Radio over Fiber systems	(RF) sensor network system
5-0		Demodulation Schemes	WDM Components	Fiber Bragg Gratings	Features of ROF	Advantages and its applications
S-7	SL0-1	Shot Noise	System Performance Issues	Dispersion Equalizing Filters	Categories RoF systems	(FSO) sensor network system
3-1	SLO-2	Bit-Error Rate	Time-Division Multiplexing	Optical Phase Conjugation	Performances RoF systems	Advantages and its applications
S-8	SL0-1	Sensitivity Degradation Mechanisms	Subcarrier Multiplexing	Channels at High Bit Rates	Applications of RoF Technology	Recent Advancement in Optical Wireless Communication
0-0	SLO-2	Impact of Nonlinear Effects	Code-Division Multiplexing	Electronic Dispersion Compensation	Advantages of RoF Technology	Advantages and its applications
S-9	SL0-1	Recent Progress	Solving Problems	Solving Problems	Solving Problems	Solving Problems
3-9	SLO-2	Ultimate Channel Capacity	Solving Problems	Solving Problems	Solving Problems	Solving Problems

		1.	Nathan J. Gomes. Paulo P. Monteiro and Atilio Gameiro "Next Generation wireless communications	3.	ShlomiArnon, John R. Barry, George K. Ka
	earning		using Radio over Fiber" John Wiley & Sons, Ltd, 2012	-	Optical Wireless Communication Systems"
F	Resources	2.	G.P. Agarwal, Fiber optic communication systems, 4nd Ed, John Wiley & Sons, New York, 2010	4.	Shlomi Arnon, "Visible light Communication

 ShlomiArnon, John R. Barry, George K. Karagiannidis, Robert Schober, Murat Uysal, "Advanced Optical Wireless Communication Systems" Cambridge University Press, 2012
 Shlomi Arnon, "Visible light Communication", Cambridge University Press, 2015

Learning As	sessment										
	Bloom's				Einel Exeminatio	n (E00/ woightogo)					
		CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	10	0 %	10	0 %	10	0 %	10	0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. C.T. Manimegalai, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE240T	Cours Name	-	Co-requisite			Cours Catego	-	Ε				F	Profes	ssiona	l Elec	tive					L 3	T P 0 0	C 3
Pre-requ Cours	18EU.U.104	-		Co-requisite Courses	lil			ogres Cours	ssive ses	18EC	E341	Т												
Course Of	Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards				Nil																			
Course Learning Rationale (CLR): The purpose of learning this course is to:					Learn	ing]				I	Progra	am Le	earnir	າg Oເ	utcom	nes (P	PLO)						
CLR-1: Learn about multiresolution analysis and wavelet signal processing				1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13 14	4 15			
CLR-2 : CLR-3 : CLR-4 : CLR-5 : CLR-6 : Course Le	Identify the families Study the of discret. Study various filter Analyze various rea Acquire knowledge arning Outcomes	of wavelets of systems th banks of dis al time applic about wavel CLO): At t	equired to apply th at employs wavelee crete systems used ations that employ at transforms, types the end of this cour-	e transformation to vi t transformation d in wavelet transform 's filter banks s and applications of i se, learners will be ab	nation multiresolution a		Lavel of Thinking (Rhom)	Expecter	Expected Attainment	_	Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	Achieven PSO – 2:	Techniques PSO – 3: Analyze & Research
	Understand multi re		lysis for discrete si	ignals			3	80			Н	Н	-	-	-	-	-	-	-	-	-	-	Η-	-
CLO-2 : Know the families of wavelets				3	80		1	Н	-	М	-	-	-	-	-	-	-	-	-		М			
CLO-3 : Identify Discrete wavelet transform				3	75			М	М	М	-	-	-	-	-	-	-	-	-		-			
					3	80			Н	-	М	-	-	-	-	-	-	-	-	-				
				3	80			Н	-	М	L	-	-	-	-	-	-	-	-	- N	1 H			
CLO-6 :	LO-6 : Know about wavelet transforms, types and applications of multiresolution analysis			3	80	70		М	Н	-	-	-	-	-	-	-	-	-	-		-			

		Multiresolution Analysis (MRA)	Families of wavelets	Discrete Wavelet Transform (DWT)	Filter banks	Applications
Durati	ion (hour)	9	9	9	9	9
S-1	SLO-1	Introduction to multiresolution/ multiscale analysis	Orthogonal	Discretization in steps	Introduction to Variants of the wavelet transform	Transient analysis
3-1	SLO-2	Introduction to multiresolution/ multiscale analysis	Orthogonal	Discretization in steps	Introduction to Variants of the wavelet transform	Transient analysis
S-2	SLO-1	Time-frequency analysis and wavelets	Time-frequency analysis and wavelets Biorthogonal wavelets Disc		Implementational structures	Singularity detection
3-2	SLO-2	Time-frequency analysis and wavelets	analysis and wavelets Biorthogonal wavelets Discretization of scale		Implementational structures	Singularity detection
S-3	SLO-1	Piecewise constant approximation	Daubechies' family of wavelets	Generalized filter bank	The wavepacket transform	Biomedical signal processing applications
5-3	SLO-2	Piecewise constant approximation	Daubechies' family of wavelets	Generalized filter bank	The wavepacket transform	Biomedical signal processing applications
• •	SLO-1	Haar wavelet	Daubechies' family of wavelets	Discretization of translation	Computational efficiency in realizing filter banks	Efficient signal design and realization
S-4	SLO-2	Haar wavelet	Conjugate Quadrature Filter Banks (CQF) and their design	Discretization of translation	Computational efficiency in realizing filter banks	Efficient signal design and realization
0.5	SLO-1	Building up the concept of dyadic Multiresolution Analysis (MRA)	Conjugate Quadrature Filter Banks (CQF) and their design	Generalized output sampling	Computational efficiency in realizing filter banks	Wavelet based modulation and demodulation
S-5	SLO-2	Building up the concept of dyadic Multiresolution Analysis (MRA)	Conjugate Quadrature Filter Banks (CQF) and their design	Generalized output sampling	Polyphase components	Wavelet based modulation and demodulation
S-6	SLO-1	Relating dyadic MRA to filter banks	Data compression	Discretization of time/ space (independent variable)	Polyphase components	Applications in mathematical approximation

	SLO-2	Relating dyadic MRA to filter banks	Data compression	Discretization of time/ space (independent variable)	Polyphase components	Applications in mathematical approximation
S-7	SLO-1	A review of discrete signal processing	Fingerprint compression standards	Going from piecewise linear to piecewise polynomial	The lattice structure	Applications to the solution of some differential equations.
3-1	SLO-2	A review of discrete signal processing	Fingerprint compression standards	Going from piecewise linear to piecewise polynomial	The lattice structure	Applications to the solution of some differential equations.
S-8	SLO-1	Elements of multirate systems	JPEG-2000 standards	The class of spline wavelets	Solving Problems	Solving Problems
3-0	SLO-2	Elements of multirate systems	JPEG-2000 standards	The class of spline wavelets	Solving Problems	Solving Problems
c 0	SLO-1	Two-band filter bank design for dyadic wavelets.	Solving problems	A case for infinite impulse response (IIR) filter banks	The lifting scheme.	Solving Problems
S-9	SLO-2	Two-band filter bank design for dyadic wavelets.	Solving problems	A case for infinite impulse response (IIR) filter banks	The lifting scheme.	Solving Problems



 M. Vetterli, J. Kovacevic, Wavelets and Subband Coding, Prentice Hall, 1995
 S. Mallat, A Wavelet Tour of Signal Processing, 2nd ed., Academic Press, 1999
 P.P. Vaidyanathan, Multirate Systems and Filter Banks, Pearson Education, 1993
 C.S.Burrus, Ramesh A. Gopinath, and Haitao Guo, Introduction to Wavelets and Wavelet Transforms: A Primer, Prentice Hall, 1997

- Gilbert Strang, Truong Nguyen, Wavelets and Filter Banks, 2nd ed., Wellesley-Cambridge Press, 1998.
 Ingrid Daubechies, Ten Lectures on Wavelets, SIAM, 1992
- Howard L. Resnikoff, Raymond O. Wells, "Wavelet Analysis: The Scalable Structure of Information", Springer, 1998

Learning Assess	sment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA – S	3 (15%)	CLA – 4	l (10%)#		i (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	30 %		30 %		30 %		30 %		30%	
Lever	Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %		40 %		40 %		40%	
Level Z	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	4070	-
Level 3	Evaluate	30 %		30 %		30 %		30 %		30%	
Level J	Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	10	0 %	100) %	100) %	100) %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. Sabitha Gauni, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@jci.com</u>	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE241J	Course Name	SIGNAL PROCESSING	FOR AUDITORY SYSTEMS	Course Category	Е	Professional Elective	L	Т	P	C
Coue		Name			Category			2	U	Z	3
Pre-requisi	te ANTOCADAT		Co-requisite		Progr	essive	105052427				
Courses	18ECC104T		Courses		Cou	rses	18ECE343T				
Course Offer	ing Department	Electroni	cs and Communication Engineering	Data Book / Codes/Standard	s Nil						

Course Learning Rationale (CLR):	The purpose of learning this course is to:	L	earni	ng					Prog	ram L	earni	ing Ou	utcon	nes (P	(PLO)							
CLR-1: Learn basics of signal proce	essing	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15				
CLR-2 : Know Feature Extraction te											~							ent ch				
CLR-3 : Identify Frequency characte	ristics of Speech signal	Ê	(%					arch			bilit							sear				
CLR-4 : Construct the Digital model	of speech signal	(Bloom)		t (%)	dg e		ent	see			Sustainability		Work		9	_	_	Manage e & Rese				
CLR-5 : Identify the Ethical issues of		B (B)	ency	nen	Me e	s	, mad	, Re	age	Ð	Sust		≥ E		Finance	ĝ	ssional	t Ma ze &				
CLR-6 : Learn the basic of speech s	ignal processing and its model	Thinking	ofici	Attainment	х Р	Analysis	Development	sign,	Us:	Culture	∞ð		Team	ion	∞ŏ	Leaming	ess.	Project I es Analyze				
		Thir I	μΡ		ing	Ana	De	De	Tool Usage	& CL	nment		∞ŏ	licat	Mgt.	Le	nent .	An Les				
Course Learning Outcomes (CLO):	At the end of this course, learners will be able to:	Level of .	Expected	Expected	Engineering Knowledge	Problem.	Design &	Analysis,	Modern 7	Society 8	Environn	Ethics	Individual	Communication	Project N	Life Long	Achiever	PSO – 2: Pr Techniques PSO – 3: Ar				
CLO-1 : Appreciate the functioning of	f the human vocal and auditory systems	3	80	75	Н	-	Н	-	Н	-	-	-	-	-	-	-	М	- H				
CLO-2 : Analyze the function of feat	re extraction in speech and audio signal processing using Time Domain Characteristics	3	80	70	Н	-	Н	-	-	М	-	М	-	-	-	-	М	- H				
CLO-3 : Explore the frequency char	acteristics of speech signal	3	75	70	Н	-	Н	Н	-	-	-	-	-	-	-	-	М	- H				
CLO-4 : Apply appropriate Digital me	odels for speech signal	3	80	75	Н	-	-	-	Н	-	-	-	-	-	-	-	Н	M M				
CLO-5 : Analyze the elements of mu	sic	3	80	70	-	-	-	М	-	-	-	-	-	-	-	-	М	- H				
CLO-6 : Know about speech signal p	Know about speech signal processing and its model			70	Н	-	Н	-	Н	-	-	-	-	-	-	-	Н	- M				

		Basic Audio Processing using MATLAB	Speech Signal Analysis in Time Domain	Speech Signal Analysis in Frequency Domain	Digital Models for Speech Signal	Time Elements in Music
Durati	ion (hour)	12	12	12	12	12
S-1	SLO-1	Introduction to Digital audio	Speech signal analysis	Short Time Fourier analysis	Introduction to Acoustic Phonetics	Sound vibrations – pure tones and perception of pitch
3-1	SLO-2	Capturing and converting sound	Segmental analysis	Filter bank analysis	Introduction to Acoustic Phonetics	Sound vibrations – pure tones and perception of pitch
S-2	SLO-1	Sampling of sound wave	Sub-segmental	Formant extraction	Acoustic theory of speech production:- Sound propagation	Auditory coding in the nervous system
3-2	SLO-2	Handling audio in MATLAB	Supra segmental levels	Pitch Extraction	Acoustic theory of speech production:- Sound propagation	Auditory coding in the nervous system
S 3-4	SLO-1 SLO-2	Lab 1: Read & write a speech signal, Record a speech signal, playback, convert into a wave file, plot the speech signal, and spectrogram plot.	Lab 4: Short-term energy of a speech signal	Lab 7: Estimation of pitch period using simplified inverse filter tracking (SIFT) algorithm	Lab 10: Phoneme-level segmentation of speech	Lab 13:Feature Extraction of speech signal
S-5	SLO-1	Normalization	Time domain parameters of speech signal	Homomorphic speech analysis	Vocal tract transfer function of vowels	Subjective pitch and role of nervous system
3-0	SLO-2	Audio processing	Time domain parameters of speech signal	Homomorphic speech analysis	Vocal tract transfer function of vowels	Subjective pitch and role of nervous system
S-6	SLO-1	Segmentation	Methods for extracting the parameters Energy	Formant and Pitch Estimation	Effect of nasal coupling	Acoustical energy –perception of loudness, pitch, timbre
3-0	SLO-2	Analysis of window sizing	Methods for extracting the parameters Average Magnitude	Formant and Pitch Estimation	Excitation of sound in vocal tract	Pitch contour Musical Structure
S 7-8	SLO-1 SLO-2	Lab 2: Convert into a wave file, plot the speech signal, and spectrogram plot	Lab 5: Short-time Fourier transform magnitude spectrum	Lab 8: Estimation of pitch period using harmonic product spectrum	Lab 11: Estimation of sound in vocal tract	Lab 14: Speech production mechanism

S-9	SLO-1	Visualization	Zero crossing Rate	Linear Predictive analysis of speech	Vocal tract transfer function of vowels	Detecting beats, rhythm, meter							
3-9	SLO-2	Sound generation	Zero crossing Rate	Linear Predictive analysis of speech	Vocal tract transfer function of vowels	Recognizing pitch – melody							
0.40	SLO-1	Speech production mechanism	Silence Discrimination using ZCR and energy	Autocorrelation method, Covariance method	Effect of nasal coupling	Auditory streaming							
S-10	SLO-2	Speech production mechanism	Silence Discrimination using ZCR and energy	Solution of LPC equations	Excitation of sound in vocal tract	Tonality and context – algorithms							
s	SLO-1	Lab 3: Cepstrum smoothed magnitude	Lab 6: (i)Linear prediction magnitude spectrum	Lab 9: Pitch and duration modification using time-domain pitch synchronous	Lab 40: David a therefore	Lab 15:Study of Feature extraction and							
11-12	SLO-2	spectrum	(ii) Estimation of formant frequencies using linear prediction	overlap and add (TD-PSOLA) method	Lab 12: Sound vibrations	SVM classifier							
Learni Resou	•	1. Ian McLaughlin, Applied Speech and Audio processing, with MATLAB examples, 1 st ed., Cambridge University Press, 2009 3. Lawrence Rabiner,B.H.Juang, Fundamentals of Speech Recognition, 2 nd ed., Prentice-hall, 1993											

Learning Assess	sment												
	Disamia		Continuous Learning Assessment (50% weightage)										
	Bloom's Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)		
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%		
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
Level Z	Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%		
Laural 2	Evaluate	100/	100/	150/	150/	150/	150/	450/	150/	15%	150/		
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%		
	Total	10	0 %	10	0 %	10	0 %	100	0 %	-			

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mrs. K. HariSudha, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECE2421 PALLERN RECOGNITION AND NEURAL NETWORKS				Course Categor		Ε				Profe	ssiona	al Elec	ctive					L 2	T 0		C 3
Pre-requ Cours				gres: ourse		18ECE3	40T															
Course O	ffering Department	Electronic	s and Communication Engineering	Data Book / Codes/Standards	Nil																	
Course Le	earning Rationale (CLI	R): The purpos	se of learning this course is to:		L	earni	ing					Prog	am L	.earni	ng Oı	utcom	nes (P	PLO)				
CLR-1 :	Learn the concepts of	oattern recogniti	on		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 : Analyze few parameter estimation methods for pattern recognition CLR-3 : Acquire knowledge on the fundamental neural networks CLR-4 : Apply the neural network recurrence for pattern recognition studies CLR-5 : Utilize the practical applications of neural networks in pattern recognition CLR-6 : Understand the pattern and apply neural network based learning algorithm to analyze the data from real world applications					Level of Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment	- Engineering Knowledge	Problem Analysis	. Design & Development	Analysis, Design, Research	: Modem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt & Finance		0-1: P	I .⊆	PSO – 3: Analyze & Research
CLO-1 :	Identify the fundament	als of recognitior	n of patterns, regularities in data and clas	sifiers	3	80	75	L	-	L	Н	М	-	-	-	-	-	-	-	-	-	-
CLO-2 :			itions, test-set error estimation and train	ing-set error estimation	3	80	70	N		-	Н	-	-	-	-	-	-	-	-	-	-	Н
CLO-3 :			entals on learning algorithms		3	75		N		-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-4 :			the deviation with back propagation netwo	orks	3	80	75	N	- 1	М	Н	-	-	-	-	-	-	-	-	М		Н
CLO-5 :			orks in the area of pattern recognition		3	80	70	L	-	М	Н	-	-	-	-	-	-	-	-	-	-	Н
CLO-6 :	Analyze and compare recognition.	a variety of patte	ern classification techniques to real-world	l problems such as document analysi	is and 3	80	70	N	-	М	Н	М	-	-	-	-	-	-	-	L	-	Н

		Introduction To Pattern Recognition	Parameter Estimation Methods	Introduction to Neural Networks	ANN for Classification and Regression	ANN for Organization and Recognition
Durati	on (hour)	12	12	12	12	12
S-1	SLO-1	Introduction to Statistical Pattern Recognition	Introduction to parameter estimation	Introduction to neural networks	Introduction to Hopfield networks	Self-organizing map
5-1	SLO-2	Overview of Pattern Classifiers	Maximum-Likelihood estimation	Neuron model	Hop-field network- architecture	SOM algorithm
S-2		Process of Classifier Design, Decision making theory	Maximum a Posteriori estimation	Learning methods of ANN, Supervised, Unsupervised and reinforced	Recurrent networks	Learning vector quantization
0-2	SLO-2	Bayesian decision making	Bayesian estimation	Basic learning rules of ANN-	Sample recurrent network structure	Kohonen self-organizing map
S 3-4	SLO-1 SLO-2	Lab1: Digitization of analog signals	Lab4: Programs on Estimation	Lab 7: Logic gate function description with Hebb rule	Lab 10: Programs on training a Hopfield network	Lab 13: programs on orthogonality and evaluating input and output for association
S-5	SLO-1	Bayes Classifier	Unsupervised learning and clustering	McCulloh pitt neuron	Associative memories- Introduction:	Feature selection
5-0	SLO-2	Bayes Classifier for minimizing Risk	Clustering vs. Classification-Supervised vs. unsupervised	Problems on McCulloh pitt	Auto and hetero associative memory	Feature map classifier, applications
S-6	SLO-1	Estimating Bayes Error	Criterion functions for clustering Algorithms for clustering	Hebb learning rule	Bi directional memories	Architecture of Adaptive Resonance Theory
3-0	SLO-2	Effect of sample size in estimation	K-Means clustering	Problems on Hebb learning rule	XOR problem	ATR1 algorithm
S 7-8		Lab 2: Program to count the white pixels from the image	Lab 5: Loading a data set and selecting predictive features	Lab 8: Evaluating function with different learning rules	Lab 11: Programs on Auto and hetero association of memory	Lab 14: Character Recognition
S-9	SLO-1	Minimax Classifiers	Hierarchical methods of clustering	Single layer perceptron architecture Training algorithm	Back-propagation Algorithm	ART2 algorithm - Training

	SLO-2	Neymann Classifiers	Comparison of methods, cluster distance and validation	Multilayer perceptron	Counter propagation networks- architecture	ART2- network architecture
S-10	SLO-1	Pearson Classifiers	Sequential Pattern Recognition	Adaline architecture	Simulated annealing	Hand written digit recognition
3-10	SLO-2	Applications	Sequential Pattern Recognition	Madaline architecture	Boltzmann machine	Character recognition networks
S 11-12	SLO-1 SLO-2	Lab3: Analysis of a data set with classifiers	Lab 6: Programs on clustering technique	Lab 9 : XOR problem with Perceptron network	Lab 12: Evaluation of error in BPN	Lab 15: Mini Project

Learning Resources		Christopher M. Bishop, Pattern Recognition and Machine Learning, Springer Verlag, 2016 Dionisis Cavouras, S. Theodoridis, K. Koutroumbas, A. Pikrakis, An Introduction to Pattern Classification: A Matlab Approach, Elsevier Science Publishing Co Inc. 2010	5.	Simon O. Haykin, Neural Network and Learning Machines, 3 rd ed., Pearson Education, 2009 Ke-Lin Du ,M. N. S. Swamy, Neural Networks and Statistical Learning, Publisher Springer, 2014 Kosko B, Neural Networks and Fuzzy Systems: A dynamical system approach to machine intelligence,
	3.	Martin T.Hagan, Neural network design, Cengage publications, 2010		Prentice Hall, 2009

Learning As	sessment										
	Bloom's			Contir	nuous Learning Ass	essment (50% weigl	ntage)			Einal Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4 (10%)#			i (50 % weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	100) %	100	0 %	100	0 %	100	0 %		-

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

		.		Course	-		L	Т	Р	С
Course Code	18ECE243J	Course Name	Digital Image and Video Processing	Category	E	Professional Elective	2	0	2	3
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			• • •	-						

Pre-requisite Courses	18ECC204J	Co-requisite Courses	Nil	Progressive Courses	Nil
Course Offering De	partment Electronics and Commun	ication Engineering	Data Book / Codes/Standards		Nil

Course Lea	arning Rationale (CLR):	The purpose of learning this course is to:	L	earniı	ıg						Pro	ogram	Learn	ning O	utcom	es (PL	.0)				
CLR-1 :	Introduce the fundamentals of	f image processing and transforms	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Understand the concepts of im	age enhancement and restoration																		Ŧ	_
CLR-3 :	Acquire knowledge on image o	compression and segmentation methods	(Bloom)	(%)	(%)					arch			oility							Management	earch
CLR-4 :	Gain knowledge on basics of v	ideo processing	(Blo				dge		ent	ŝ			Sustainability		Work		ce		_	inage	Rese
CLR-5 :	Know about motion estimation	methods in video processing	king	Proficiency	Attainment		owle	.si	evelopment	in, Re	sage	e			Team V	_	& Finance	ing	sione		'ze &
CLR-6 :	Utilize the concepts of image a	nd video processing for practical applications	Thinking				ing Kn	Analysis		Design, I	Tool Usage	Culture	ient &			icatior		Long Leaming	Professional ment	Project es	Analyze
Course Lea	arning Outcomes (CLO):	At the end of this course, learners will be able to:	-evel of	Expected	Expected		Engineering Knowledge	^o roblem	Jesign &	Analysis,	Modem 7	Society &	Environment	Ethics	ndividual &	Communication	^o roject Mgt	-ife Long	PSO-1: I Achiever	>SO - 2: Fechniqu	-SO-3:
CLO-1 :	Understand the basics of digita	al image processing fundamentals and transforms	1,2	95	70		L	-	-	-	-	-	-	-	-	-	-	H	M	-	-
CLO-2 :	Design 2D filters and apply it I	for image enhancement and restoration	2	90	70		Μ	Н	-	Н	Н	-	-	-	-	-	-	Н	М	-	Н
CLO-3 :	Apply image compression and	segmentation methods on digital images	2	90	65		Μ	Н	-	Н	Н	-	-	-	-	-	-	Н	-	-	Н
CLO-4 :	Analyze the video formation te	echniques	1	95	70	1	Н	Н	-	Н	Н	-	-	-	-	-	-	Н	-	-	Н
CLO-5 :	Learn about the techniques for	r applying motion estimation in video coding	1,2	90	65	1	М	Н	-	Н	Н	-	-	-	-	-	-	Н	-	-	Н
CLO-6 :	Apply the concepts of digital in	nage, video processing and their applications	1,2	90	70	1	-	-	-	-	-	-	-	-	-	-	-	-	М	-	Н

Duratio	n (hour)	Digital Image Fundamentals and Image Transforms	Image Enhancement and Restoration	Image Compression and Segmentation	Basic Steps of Video Processing	2D Motion Estimation
	(,	12	12	12	12	12
S-1	SLO-1	Origin of digital image processing	Some basic intensity transformation functions – image negatives, log transformations	Fundamentals of image compression- coding redundancy, spatial and temporal redundancy	Analog video signals standard	2D motion estimation – Optical flow – 2D motion vs. apparent motion
	SLO-2	Fundamental steps in digital image processing	Piecewise linear transformation functions	Irrelevant information, measuring image information	Digital video signal, standard, Digital video processing	Correspondence and optical flow
S-2	SLU-1	Components of an image processing system	Histogram equalization, Matching	Image compression model, Lossless compression, Huffman coding	Time varying image formation models – 3D motion models	Occlusion problem
5-2	SLO-2	Structure of human eye, Image formation	Local Histogram Processing	Arithmetic Coding, Run length coding	Rigid motion in Cartesian, Homogenous coordinates	Aperture problem, 2D motion field models
		Lab 1: To learn MATLAB software and				Lab 13: Convert video into frames and
S- 3-4	SLO-2	its basic commands for image processing	Lab 4: Histogram Modifications	Lab 7: Run length coding	Li ab 10: Wavelet coding	process them
S-5	SLO-1	Brightness adaptation and discrimination	Using histogram statistics for image enhancement	Lossy compression - Transform coding	Deformable motion	Block motion models- translational block motion
3-0	SLO-2	Basic concepts in sampling and Quantization , Representing digital	Smoothing linear filters	Wavelet coding	Geometric image formation	Generalized/ Deformable block motion

		images				
S-6	SLO-1	Neighbors of a pixel, Adjacency, Connectivity, Regions and Boundaries	Order statistics nonlinear filters	Image segmentation – detection of isolated points, line detection	Perspective projection	Block matching criteria, Matching procedures
3-0	SLO-2	Distance Measures, A simple image formation model	Sharpening spatial filters	Edge models, Basic edge detection	Photometric image formation	Hierarchical motion estimation
	SLO-1		Lab 5: Image smoothing and	Lab 8: Basic edge detection		
S- 7-8	SLO-2	Lab 2: Fourier analysis of image	sharpening	operations	Lab 11: JPEG Compression	Lab 14:Filtering video signals
	SLO-1	Fourier transform of sampled functions	Combined spatial enhancement methods	Region based segmentation – region growing	Photometric effects of 3D motion	Gradient based optimization
S-9	SLO-2	Sampling theorem, Aliasing, Obtaining the DFT from the Continuous Transform of a Sampled Function	Homomorphic filtering, A model of image degradation/ restoration process		Observation noise, Sampling structures of analog, digital video	Steepest Descent method
S-10	SLO-1	Properties of 2D DFT – Relationship between spatial and frequency interval, Translation and Rotation, Periodicity, symmetric properties	A model of image degradation/ restoration process, Noise models		2D fourier transform relations, Intra frame filtering- LMMSE filtering	Newton Raphson method, Transform coding , 3D waveform coding
	SLO-2	DWT, DCT	Singular value decomposition		Median and weighted median filtering, Motion detection based filtering	Local vs. Global minima, Predictive coding
S- 11 -	SLO-1	I ab 2. Imaga filtaring	I ah fu Singular value decomposition	I ah () Banaat/Bayiaian of averagimenta	Lab 12: Region based image	l ah dE. Mini nyaisat
12	SLO-2	Lab 3: Image filtering	Lab 6: Singular value decomposition	Lab 9: Repeat/Revision of experiments	segmentation	Lab 15: Mini project

Learning Resources	2.	Rafael C Gonzalez, Richard E Woods, "Digital Image Processing"- 3rd Edition, Pearson Education 2008. Yao wang, JoemOstarmann and Ya – quin Zhang, "Video processing and communication ",1st edition , PHI M. Tekalp ,"Digital video Processing", Prentice Hall International	4. 5.	A.K. Jain, "Fundamentals of Digital Image Processing". Pearson education William K Pratt, "Digital Image Processing", John Willey (2001).	
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Learning Asse	essment										-			
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examination (50% weightage)				
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		r (50 % weightage)			
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%			
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%			
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%			
	Total	100) %	10	0 %	10	0 %	10	0 %		-			

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Course Code	18ECE244J	Course Name		DS	SP System Des	sign	Course Category	Е	Professional Elective	L 2	Т 0	P 2	C 3
Pre-requisit Courses	e	18ECC204J		Co-requisite Courses		Nil		ressive Irses	Nil				
Course Offering	g Department	Electron	cs and Communica	ation Engineering		Data Book / Codes/Standards	IEEE 164	1-2010, IE	EE 754, IEEE Standard. 1149.1				

Course L	earning Rationale (CLR): The purpose of learning this course is to:	Lea	arnin	9					Prog	ram L	earn	ing O	outcor	nes (I	PLO)				
CLR-1 :	Acquire knowledge on Floating and Fixed point Processor such as TMS320C6X for complex signal	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Learn and code TMS320C6x Assembly level programming for real time signal processing applications																	ŧ	_
CLR-3 :	Design and coding DSP algorithm such as FFT, DFT, Convolution , IIR and FIR filters in TMS320C6x	_						цсh			ility							mer	earch
CLR-4 :	Gain knowledge on advance filter concepts and filter signal noise using Filter Bank, adaptive filters and analyzes such filters for high end designing.	(Bloom)	ncy (%)	ent (%)	Engineering Knowledge		Development	Resea	ge		Sustainability		l Work		Finance		nal	Management	& Rese
CLR-5 :	Design DSP system for real time applications.	king	Proficiency	Attainment	Non	ysis	/elop	Design,	Usa	Culture	& Sl		Team	5	& Fin	Leaming	ssic	ect	Analyze
CLR-6 :	Utilize the concept of DSP for Engineering and Technology	Thinking	d Pro	d Atta	ring I	Analysis	& Dev	, Des	Fool Usage	& Cul	nent		∞ŏ	ommunication	Mgt. 8	g Lea	Profession nent	: Proj	: Ane
		el of	Expected	xpected	inee	Problem	esign 8	Analysis,	Modern ⁻	Society 8	ironr	S	Individual	Inmu	^o roject N	Long	-1- ievel	- ' hniqi	0 - 3
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Lev	ĔX	Exp	Ело	Р В	Des	Ana	Moc	Soc	Enviro	Ethics	Indi	Ğ	Pro	Life	Ach Ach	Tec Tec	PS(
CLO-1 :	Acquire in-depth knowledge on DSP architecture and instruction sets of TMS320C6X	L1	85	65	Н	-	Н	-	-	-	-	-	-	-	1	-	Н	М	L
CLO-2 :	Attain assembly programming knowledge and analyze using TMS320C6x	L1, L2	85	65	Н	Н		Н	-	-	-	-	-	-	-	-	Н	М	L
CLO-3 :	Implement various DSP algorithm such as FFT, DFT, Convolution , IIR and FIR filters in TMS320C6x	L2, L3	85	65	-	Н	-	Н	-	-	-	-	-	-	-	-	М	L	Н
CLO-4 :	Acquire knowledge and analyze on Filter Banks and adaptive filters and analyze such filters.	L1, L2	85	65	-	Н	Н	-	Н	-	-	-	Н	-	-	-	М	L	Н
CLO-5 :	Gain knowledge on DSP system design based applications.	L3	85	65	Н	-	-	-	Н	-	-	-	Н	-	-	-	М	L	Н
CLO-6 :	Apply the concept of DSP for real time applications				-	-	Н	-	Н	-	-	-	-	М	-	-	Н	М	М

Duratio	on (hour)	Learning Unit / Module 1 – TMS320C6X Architecture	Learning Unit / Module 2 – TMS3206X Assembly Language	Learning Unit / Module 3 – Frequency Transforms	Learning Unit / Module 4 – Digital Filters	Learning Unit / Module 5 – DSP Applications
	· /	15	15	15	15	15
S-1	SLO-1	Architecture of TMS320C6X	TMS320C6X Assembly Language Operations	Digital filtering using the DFT	Filter banks – Decimation,	Dual tone Multi-Frequency (DTMF) Signaling
•	SLO-2	Pipeline CPU	Individual Instruction Descriptions	Convolution and correlation	Inverse Decimation	Software Defined Radio (SDR)
• •	SLO-1	VelociTI, Functional Units,	Arithmetic operations, ,	Fast Fourier Transform –DIT	Perfect Reconstruction	QAM Transmitter and QAM Receiver
S-2	SLO-2	Addressing modes,	logical operations,	Fast Fourier Transform –DIT	Analysis of M-Band filter Banks	Miscellaneous ProjectsFSK Modem
S-3	SLO-1	Lab1: Generation of sequences	Lab 7: MAC operation using various	Lab 13: Spectrum analysis using	Lab 19: FIR Implementation using TMS	Lab 25: Equalization (Matlab)
3-3	SLO-2	(functional & random) (Matlab)	addressing modes	DFT(Matlab)	Processor	Lab 25: Equanzation (Matiab)
	SLO-1		Lab 8: MAC operation using various	Lab 14: FFT Implementation(DSP	Lab 20: FIR Implementation using TMS	
S-4	SLO-2	Lab 2: Correlation(Matlab)	addressing modes	processor)	Processor	Lab 26: Equalization (Matlab)
S-5	SLO-1	TMS320C6X Instruction Sets,	Memory data operations	Fast Fourier Transform –DIF	Orthogonality and Biorthogonality in Filter banks	u-Law for Speech Companding,
	SLO-2	Assembler directives	Conditional Operations	Fast Fourier Transform DIF	QMF Filter banks and	Acoustic Direction Tracker

S-6	SLO-1	Multichannel Buffered Serial Ports	Floating Point –Data type operations,	IFFT	(') E Filtor Banks	MultirateFilter,Neural Network for Signal Recognition
3-0	SLO-2	Memory Considerations –Constraints	Floating Point –Data type operations	FIR filters	Transmultiplixers;	PID Controller, Four-Channel Multiplexer for Fast Data Acquisition
S-7	SLO-1 SLO-2	Lab 3: Linear Convolution (Matlab)	Lab 9: MAC operation using various addressing modes	Lab 15: FIR filter design-Windowing Techniques(Matlab)	Lab 21: IIR implementation using TMS processor	Lab 27: Real time audio signal processing with Processor
S-8	SLO-1 SLO-2	Lab 4 :Circular convolution(Matlab)	Lab 10: Linear convolution(DSP processor)	Lab 16: FIR filter design-Windowing Techniques(Matlab)	Lab 22: IIR implementation using TMS processor	Lab 28: Real time audio signal processing with Processor
S-9	SLO-1	Instruction Operation and Execution notations	Fixed- Point Operations,	FIR filters	Structures and Programming Examples for Noise cancellation	Video Line Rate Analysis
2-9	SLO-2	Overview of IEEE Standard single and Double Precision formats ,	Fixed- Point Operations	IIR filter	Adaptive Filters-Adaptive filters in DSP simulation software's and TMSC320C6x	DSP System Design
S-10	SLO-1	Q-format Number Representation on Fixed Point DSPs, Finite Word length effects on Fixed point DSPS	Pipeline Operations overview	IIR filter	Software simulation of FIR	MP3 Player
	SLO-2	Floating point number representation, , Overflow and Scaling	Interrupts-overview.	FIR and IIR filter design using TMS320C6x	IIRFilters and Filter banks	DSP Automotive application
S-11	SLO-1	Lab 5: Study of architecture of Digital	Lab 11: Circular convolution(DSP	Lab 17: IIR filter design-Bilinear and		Lab 29: Real time audio signal processing
0-11	SLO-2	Signal Processor	processor)	Impulse Invariance Technique(Matlab)		with Processor
S-12	SLO-1 SLO-2	Lab 6: Study of architecture of Digital Signal Processor	Lab 12: Waveform generation(DSP processor)	Lab 18: IIR filter design-Bilinear and Impulse Invariance Technique(Matlab)	Lab 24: Finite Word Length Effect	Lab 30: Real time audio signal processing with Processor

1. B Venkataramani, M Bhaskar, "Digital Signal Proce:	 4. RulphChassaing - "DSP Applications Using C and the TMS320C6x DSK" John Wiley & Sons,
Applications", TMH Publishers, 2nd edition, 2017 Learning 2. Paulo S. R.DinizEduardo A. B. da Silva and Sergio I	Inc. 2002. 5. Nasser Kehtarnavaz , "Real-Time Digital Signal ProcessingBased on the TMS320C6000",
Analysis and Design", Cambridge University Pres 3. Nasser Kehtarnavaz, Namjin Kim, "Digital Signal Proces" 3. Nasser Kehtarnavaz, Namjin Kim, "Digital Signal Proces	Newnes, 2005.

	Bloom's			Cont	inuous Learning Asse	essment (50% weigl	htage)			Final Examinatio	n (EOO/ waightaga)
	Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA – S	3 (15%)	CLA – 4	(10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 1	Understand	20%	20%	15%	10%	10%	10%	15%	15%	15%	15%
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Leverz	Analyze	2070	20%	2076	2070	2070	2076	2076	20%	20%	20%
1	Evaluate	400/	400/	450/	450/	450/	450/	450/	450/	450/	450/
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	100) %	10	0%	100	0%	100) %		-

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	Mrs. K. Hari Sudha, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2 Dr. Vonkatosan Sr. Scientist NILUL Enernal Venkatiginiot res in	Dr. S. Dhanalakshmi, Assoc. Professor, SRMIST

Course Code	18ECE245T	Course Name		ΔΠΔΡΤΙΛ	E SIGNAL PR		JC					Cours	e	Е		Prot	fossion	nal Ele	ctivo		L	Т	Ρ	С
Course Coue	102022431	oourse name			LOIONALTIN							Catego	ory	L		110	10331011		01110		3	0	0	3
Pre-requisite 18ECC204J Co-requisite Nil Courses Nil Nil Nil									essive Irses)				18E	CE342	CE342T								
Course Offerin	ng Department		Electronics and C	ommunication Engine	eering	Data Bo	ook / (Codes	/Standar	ds								Nil						
Course Learni	ng Rationale (CLR):	The purp	pose of learning th	is course is to:			Le	earnin	g					Pro	gram	Learn	ing Ou	itcome	es (PL)	0)				
CLR-1 :	Have an insight on basi	cs of random pro	ocesses				1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Gain knowledge on the	applications of a	daptive filters				â	(%)	(%)				۲			ity								
CLR-3 :	Have an introduction on	LMS technique	s				300			e		÷	Research			Sustainability		논		a a			les	
CLR-4 :	Analyze the types of LN	IS algorithm					g (F	cien	Imel	ledo		men	Res	e		ıstaiı		Work		ance	5	nal	ct echniques	∞ŏ
CLR-5 :	Have an introduction on	RLS algorithm					nkir	Proficiency	Attainment	Knowledge	ysis	Development	Design, I	Usaç	Culture	& SL		Team	ы	& Fin	Luin	SSIO	0 H	Analyze
CLR-6 :	Understanding on need	and design of a	daptive filters using	different algorithms			of Thinking (Bloom)	ЧÞ		ing i	Analysis		Des	Tool Usage	Cul	lent		1& 1	Communication	Project Mgt. & Finance	ife Long Learning	: Professional ement	2: Proj	Ane
<u> </u>							alof	Expected	Expected	neer	Problem .	gn &	Analysis,	Em	ety &	Environment	ş	ndividual &	unu	ect N	Long	÷. €	1-2: ager	PSO-3: / Research
Course Learni	ng Outcomes (CLO):	At the er	nd of this course, le	earners will be able to):		Level	Exp	Шхр	Engineering	Prob	Design	Anal	Modem	Society	Envi	Ethics	ndiv	Com	Proje	Life	PSO- Achiev	PSO-2 Manager	PSC Rese
CLO-1 :	To review the basics of	statistical signal	processing				1	95	70	Н	-	-	-	-		-	-		-		-	-	-	-
-CLO-2 :	To understand about the	e need for adapt	ive filters and learr	the design of it.			1,2	95	70	Н	М	М	Н	-		-	-		-		-	-	L	Μ
CLO-3 :	To acquire knowledge c	on LMS algorithm	ns and constraints	associated with it.			2,3	85	65	Н	Н	Н	Н	L							М	-	М	Μ
CLO-4 :	To learn the variants of	LMS algorithm a	and design of lattice	e structures			2	85	65	Н	М	Н	Н	-	-	-	-		-		-	-	М	М
CLO-5 :	To gain knowledge on d	lesign of RLS filt	ers and others asp	d others aspects of filter design			1,2	85	65	Н	М	М	Н	L	-	-	-	-	-	-	М	-	М	М

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85 65 Н

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Duratio	on (hour)	Introduction to Stochastic Process	Adaptive Filters	Least Mean Square Algorithm	Variants of LMS Algorithm And Lattice Structures	Recursive Least Square Algorithm
	()	9	9	9	9	9
	SLO-1	Introduction to random process	Introduction to adaptive filters	Least mean square algorithm	Sign LMS algorithm	Recursive adaptive filters
S-1	SLO-2	Variables, vectors	Block diagram of adaptive structure with shift variant filter	Derivation	Normalized LMS	Principle of RLS algorithm
	SLO-1	Ensemble averages	Properties of adaptive filter	Properties of LMS adaptive filters	Leaky LMS	FIR RLS filter algorithm
S-2	SLO-2	Time averages	Error sequence generation in adaptive filters	Properties of LMS adaptive filters	Block LMS	Derivation
S-3	SLO-1	Stationarity and Stationary random process	Channel Equalization- Block diagram of communication system with Channel equalization	Complex LMS algorithm	FFT based implementation of block LMS	Sliding window RLS
	SLO-2	Wide sense stationarity	Echo cancellation	Convergence of LMS algorithm	FFT based implementation of block LMS	Derivation
S-4	SLO-1	Power Spectral Density	Concept of adaptive noise cancelling	Learning curve for adaptive filters	Comparison of variants on LMS for some practical problem	Comparing variants of RLS using MATLAB program
5-4	SLO-2	Properties of PSD	Beam forming with pilot signals	Sample MATLAB program for LMS convergence and plotting learning curve	Comparison of variants on LMS for some practical problem	Comparing variants of RLS using MATLAB program
S-5	SLO-1	Sample problems on WSS random process	System modeling using adaptive filters	Performance analysis of LMS adaptive filters by varying step size (MATLAB)	Lattice filters introduction	Kalman filters
3-3	SLO-2	Sample problems on WSS random process	System Identification structure	Performance analysis of LMS adaptive filters by varying step size (MATLAB)	Advantages of Lattice structures	Kalman filters

CLO-6 :

adaptive filters

To understand the applications of adaptive signal processing and algorithms in designing the

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	SLO-1	Filtering of random process	System inversion using adaptive filters	Weight error correlation matrix	Forward linear prediction	Sample problems on RLS algorithms
S-6	SLO-2	Filtering of random process	Interference cancellation in multi sensor systems	LMS misadjustment definition	Forward linear prediction	Sample problems on RLS algorithms
	SLO-1	Autocorrelation Structures	Minimization of mean square error	Effects of misadjustment factor	Backward linear prediction	Non linear adaptive filters
S-7	SLO-2	Covariance Structures	Derivation on MMSE	Sample problems for designing adaptive filters using LMS	Backward linear prediction	Introduction to Neural networks
S-8	SLO-1	Eigen value decomposition	Steepest Descent algorithm	Sample problems on step size		Neural networks and multilayer perceptorns
	SLO-2	Eigen value analysis of autocorrelation matrices	Linear prediction example	Sample problems on stop size		Neural networks and multilayer perceptorns
S-9	SLO-1	Ergodicity	Wiener filters	Stability analysis of LMS algorithms	Properties of Lattice structures	Adaptive IIR filtering
3-9	SLO-2	Ergodic random process	Optimization solution in wiener filters	Stability analysis of LMS algorithms	Updating predictor coefficients	Adaptive IIR filtering

	1.	S. Haykin ,Adaptive Filter Theory, Prentice-Hall, 4-th edition, 2001.		
Learning	2.	Ali H. Sayed ,Fundamentals of Adaptive Filtering, John Wiley, 2003.		 B. Widrow, S. Stearns, Adaptive Signal Processing, Prentice-Hall, 1985
Resources	3.	D. Manolakis, V. Ingle, S. Kogan, Statistical and Adaptive Signal Processing: Spectral Estimation,	5.	Monson H. Hayes, Statistical Digital Signal Processing and Modeling, Edition: 1st, 2008.
		Signal Modeling, Adaptive Filtering and Array Processing, McGraw Hill, 1999.		

Learning Asse	essment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examinatio	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100	0 %	100	0 %	100	0 %	100	0 %	10	0 %

Course Designers		
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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Cou	rse Code	18ECE340T	Course Nan	e	MACHINE PERCEPTION	WITH COGNITION	Course Category	Е	Professional Elective	L 3	Т 0	P 0	C 3
P	Pre-requisi Courses		18ECE242		Co-requisite Courses	Nil	5	ressive urses	Nil	1			
Cou	Course Offering Department			Electronics and C	Communication Engineering	Data Book / Codes/Standards			Nil				

Course Lea	arning Rationale (CLR):	The purpose of learning this course is to:	L	earni	ng					Pro	gram	Learn	ing Ou	utcom	es (PL	0)				
CLR-1 :	Have an insight on image a	nd color fundamentals	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Analyze the various shapes	and regions for the image description		cy	Ħ	je		t						¥		a		_	niques	
CLR-3 :	Acquire knowledge on the te	þ	cien	ame	vledç		mer		ge				n Work		Finance	0	ional	hniq		
CLR-4 :	Identify the relation betweer	n the templates to match the image requirements	hinking	Proficie	Attainme	know	nalysis	/elop	sign,	Usage	Culture	~		Team	u	& Fin	eaming	fess	ject Tec	alyze
CLR-5 :	Know the practical application	ons of computer vision in images understanding	of T	cted	cted A	Engineering Knowledge	<	n & Dev	s, De ch	n Tool	~ð	nment nability		حە	ommunication	Mgt.	ong Lea	- 1: Pro /ement	- 2: Pro	- 3: Ana
Course Lea	rning Outcomes (CLO):	At the end of this course, learners will be able to:	Level		Expe	Engine	Problem.	Design	Analysi Resear	Moder	Society	Envirol Sustair	Ethics	Individual	Comr	Project	Life L (PSO - Achie	PSO- Manag	PSO - &Rest
CLO-1 :	To outline the fundamentals	of image and color models	1	90	85	Н		L		М	Н							L	L	М
CLO-2 :	Understand the basic shape	es and region based image modeling	1	85	85		Н						Н					Μ	Н	Μ
CLO-3 :	Analyze the various textures	s for image synthesis	1&2	85	80			М			Μ		Н					L	М	М
CLO-4 :	Identify the objects based of	n template relations	1&2	85	75							Н	М	М				L	М	М
CLO-5 :	To apply the image underst	anding knowledge for image recognition	2&3	85	80	Н	М						Н	М				L	М	Н
CLO-6:	0-6: To understand the principles of image modeling and synthesis with image recognition			85	80	М	Н	Н	Н	М								М	М	Н

		Learning Unit / Module 1	Learning Unit / Medule 2	Learning Unit / Module 3	Learning Unit / Module 4	Learning Unit / Module 5
Duratio	n (hour)	Basic Audio Processing	Learning Unit / Module 2 Human Auditory System	Speech Signal Analysis in Time Domain	Speech Signal Analysis in Frequency Domain	Speech and Audio processing applications
		12	12	12	12	12
S-1	SLO-1	Review of Image processing methods	Binary Shape analysis	Representing textures	Finding objects by voting on relation between templates	Face detection
3-1	SLO-2 Review of Image processing metho		Binary Shape analysis	Representing textures	Interest points, Simple voting, Voting on raltions.	Face detection
S-2	SLO-1	Introduction to image formation	o image formation Connectedness Extracting image Structure with filte banks		Relational reasoning using probabilistic framework	Face recognition
5-2	SLO-2	Introduction to image formation	Object labeling and counting	Extracting image Structure with filter banks	Growing Assemblies Incrementally, Detection, Pruning	Face recognition
S-3	SLO-1	Image models	Size filtering	Representing texture using statistics of filter output	Frames and probability models	Eigen faces
3-3	SLO-2	Camera models	Distance functions	Representing texture using statistics of filter output	Representing coordinate frames	Active appearence
S-4	SLO-1	Sample programs for reading images, understanding pixels	Skeletons and thinning	Analysis using oriented pyramids	Using probability model for detecting the frames	3D shape models of face surveillance
3-4	SLO-2	Sample programs for reading images, understanding pixels	Deformable shape analysis	Laplacian pyramids	Building probability models for frame invariant	3D shape models of face surveillance
S-5	SLO-1	Shadows	Boundary tracking procedures	Filters in the spatial frequency domain	Classifiers to prune search	Foreground separation

	SLO-2	Color representation	Boundary tracking procedures	Filters in the spatial frequency domain	Identifying acceptable assemblies	Background separation
S-6	SLO-1	Human color perception	Shape models	Oriented pyramids	Sample examples for prune search	Particle filters
3-0	SLO-2	Human color perception	Shape recognition	Oriented pyramids	Hidden Markov model	Particle filters
S-7	SLO-1	Image color	Centroidal profiles	Synthesizing textures for rendering	Computing, Maximizing parameters	Champer matching, tracking and occlusions
3-1	SLO-2	Image color	Handling occlusions	Synthesizing textures for Homogeneity	Varieties of HMM	Champer matching, tracking and occlusions
	SLO-1	Handling Color Images (MATLAB)	Boundary descriptors	Synthesis by sampling local models	Background subtraction	Combining views from multiple cameras
S-8	SLO-2	Handling Color Images (MATLAB)	Boundary descriptors	Synthesis by sampling local models	Sample programs on background subtraction	Human gait
S-9	SLO-1	Surface Color	Region descriptors	Shape from texture planes		
3-9		Surface Color	Region descriptors	Texture from shape planes	Sample problems on Hough transforms	Scene modeling from registered and unregistered images

Learning Resources	 E. R. Davies, "Computer & Machine Vision", Fourth Edition, Academic Press, 2012. R. Szeliski, "Computer Vision: Algorithms and Applications", Springer 2011. Simon J. D. Prince, "Computer Vision: Models, Learning, and Inference", Cambridge University Press, 2012 	 Mark Nixon and Alberto S. Aquado, "Feature Extraction & Image Processing for Computer Vision", Third Edition, Academic Press, 2012 D. L. Baggio et al., "Mastering OpenCV with Practical Computer Vision Projects", Packt Publishing, 2012 Jan Erik Solem, "Programming Computer Vision with Python: Tools and algorithms for analyzing images", O'Reilly Media, 2012.
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Learning Assessment											
	Bloom's	Continuous Learning Assessment (50% weightage)							Final Examination (50% weightage)		
	Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4 (10%)#		Final Examination (50 % weightage)	
		Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Understand										
Level 2	Apply	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
	Analyze										
Level 3	Evaluate	30 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Create										
	Total	100 %		100 %		100 %		100 %		100 %	

Course Designers						
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts				
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. A. Ruhan Bevi, SRM IST				
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@ici.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in					

Course C	ode	18ECE341T	Course Name		Multimedia Compression Techniques							Course ategory	1	Е		Profe	ession	al Elec	tive	:	L 3	T 0	P 0	C 3	
Pre-rec Cou			18ECE240T		Co-requisite Courses			I	Nil				F	rogres Cours							Nil				
Course Of	fering De	partment		Electronics and C	ommunication Engine	eering	Data	Book / Co	odes/S	Standards									Nil						
Course Le	arning Ra	ationale (CLR):	The purpo	ose of learning this	s course is to:		7	Le	arning	1					Pro	gram	Learn	ing Oı	utcom	es (PL	.0)				
CLR-1 :	Learn abo	ut probability mod	lel and coding the	eory				1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Understan	nd about lossless o	compression						cy	t	e	5	Ħ						ork		Θ		_	PSO – 2: Project Management Techniques	
CLR-3 :	Understan	nd about Lossy da	ta compression					бu	cien	ame	vlede		ome		ge				υMα		Janc	Ð	iona	hniq	
		ut the encoding m						inki	Profi	Attai	X NO	lysis	velol	sign,	Usa	Iture	8		Tean	ion	& Fii	amin	ofess t	ject t Tec	alyze
CLR-5 :	Compress	ion Techniques a	nd their applicatio	ns				f Th)	BdF	d be	uino.	Ana	s De	De Pe	Tool	& Cu	nen t ability		al & '	nicat	Agt.	g Le	: Pro ment	E Pro	ch An
								Level of Thinking (Bloom)	Expected Proficiency	Expected Attainment (%)	Encineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modern Tool Usage	Society & Culture	Environment & Sustainability	cs	ndividual & Team Work	Communication	Project Mgt. & Finance	Life Long Leaming	0 – 1: Professional iievement) – 2 nage	PSO – 3: A &Research
		utcomes (CLO):			arners will be able to:								Des				Env Sus		Indi	Sor	D D	Life	PSO. Achie	PS(Mar	PS R R
			1 1		practical limits specif	, ,	theory	L1&L2	90	68	Н			М	М	М		М				М	Н	М	Н
			1							68	Н			M	М	М		М				М	Н	М	Н
		nd the fundamenta	11	, ,				L2&L3	86	68	Н			Н	М	М		M				М	М	M	Н
		•			ntify important compo	onents to be er	ncoded		85	63	Н			Н	Н	М		Н				М	М		Н
		ns of various com	1					L2&L3 L1.L2.L3	85	63 68	M			M	М	H M		М				М	Н	M	M
CLO-6:	Learn abo	ut data compressi	on and encoding	metnoas				L1,L2,L3	90	68	Н	IVI		М	М	IVI		Н				М	Н	М	Н
	<i>a</i>)	Learning	g Unit / Module 1		Learning Unit / Modu	ule 2		Learning	y Unit	/ Module 3	}		Lea	rning l	Jnit / I	Nodul	e 4			Lear	rning l	Jnit / I	Nodul	e 5	
Duratior	n (hour)		9		9				9						9				9						
S-1	SLO-1	The discrete men source	nory less informat	tion Mathema	atical Preliminaries fo	r Lossless	Rate dis	stortion fu	nction			Vec	or Qua	ntizatio	ו				Transf Karhui Image 2000-	nen-Lo comp	oeve tra ression	η – EZ	m, , , , W, SP		,
	SLO-2	Kraft inequality; c	ptimal codes	Mathema Compres	atical Preliminaries fo	r Lossless	Rate dis	stortion fu	nction			LBG	algorith	nm					Karhu	nen-Lo	beve tr	ansfor	m		
S-2	SLO-1	Source coding the	eorem-Entropy	Huffman	Coding		Properti	es of RD				Tree	structu	red VQ					Karhu	nen-Lo	beve tr	ansfor	m		
012	SLO-2	Joint Entropy and	I Conditional Entr	opy Huffman	Coding			es of RD				Stru	ctured \	/Q					discret	te cosi	ne trar	nsform	,		
S-3	SLO-1	Relative Entropy		Optimali	ty of Huffman codes	i	and the	Gaussiar	n sourc			Vari	ations o	f VQ					discret	te cosi	ne trar	nsform	,		
0-0	SLO-2	Mutual Informatic	'n	Extende	d Huffman Coding			tion of RD Gaussiar		e binary so ce	ource	Gair	shape	VQ					discret	te Wal	sh Hao	damaro	d trans	form	
S-4	SLO-1	Chain Rules		Adaptive	Huffman Coding		Rate dis	stortion th	eorem			Mea	n remov	ved VQ						te Wal				-	
3-4	SLO-2 Data-Processing Inequality Arithmetic Coding Rate distortion theorem					Classified VQ					Quantization and coding of transform coefficients														
S-5	SLO-1 Fano's Inequality Symmetric Channels Adaptive Arithmetic coding Converse of the				se of the I	Rate d	istortion the	eorem	Mult	istage \	/Q					Quantization and coding of transform coefficients									
3-3																									

SLO-2 Fano's Inequality Symmetric Channels

Run Length Coding

Quantization problem

Adaptive VQ

JPEG

S-6	SLO-1	Properties of Channel Capacity, Jointly Typical Sequences	Dictionary Techniques	Scalar Quantization- Uniform Quantizer	Trellis coded quantization Transforms.	JPEG
3-0	SLO-2	Properties of Channel Capacity, Jointly Typical Sequences	Lempel Ziv coding	Scalar Quantization- Uniform Quantizer	Trellis coded quantization Transforms.	MDCT
	SLO-1	Channel Coding Theorem	Applications	Adaptive Quantization	Basic algorithm	MDCT
S-7	SLO-2	Channel Coding Theorem	Predictive Coding	Adaptive Quantization	Prediction in DPCM	Image compression – EZW- Analysis/Synthesis Schemes
S-8	SLO-1	Fano's Inequality	Prediction with Partial Match	Non-uniform Quantization	Prediction in DPCM	Image compression – SPIHT- Analysis/Synthesis Schemes
3-0	SLO-2	Fano's Inequality	Burrows Wheeler Transform	Non-uniform Quantization	Adaptive DPCM	Image compression – JPEG 2000- Analysis/Synthesis Schemes
S-9	SLO-1	Converse to the Coding Theorem	Dynamic Markov Compression	Entropy coded Quantization	Adaptive DPCM	Audio coding:-MPEG audio coding
3-9	SLO-2	Converse to the Coding Theorem	Dynamic Markov Compression	Entropy coded Quantization	Delta Modulation	Audio coding:-MPEG audio coding

Learning Resources		 K. Sayood, "Introduction to Data Compression", 3rd Edition, Morgan Kaufmann Publishers, 2006. N. Jayant and P. Noll, "Digital Coding of Waveforms: Principles and Applications to Speech and Video", ISBN10 0132119137, Prentice Hall, USA, 1984. D. Salomon, "Handbook of Data Compression", 5th Edition, Springer-Verlag London Limited 2010. 	4. 5.	Ze.Nian. Li and M.S. Drew, "Fundamentals of Multimedia",2 nd Edition, Pearson Education (Asia) Pvt. Ltd., 2004. M.Rabbani: "Digital image compression techniques", 1 st Edition, SPIE Press Book, 1991.
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Learning As	Learning Assessment												
	Bloom's				Final Examination (50% weightage)								
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		ii (50 % weightage)		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Level 1	Remember Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-		
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-		
Level 3	Evaluate Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-		
	Total 100 %			100) %	100	0 %	100) %	100 %			

Course Designers		
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Course Code	18ECE342T	Course Name	ACOUSTICAL SIGN	AL PROCESSING	Course Category	Е	Professional Elective	L 3	T 0	P 0	C 3
Pre-requis Courses		18ECE245T	Co-requisite Courses	Nil	Progressiv Courses	/e	Nil				
Course C	Offering Departmen	t Ele	ctronics and Communication Engineering	Data Book / Codes/Standards	des/Standards ISO/TC 43/SC 1, ISO/TC 43/SC 2						

Course Learning Rationale (CLR): The purpose of learning this course is to:	Learning			Ρ	rogra	am Le	arning	g Outo	omes	(PLO)									
CLR-1: learn what is acoustic and its basic sound equations, and how acoustics transmission, reflection, absorption when subjected to various mediums	1	2	3	1	•	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 : learn how does human auditory system and hearing function.		(%)	(%)					ch			ility								
CLR-3 : learn what is acoustic echo, and how to control such echo due to noise and cancel echo using various algorithms.	D	Proficiency	Attainment (ledge		Development	Resear	je		Sustainability		Work		ance	5	nal	hniaues	~ð
CLR-4 : learn what are the various types of transducers used for acoustic measurements	Thinking	ofic	ttair		Nov,	ysis	elop	Design,	Usaç	Culture			Team	5	& Financ	arning	ssio	Tec	lyze
CLR-5 : knowwhich transducers can be used in various applications of acoustics.	D of	sted P	pected A		Engineering Knowledge	m Analysis	∞ŏ	is, Des	n Tool Usage	/ & Cul	Environment &			ommunication	Project Mgt. 8	ong Lea	1: Professional /ement	2: Proj	3: Analy. rch
Course Learning Outcomes (CLO): At the end of this course, learners will be able to:	Level (Bloor	Expected	Expe		Engine	Problem	Design	Analysis,	Modem	Society &	Enviro	Ethics	Individual &	Comm	Projec	Life Lo	PSO-	PSO- Manac	PSO - Resea
CLO-1 : To understand the basics of acoustic such as Sound equation, Transmission, Reflection, Absorption under various mediums	L1	80	65		М											Н	Н	М	Н
CLO-2 : To acquire knowledge on human auditory system and hearing	L1	85	65		М											Н	Н	М	Н
CLO-3 : To understand and analyze acoustic echo, noise control and cancel echo using various algorithms.	L1, L2	85	65		Н	Н	Н	Н	Н							Н	М	М	Н
CLO-4: To understand about various types of transducers used for acoustic measurements	L1	85	65		М	Н	Н										Н	М	Н
CLO-5 : To gain knowledge on various applications of acoustics.			65			Н		Н								Н	Н	М	М
CLO-6 Speech processing analysis in different environment			65		Н	Н	Н	Н	Н							Н	Н	Н	М

-	iration hour)	Basics of Acoustic Engineering	Auditory System and Hearing	Acoustic Echo and Noise control	Transducers for Acoustic Measurements	Applications of Acoustics
ų	iour)	9	9	9	9	9
S-1	SLO-1	Introduction to acoustic	Anatomy of the auditory systems	Human Perception of Echoes	Fundamental properties of Transducers	Architectural acoustics – Sound in enclosures
5-1	SLO-2	Introduction to acoustic Anatomy of the auditory systems Human Perception of Echoes Fun		Fundamental properties of Transducers	Reverberation time	
S-2	SLO-1	Harmonic Plane Waves	Physiology of the auditory systems	Echo Problem	Condenser Microphones	Sound absorption materials
5-2	SLO-2	Harmonic Plane Waves	Physiology of the auditory systems	Echo Problem	Condenser Microphones	Measurements of acoustic output in living rooms
S-3	SLO-1	Energy Density	Function of the auditory systems	Adaptive Filters for Echo Cancellation	Dynamic Pressure Microphones	Acoustic Factors in architectural design
3-3	SLO-2	Energy Density	Function of the auditory systems	Adaptive Filters for Echo Cancellation	Dynamic Pressure Microphones	Environmental acoustics – Introduction
S-4	SLO-1	Acoustic Intensity	Physiological measures	LMS algorithm	Dynamic Pressure Microphones	Weighted sound level
3-4	SLO-2	Specific Acoustic Impedance	Physiological measures	NLMS algorithm	Dynamic Pressure difference Microphone	speech interference

S-5	SLO-1	Spherical Waves	Physiological measures	Least Squares Algorithms	Dynamic Pressure difference Microphone	Highway noise
3-3	SLO-2	Spherical Waves	Auditory processing models	Least Squares Algorithms	Piezo ceramic accelerometer	Aircraft noise rating
S-6	SLO-1	Decibel Scales	Auditory processing models	Recursive Least Squares Algorithms	Piezo ceramic accelerometer	Virtual Sound
3-0	SLO-2	; Rays and Waves	Auditory processing models	Recursive Least Squares Algorithms	Piezo ceramic accelerometer	Sound localization cues
S-70	SLO-1	Transmission-Incidence	Auditory processing models	Affine Projection algorithm	Laser Doppler velocimeter	synthetic 3D Audio
5-70	SLO-2	Transmission-Incidence	Auditory processing models	Affine Projection algorithm	Laser Doppler velocimeter	synthetic 3D Audio
S-8	SLO-1	Reflection	Speech Intelligibility	Noise cancellation using Affine Projection algorithm	II aser Loppier velocimeter	Seismology- Signal Model in seismic processing
5-6	SLO-2	Absorption	Speech Intelligibility	Noise cancellation using Affine Projection algorithm	Capacitive sensors	Optical sensor Signal Model in seismic processings
S-9	SLO-1	Viscosity	signal processing in hearing aids	Fast Affine Projection Algorithm (FAP).	Capacitive sensors	Underwater and Oceanographic acoustics
5-9	SLO-2	Thermal conduction	signal processing in hearing aids	Fast Affine Projection Algorithm (FAP)	Capacitive sensors	Inverse Problems in underwater acoustics

Learning Resources	 Lawrance E Kinseler, Fundamental of Acoustic, , Wiley 4th Edition. Steven L. Gay, Jacob Benesty, Acoustic Signal Processing for Telecommunication, Springer; 2001 edition (March 31, 2000) 	 Havelock, David; Kuwano, Sonoko, Vorländer, Michael (Eds.), Handbook of Signal Processing in Acoustics, Springer; 2008 edition.
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Learning As	earning Assessment												
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			- Final Examination (50% weightage			
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	l (10%)#		in (50 % weightage)		
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Level 1	Remember Understand	40 %		30 %		30 %		30 %		30 %			
Level 2	Apply Analyze	40 %		40 %		40 %		40 %		40 %			
Level 3	Evaluate Create	20 %		30 %		30 %		30 %		30 %			
	Total	100	0 %	10	0 %	10	0 %	10) %	10	0 %		

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. S. Dhanalakshmi, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@jci.com</u>	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. Damodar Panigrahy, SRMIST

mic													
Course	18ECE343T	Cours	e	Autor	natic Space	h Recognition	Course	E	Professional Elective	L	Т	Ρ	С
Code	102023431	Name	•	Autor	nalic Speec		Category	L	riolessional Liective	3	0	0	3
	_			1			_						
Pre-requisite		18ECE2	41J	Co-requisite		Nil	Progressiv	e	Nil				
Courses				Courses			Courses						
Course Offering	g Department		Electronics and Co	ommunication Eng	ineering	Data Book / Codes/Standards			Nil				

Course Lea	arning Rationale (CLR): The purpose of learning this course is to:	ר ר	Le	earnir	ng					Prog	ram L	earn	ing O)utcor	nes (F	PLO)				
CLR-1 :	Understand the basic Techniques of Speech Recognition		1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Analyze the different Statistical models		Ê	(%)	(%)				сŀ			lity								
CLR-3 : /	Modeling different speech recognition systems		(Bloom)		nt ('	ą		ŧ	earc			stainability		¥		Ð			ues	
CLR-4 :	Evaluation of dialogue system design		J) G(Proficiency	Attainment	hedr	Í	Development	Res	ge		ıstai		Nork		Finance	0	nal	ct echnique:	×ð
CLR-5 :	Analyzing the Stochastic Approaches to dialogue		hinking	rofic	ttair		Analysis	/elop	sign,	Usage	Culture	& Su		Team	Б	& Fir	Leaming	essic	Tect	alyze
CLR-6 : (Itilize the concepts in signal processing for the understanding of engineering and technology		—	Ъ	A bi		Anal		De	Tool	& Cul	ent		~~	nication	Mgt. 8	Lea	^{>} rofe	Pro	Å
			el of	ecte	ecte	⁻ nninee ring Knowledge	Problem	esign &	ysis,	Aodern 7	ociety 8	Environment	ş	dividual	_	ect N	Long	⊢1: ever	2: ager	earch
Course Lea	arning Outcomes (CLO): At the end of this course, learners will be able to:		Lev	Expected	Expected		pod d	Desi	Analy	Mod	Soci	Envi	Ethics	Indiv	Comm	Project I	Life	PSO Achi	Man	PSC Rese
CLO-1 : (Inderstand the basic techniques in speech signal processing broadly used in the area of speech recognition		2	80	63	H	Н	-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-2 : 1	earn how hidden Markov models can be used as generative models for speech and how they can be trained		2	85	68	H	Н	-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-3 :	To help in understanding account for commercial as well as research-oriented applications within speech recognition		2	75	68	H	-	-	Н	-	-	-	-	-	-	-	-	-	-	-
CLO-4 :	To understand basic understanding of dialogue system design and evaluation		2	85	68	H	Н	-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-5 : /	mplement simple dialogue systems and Stochastic Approaches		2	85	68	H	-	Н	-	-	-	-	-	-	-	-	-	-	-	-
CLO-6 : /	Apply the speech recognition techniques in real time applications.		2	80	68	H	-	-	-	-	-	-	-	-	-	-	-	-	-	-

	iration	Distance Measurements For Comparing Speech Patterns	Statistical Models For Speech Recognition	Architecture of Continuous Speech Recognition System	Understanding of Spoken Dialogue Systems	Natural Language Generation and Stochastic Process
ų	hour)	9	9	9	9	9
S-1	SLO-1	Feature, Feature Extraction and Pattern Comparison Techniques	Introduction to Perceptual Motivated Representations	Introduction to speech recognition	Simple models of dialogue structure:	Natural language generation for dialogue systems
3-1	SLO-2	Feature, Feature Extraction and Pattern Comparison Techniques	Perceptual Motivated Representations	Introduction to speech recognition	Simple models of dialogue structure:	Natural language generation for dialogue systems
S-2	SLO-1		Formant Frequencies – Role of Pitch – Pitch Detection of Speech and Music	Large vocabulary continuous speech recognition	Trees and finite state approaches	Text-to-speech synthesis
3-2	SLO-2	Speech Distortion measures-Mathematical	Formant Frequencies – Role of Pitch – Pitch Detection of Speech and Music	Large vocabulary continuous speech recognition	Trees and finite state approaches	Text-to-speech synthesis
S-3	SLO-1	Perpectual-Log spectral distance	Channel Vocoders and Predictive Coding Scalar Waveform Coders	Architecture of large vocabulary continuous speech recognition system	Dialogue acts, key phrase reactive approaches	Use of speech synthesizers in dialogue systems
3-3	SLO-2	Perpectual-Log spectral distance	Channel Vocoders and Predictive Coding Scalar Waveform Coders	Architecture of large vocabulary continuous speech recognition system	approaches	Use of speech synthesizers in dialogue systems
S-4	SLO-1	Cepstral Distances, Weighted Cepstral distances and Filtering	Scalar Frequency Domain Coders	Architecture of large vocabulary continuous speech recognition system	Information retrieval-based approaches	Dialogue system evaluation
3-4	SLO-2	Likelihood Distortions	Code excited linear Prediction	Architecture of large vocabulary continuous speech recognition system	Information retrieval-based approaches	Dialogue system evaluation
S-5	SLO-1	Spectral distortion using a Warped	Low – Bit rate Speech coders	Acoustics model	Voice XML	Stochastic approaches to dialogue

	SLO-2	Frequency scale	Speech Recognition			
S-6	SLO-1	LPC, PLC and MFCC Coefficients	Hidden Markov Models (HMM) – Practical Issues in Using HMMs – HMM Limitations	Language model	Speech recognition	Dialogue policy design and training
3-0	SLO-2	LPC, PLC and MFCC Coefficients	Hidden Markov Models (HMM) – Practical Issues in Using HMMs – HMM Limitations	Language model	Speech recognition	Dialogue policy design and training
S-7	SLO-1	Time Alignment and Normalization	Acoustic Modeling – Phonetic Modeling, Language Modeling	n-gram model	Use of speech recognizers in dialogue systems	MDP reinforcement learning
3-1	SLO-2	Time Alignment and Normalization	Acoustic Modeling – Phonetic Modeling, Language Modeling	n-gram model	Use of speech recognizers in dialogue systems	MDP reinforcement learning
S-8	SLO-1	Dynamic Time warping	Speaker Recognition Algorithm	context dependent sub word units	Natural language understanding	POMDP reinforcement learning
	SLO-2	Dynamic Time warping	Speaker Recognition Algorithm	context dependent sub word units	Natural language understanding	POMDP reinforcement learning
S-9	SLO-1	Multiple Time-Alignment Paths	Signal Enhancement for Mismatched Conditions	Applications and present status	Natural language understanding	Simulated users
3-9	SLO-2	Multiple Time-Alignment Paths	Signal Enhancement for Mismatched Conditions	Applications and present status	Natural language understanding	Simulated users

Learning Resources		Huang, A. Acero, H-W. Hon, "Spoken Language Processing: A guide to theory, algorithm and system development", Prentice Hall 2001 Rabiner and Juang, "Fundamentals of Speech Recognition", Prentice Hall, 1993	4. 5.	Jurafsky, Daniel, and James H. Martin, "Speech and Language Processing: An Introduction to Natural Language Processing, Speech Recognition, and Computational Linguistics", 2nd edition. Prentice-Hall, 2009. Jokinen and McTear, "Spoken Dialogue Systems, Morgan & Claypool, Synthesis Lectures on Human
	3.	F. Jelinek, "Statistical Methods for Speech recognition", MIT Press, 1997		Language Technologies", Morgan & Claypool Publishers, 2009

Learning As	sessment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Einal Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	¥ (10%)#		i (50 % weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40%	-	30%	-	30%	-	30%	-	30%	-
Level 2	Apply Analyze	40%	-	40%	-	40%	-	40%	-	40%	-
Level 3	Evaluate Create	20%	-	30%	-	30%	-	30%	-	30%	-
	Total	100 % 100 %			0 %	10	0 %	10	100 %		0 %

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B. Tech in Electronics and Communication Engineering

2018 Regulations

OpenElective Courses (O)

Department of Electronics and Communication Engineering SRM Institute of Science and Technology SRM Nagar, Kattankulathur – 603203, Kancheepuram District, Tamilnadu

Course Co	do	18ECO101T	Course Name		Short Range Wireless Con	nmunica	ation						Cours	e	0			Open	Floctiv	0		L	Т	Р	С
000130 00	uc	102001011	oourse num		Chort Mange Wholess Con		auon					(Catego	ory	Ŭ			Open	Liccur	0		3	0	0	3
Pre-req Cour			Nil		Co-requisite Courses			Nil						•	ressiv urses	e					Nil				
Course Off	ering De	epartment		Electronics and C	ommunication Engineering	Data B	ook /	Code	s/ Stan	dards									Nil						
Course Lea	arning R	ationale (CLR):	Understand the	concept of Short r	ange Wireless Communication	1 [L	earnin	g						Pro	gram	Learn	ing Ou	utcom	es (PL	0)				
		of different modula					1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-3 : (CLR-4 : CLR-5 : (COurse Lea	Analysis o Communi To know a Design ar Arning O	of the various kinds ication. about regulations a nd analysis of short utcomes (CLO):	s of transmitters and standards o t-range radio lik The purpose of communication receivers, desig	and receivers used f ISM band commune UWB and Visible of this course is to including wave gn principles, telecco	light. introduce practically all aspects of propagation, antennas, transm mmunication regulations	nitters,	Level of Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment (%)		Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	PSO-1: Professional Achievement	PSO – 2: Project Management Techniques	PSO – 3: Analyze & Research
		the various forms on system properties.	of signals used t	or information trans	mission and modulation, and overall		2	80	70		L	-	-	-	-	-	-	-	-	-	-	-	-	Н	-
					nent a short-range radio system.		2	85	75		-	-	М	L	-	-	-	-	-	-	-	-	Н	-	-
		be the various kind					2	75	70		-	-	Н	М	-	-	-	-	-	-	-	-	-	Н	-
				band communicati			2	85	80		М	-	-	-	-	-	-	-	-	-	-	-	М	-	<u></u>
CLO-5 :	to covers	s some of the most	important new	developments in sh	ort-range radio like UWB and Visible	light.	2	85	75		-	-	L	М	-	-	-	-	-	-	-	-	-	-	Н

D	tion (hour)	Wireless Systems	Baseband Coding basics	RF transceivers	Wireless standards	Optical wireless Technologies
Dura	tion (hour)	9	9	9	9	9
S-1	SLO-1	Introduction to wireless systems	Types of Antennas-Dipole, groundplane, loop	RF Receivers- Introduction	Technical Background to the WPAN Concept -Regulation and Standardization Issues	Fundamentals of UROOF Technologies
		Reasons for the Spread of Wireless Applications	Helical, Patch antennas	RF Source-Frequency control	European Consortium: Overview	Conversion from RF to Optical Domain
S-2	SLO-1	Characteristics of Short-range Radio	Antenna Characteristics-Impedence, directivity and gain, Effective area	Modulation types	Millimeter-Wave Applications and Services - PAN scenarios in the IST Magnet project	Conversion from Optical to RF Domain
	SLO-2	Wireless Applications	Polarization, Bandwidth, Antenna factor	Amplifiers	Typical LDR services connected to the IST- FP6 MAGNET project	Optical Microwave Mixing Used for UWB Over Systems
S-3	SLO-1		Baseband Data Format and Protocol - Radio Communication Link Diagram	Impedance matching in transmitter and receivers	Frequency Regulation and Standardization Issues - Optional UM4 usage models issued from the IEEE802.15.3c TG	Integrated UROOF Transceiver (IUT)
	SLO-2	Elements of Wireless Communication Systems-Receiver	Code Hopping	Filtering	Flexible antenna gain, 60 GHz regulation status for wireless transmissions.	Mixed Wireless-wired UROOF Channel,Carrier-to-noise Ratio
S-4	SLO-1	Wireless Local Area Networks (WLAN)- WIFI	Baseband Coding-Digital systems	SAW band pass filter matching	Channel Propagation Characterization and Modeling- 60 GHz Propagation Measurements	Laser and Photodetector Noise Baseline,
	SLO-2	Network Architecture	Wireless Microphone System		Propagation Channel Characterization	Clipping Distortion Implication , Latency
S-5	SLO-1	Bluetooth Transceiver	RF Frequency and Bandwidth-factors	ASH Receiver	Multipath Propagation Modeling	Modelling the Propagation through the

						Fibre
	SLO-2	BluetoothModes	Propagation characteristics	Super regenerative Receiver –Block diagram	Models	Analysis of UWB Technologies for UROOF- Comparing UWB Technologies for Radio-over- fibre
S-6	SLO-1	Zigbee Architecture, Frame Structure	Modulation types	Super regenerative Receiver – Operation	MSK-Based System for LOS Gb/s Communications	MB-OFDM Over Multimode Fibre
3-0	SLO-2	Applications and conflicts	Modulation for digital event communication			All-optical Generation of Ultra-wideband Impulse Radio
S-7	SLO-1	Ultra-wideband Technology-Bit Sequence detection	Continuous Digital Communication	Super heterodyne Receiver- Operation	-	Operation Principles and Theoretical Approach
5-1	SLO-2	UWB Block Diagram	Advanced Digital Modulation		System architecture for an OFDM-based system to operate in a NLOS channel.	VLC Link –Transmitter
	SLO-1	Wireless Modules-Japan,UK,USA	Spread Spectrum-DHSS	Direct Conversion Receiver- Operation	System Design Aspects-Channel Plan	The VLC Channel
S-8	SLO-2	Wireless Modules-Austria, Honeywell, Norway	Spread Spectrum-FHSS		60 GHz Channel Characteristics, Baseband Modulation: OFDM versus Single Carrier	Receiver, Modulation
	SLO-1	FCC Regulations-Terms and definitions	RFID-transceiver	Software radio operation	60 GHz Analog Front-End Architectures	Potential Applications
S-9	SLO-2	Nomenclature for defining Emission, modulation and transmission	Design issues for RFID	Repeaters	Multiple Antenna Technologies	Challenges

Learning Resources	
 Alan Bensky, "Short range Wireless Communications-Fundamentals of RF system design and Applications",	 Rolf Kraemer and Marcos Katz, "Short-range wireless communications emerging technologies and
Elsevier Inc, 2004 Antti V. Raisanen, Arto Lehto, "Radio engineering for wireless communication and sensor applications",	applications", Wiley WWRF series, March 2009 Shlomi Amon, John Barry, George Karagiannidis, Robert Schober, Murat Uysal, "Advanced Optical Wireless
Artech House, 2003	Communication Systems", Cambridge University Press, 2012

-	Diami			Contir	nuous Learning Ass	essment (50% weigl	htage)			Einel Europia dia	(FO 0(
	Bloom's	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#	Final Examination	n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100) %	100) %	100) %	100) %	100) %

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Course	Code 18EC	0102J Cou	rse Name	Electronic Circuit	ts and Systems						Course O Open Elective		2	T		P 2	C 3							
Pre-rec	quisite Courses		Nil	Co-requisite Courses		Ni	1					Progi Cou	ressiv Irses	re					N	ïl				
Course	Offering Departm	ent	Electronics and Communic	ation Engineering	Data Book	/ Codes/	Stand	ards																
Course	Learning Rationa		he purpose of learning this co										Prog	ıram L	earni	ing Ou	utcom	es (Pl	L0)					
CLR-1 :	Provide a basis fo operation	or understanding s	emiconductor material, how a	a pn junction is formed and its	principle of	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Describe the basi as a switch and a	n amplifier		nsistors BJTs and FETs, and c																		ement	ut	ų
CLR-3 : CLR-4 :	Describe and ana application.	alyze the basic ope	eration of sinusoidal oscillator	tics and fundamentally importa s and use a 555 Timer in an o	scillator	(Bloom)	cy (%)	nt (%)		edge		nent	Research	0		Sustainability		Nork		nce		PSO-1: Professional Achievement	Project Management	– 3: Analyze & Research
CLR-5 :	telephones			tworking, radio transmission a	nd mobile	Thinking (E	Expected Proficiency (%)	Attainment (%)		Engineering Knowledge	Problem Analysis	Development	Design, F	Modem Tool Usage	Culture	nt & Sus		k Team Work	ation	^p roject Mgt. & Finance	ife Long Learning	ofession	^p roject M	vnalyze {
CLR-6 :	Encourage the lea	amer to assemble	and test real circuits in the la	boratory		ď	ected I	Expected /		ineerin	blem A	Design & [Analysis, E	dem To	Society & (Environment &	cs	ndividual &	Communication	ject Mg	Long L	0-1: Pr	- 2: nique	0 – 3: <i>I</i>
Course	Learning Outcom		t the end of this course, learn			Level	Ĕ	Exp		Eng	Pro	Des	Ana	Moc	Soc	Бл	Ethics	Indi	Cor	Pro	Life	PSG	PSO Tech	DSO
CLO-1 :	its important appl	ications		fications of semiconductor dio		I	80	70		L	L	-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-2 :	Review the transi in amplification ar		construction, operation, chara	cteristics and parameters, as	well as its applicat	ion 1	80	70		L	L	-	-	-	i.	-	-	-	-	-	-	-	-	-
CLO-3 :	observe the frequ	ency response of	p-amp analyze the parameter operational-amplifier.			1	80	70		L	L	-	-	-	-	-	-	-	-	-	-	-	-	-
CLO-4 :	Understand & der	monstrate different	t applications based on opera	ational-amplifier and special lin	ear ICs	1	80	70		L	Ĺ	-	-	-	-	-	-	-	1	-	_	-	-	-
CLO-5 :	Understand the b	asic concepts and	techniques of telecommunic	ation systems and networks		1	80	70		L	L	-	-	-	-	-	-	-	-	-		-	-	-
CLO-6 :	O-6 : Understand how circuit behavior can be studied with a computer, using a circuit simulation software				2	90	80		-	-	Н	-	Н	-	-	-	-	L	•	М	L	-	-	

		Learning Unit / Module 1 (12)	Learning Unit / Module 2 (12) (12) Learning Unit / Module 3 (12) (12) (12)		•	Learning Unit / Module 5 (12)
Duratio	n (hour)	Active Discrete Components & Circuits – I	Active Discrete Components & Circuits - II	Linear Integrated Circuits	Oscillators and Timers	Telecommunications
S-1	SLO-1	Conduction in semiconductors	JFETs: Structure & Operation	Introduction to Op-amp	RC Phase-Shift oscillator Operation	Analog & Digital Communication: Stages in telecommunication systems
	SLO-2	Conduction in diodes	Characteristics & Parameters	Basic op-amp and its characteristics	& Design	Carriers and Modulation
S-2	SLO-1	Basic operation of PN junction diode	JFET Biasing (Voltage-Divider Biasing)	op-amp modes	Wein bridge Oscillator operation	Carriers and Modulation
3-2	SLO-2	VI Characteristics of diode	CS-JFET Amplifier operation	parameters	& Design	Pulse Modulation
S-3	SLO-1 SLO-2	Lab-1: VI Characteristics of PN	Lab-4: Design & Analysis of CE BJT	Lab-7: Negative Feedback op-amp	Lab-10: Analysis & Design of RC	Lab-13: Demonstration of AM & FM
S-4	SLO-1 SLO-2	Junction Diode	Amplifier	circuits	Oscillators	Lap-15: Demonstration of AM & PM
S-5	SLO-1	Applications of diode: HWR & FWR	MUSEETS' STRUCTURE	Op-amp circuits: Scale changer, adder, subtractor	LC oscillators operation: Hartley Oscillator	Pulse Modulation
3-3	SLO-2	Clippers & Clampers	Operation	HWR & FWR		Digital Transmission, Frequency Division MultiplexingTime Division Multiplexing

S-6	SLO-1	Basic operation of Zener diode and its VI characteristics	Characteristics	Clipper &Clamper	555 Timer IC: Basic Operation	Networks: RS-232, circuit switching
	SLO-2	Zener diode as a voltage regulator	Parameters	Log & Antilog amplifiers	Astable Operation	Message switching, TCP/IP
S-7	SLO-1 SLO-2	Lab-2: VI Characteristics of Zener	Lab-5: Design & Analysis of CS-JFET		Lab-11: 555 Timer Operation &	Lab-14: Demonstration of Pulse
S-8	SLO-1 SLO-2	Diode	Amplifier	Lab-8: Op-amp Circuits-I	Applications	Modulation
S-9	SLO-1	BJTs: Structure & Operation	MOSFET as an amplifier	Instrumentation amplifier	Monostable Operation	Radio Transmission: Electromagnetic Spectrum, ground waves, sky waves
	SLO-2	Characteristics & Parameters	MOSFET as a switch	Comparator	Applications of 555 Timer	antennas, directional transmissions,
S-10	SLO-1	CE BJT amplifier operation	MOSFET Biasing (Voltage-Divider Biasing)	Comparator applications	Applications of 555 Timer	Transmitters, Receivers
	SLO-2	Differential amplifier operation	CS-MOSFET amplifier operation	Schmitt trigger	Voltage-Controlled Oscillators	Mobile telephones
S-11	SLO-1					
	SLO-2	Lab-3: Applications of PN Junction	Lab-6: Design & Analysis of CS-	Lab-9: Op-amp Circuits-II	Lab-12: VCO Operation	Mini Project / Model Practical
S-12	SLO-1 SLO-2	diode and Zener diode	MOSFET Amplifier			Examination

	1.	•	Owen Bishop, "Electronic Circuits and Systems", 4th edition, Elsevier, 2011.
Learning Resources	2		Harry Kybett, Earl Boysen, "All New Electronics", 3rd edition, Wiley, 2008.

3. Paul Scherz, "Practical Electronics for Inventors", McGraw-Hill, 2000.

Learning As	sessment											
	Bloom's Continuous Learning Assessment (50% weightage)											
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#	Final Examination (50% weightag		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100) %	100	0 %	100) %	100) %		-	

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. Manikandan AVM, SRM IST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. Rajesh Agarwal, SRM IST

Course Code	1	8ECO103T	Course Name	•	Modern Wire	less Comr	nunication System	-	ourse tego		0 <i>Open Elective</i> L T 3 0		T 0	P 0	C 3										
Pre-requisite Nil Co-requisite Nil Courses Nil Courses Nil							Nil		Progressive Nil																
Course O	ffering [Department		Electronics a	and Communication Engin	eering	Data Book / Codes/Standards									Ν	il								
Course Le					earning this course is to:				Learn						Р	rograr	n Lea	rning	Outco	mes (PLO)				
CLR-1 :					wireless communication sy			1	2	3		1	2	3	4	5 6	6 7	8	9	10	11	12	13	14	15
CLR-2 :	Underst	tand the fundan	nentals of v	/arious netw	orks in wireless communic	cation		6										~						ent	÷
CLR-3 :	Underst	tand the techniq	jues involv	ed in persor	nal communication service	S.			6	· %					arch		1							em	Research
CLR-4 :	Introduc	ce various wirel	ess systen	ns for 3G an	d future communication			(Bloom)		Attainment (%)		dge		ent	Research		C. intrinchility		Team Work		ce		_	anag	Re
CLR-5 :	Learn to	o analyze wirele	ess networi	ks for short i	range communication			Thinking	cie :	E		wle	6	ŭ	Å	age		ŝ	2		Finance	p	ona	Na I	e Se
CLR-6 :	Underst	tand the Fundar	mentals, T	echniques a	nd Networks of Wireless (Communicat	ion Systems	i, ki	Lof	ttai		Хnо	lysi	velo	sign	NS I		5	ear	u	& Fi	arnir	essi	ject	zyle
				•				L P	ted P	ted ⊿		ering	n Ana	& De	s, Design, I	Tool			lal & T	Inicat		ng Le;	: Professional ement	2: Project Management tues	3: Analyze
Course Le	earning	Outcomes (CL	O): At th	e end of this	s course, learners will be a	ble to:		ן פעם <u>ו</u>	Expected Proficiency (%)			Engineering Knowledge	Problem Analysis	Design & Development	Analysis,	Modem Tool Usage	Society -	Ethics	Individual &	Communication	Project Mgt.	Life Long Learning	PSO-1 Achieve	PSO – 2: Pr Techniques	- OSA
CLO-1 :	CLO-1: Discuss the fundamentals of transmission in wireless systems					2,	8 80	75		-	-	-	Н			-	-	-	-	-	Н				
CLO-2: Provide an overview of various approaches to communication networks				2,	8 80	85		-	-	-	Н			-	-	-	-	-	-	-	Н				
CLO-3: Study the numerous different-generation technologies with their individual pros and cons				2,	8 85	85	1 [-	-	-	Н			-	-	-	-	-	М	-	Н				
CLO-4: Discuss about the principles of operation of the different access technologies like FDMA, TDMA, SDMA and CDMA at their pros and cons.		A and 2,	8 85	80		-	-	-	Н			-	-	-	-	-	М	-	Н						
CLO-5 :	CLO-5: Learn about the various mobile data services and short range networks.				2,	8 85	80	1	-	-	-	-			-	-	-	-	-	-	-	Н			
CLO-6 :			2,			1 F	-	-	-	-			-	-	-	-	-	Н	-	-					

	ration	Transmission Fundamentals	Network Concepts	Personal Communication Services	3G and Beyond	Mobile Data Services and Short- Range Network
(1)	iour)	9	9	9	9	9
S-1	SLO-1	Cellphone Generations Communication Networks Personal communication Introduction, HSCSD, GPRS, D-AMPS, CDMA One, CDMA Two, Packet Data Systems		3G Introduction	Mobile Data Services Introduction Messaging, wireless web, WAP, site design Short-Range Wireless Networks: Unlicensed spectrum, WLANs, cordless telephony, IrDA, Bluetooth Smart Phones: Future phones, mobile OSs, smart phone applications.	
	SLO-2	1G and 2G	LANs	GSM	IMT-2000 Introduction	Data Services
	SLO-1	2.5G	MANs	GSM	IMT-2000	Messaging
S-2	SLO-2	3G	WANs	HSCSD	IMT-2000	Wireless web
• •	SLO-1	4G Transmission Introduction	Circuit switching	HSCSD	W-CDMA Introduction	WAP
S-3	SLO-2	4G Transmission Fundamentals	Packet switching	GPRS	W-CDMA	Site design

S-4	SLO-1	Time domain concepts	ATM Cellular Networks Introduction	GPRS	CDMA 2000 Introduction	Short-Range Wireless Networks
3-4	SLO-2	Frequency domain concepts	Cells	D-AMPS	EDGE	Unlicensed spectrum
S 5-6	SLO-1 SLO-2	Radio Media	Duplexing	D-AMPS	EDGE	WLANs
S-7	SLO-1	Analog Vs Digital	Multiplexing	CDMA Introduction	Wi-Fi Introduction	Cordless telephony
3-1	SLO-2	Channel capacity	Voice coding	CDMA One	Wi-Fi	IrDA
S-8	SLO-1	Transmission media	Multiple Access Techniques: FDMA	CDMA One	WiMAX Introduction	Bluetooth Smart Phones
3-0	SLO-2	Signaling Schemes	TDMA, SDMA	CDMA Two	WiMAX	Future phones
S-9	SLO-1	Carrier-based signaling,	CDMA	CDMA Two	OFDM	Mobile OSs
3-9	SLO-2	Spread-spectrum signaling	Spectral efficiency	Packet Data Systems	МІМО	Smart phone applications

 Simon Haykin, David Koilpillai, Michael Moher," Modern Wireless Communication", 1/e, Pearson Education, 2011
 Rappaport T.S, "Wireless Communications: Principles and Practice", 2nd edition, Pearson

Learning Resources

Andrea Goldsmith, "Wireless Communications", Cambridge University Press, Aug. 2005.
 Andry Doman, "The essential guide to wireless communications applications: from cellular

systems to Wi-Fi", 2nd Edition, Prentice Hall, 2002

 Ian F.Akyildiz, David M. Gutierrez Estevez, and Elias Chavarria Reyes, "The evolution of 4G cellular systems: LTE advanced", Physical communication, Volume 3, No. 4, pp. 217-298, Dec. 2010

- 6. William Stallings, "Wireless Communication & Networking", Pearson Education Asia, 2004
- 7. Andrea .F.Molisch, "Wireless communications", 2nd edition, Wiley Publications.

Learning Ass	earning Assessment											
	Bloom's Continuous Learning Assessment (50% weightage)											
	Level of Thinking	$(1 \Delta = 1/10)$		CLA –	CLA – 2 (15%)		3 (15%)	CLA – 4	l (10%)#	Final Examination	n (50% weightage)	
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	100) %	100) %	100	0 %	10) %	10	0 %	

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Course code Necconde and speech signal Processing Category Category 2 0 2 3	Course Code	18ECO104J	Course Name	Audio and Speech Signal Processing	Course	0	Open Elective	L	Т	Ρ	С
	Course Coue				Category	0	Open Elective	2	0	2	3

Pre-requisite Courses	Nil	Co-requisite Courses		Nil	Progressive Courses	Nil	
Course Offering Department	Electronics a	and Communication Enginee	ering	Data Book / Codes/Standards		Nil	

Course Lear	ning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng					Pro	gram	Learn	ing Ou	utcom	es (PL	0)			-	
CLR-1 :	To explore about Speech signal processing	1	2	3	1	2	З	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	To explore about the human auditory system		cy	ŧ	Ð								×					es	
CLR-3 :	Feature Extraction of Speech signal using Time characteristics	-	enc	nent	edge		nent						Work		nce		nal	t chnique:	
CLR-4 :	Frequency characteristics of Speech signal	king	roficienc	ttainme	Iwo	SIS.	nqole	É	ool Usage	e			Team	-	Finance	eaming	ssic	ech ech	ze
CLR-5 :	Provide a foundation for developing applications in this field.	hin	L .	Att	g Kno	Analys	é	esign,	ίΩ	Cultur	ty &			atior	~~	earr	Profes: ent	nt T	naly
CLR-6 :	Understand the concept of speech processing both in time and frequency domain	el of T	ected	ected	erin	em Ar	sign & D	/sis, D arch	H 1	ъ	onment a	s	ndividual &	ommunication	ct Mgt.	ong L	– 1: P sveme	– 2: P igeme	– 3: A earch
Course Lear	ning Outcomes (CLO): At the end of this course, learners will be able to:	(Blo	EXp(%)	°€EXD	Engine	Problem.	Desi	Analy Rese	Modern	Society	Envirol Sustair	Ethics	Indivi	Com	Project	Life L	PSO Achie	PSO	PSO &Ree
CLO-1 :	Understand the functioning of the human vocal and auditory systems in terms of signal processing	1	90	68	Н		Η		Н				-	-	-	-	М	Н	
CLO-2 :	Analyze the function of feature extraction in speech and audio signal processing using Time Domain Characteristics	2	85	67	Н			Н				М	-	-	-	-	М		Н
CLO-3 :	Understand the frequency characteristics of speech signal	2	85	68	Н		Η			М		Μ	-	-	-	-		Н	Н
CLO-4 :	Understand the Digital models for speech signal	1&2	85	65	Н		Н	Н					-	-	-	-	Н	М	
CLO-5 :	Understand the elements of music	2&3	85	66			Н		Н			Н	-	-	-	-	Н		Н
CLO-6:	Understand Speech signal processing in time and frequency domain and their models.	1,2,3	85	68	Н		Н			М		Н	-	-	-	-		М	М

Duratio	n (hour)	Learning Unit / Module 1 Basic Audio Processing	Learning Unit / Module 2 Human auditory system	Learning Unit / Module 3 Speech Signal Analysis in Time Domain	Learning Unit / Module 4 Speech Signal Analysis in Frequency Domain	Learning Unit / Module 5 Speech and Audio processing applications
		12	12	12	12	12
S-1	SLO-1	Introduction to Digital audio	Human auditory system	Speech signal analysis	Short Time Fourier analysis	Introduction to Speech recognition
3-1	SLO-2	Capturing and converting sound	Human auditory system	Speech signal analysis	Short Time Fourier analysis	Introduction to Speech recognition
S-2	SLO-1	Sampling of sound wave	simplified model of cochlea	Segmental, sub-segmental levels	Filter bank analysis	Complete system for an isolated word recognition with vector quantization /DTW
3-2	SLO-2	Handling audio in MATLAB	simplified model of cochlea	Suprasegmental levels	Formant extraction and Pitch extraction	Complete system for an isolated word recognition with vector quantization /DTW
S-3	SLO-1 SLO-2	Lab 1: Read & write a speech signal, Record a speech signal, playback,	Lab 4: Short-term energy of a speech	Lab 7: Estimation of pitch period using	Lab 10: Phoneme-level segmentation	Lab 13: Compute pitch period and
S-4	SLO-1		signal	simplified inverse filter tracking (SIFT) algorithm	of speech	fundamental frequency for speech signal
S-5	SLO-1	Normalization	Sound pressure level and loudness	Time domain parameters of speech signal	Homomorphic speech analysis	Complete system for speaker identification, verification
	SLO-2	Audio processing	Sound pressure level and loudness	Time domain parameters of speech signal	Cepstral analysis of Speech	Introduction to speech enhancement
S-6	SLO-1	Segmentation	Sound intensity and Decibel sound levels	Methods for extracting the parameters Energy	Formant and Pitch Estimation	Introduction to speech enhancement

	SLO-2	Analysis of window sizing	Sound intensity and Decibel sound levels	Average ,Magnitude	Linear Predictive analysis of speech	Speech enhancement using spectral subtraction method
S-7	SLO-1					
5-7	SLO-2	Lab 2: Convert into a wave file, plot the	Lab 5: Short-time Fourier transform	Lab 8: Estimation of pitch period using	Lab 11:To study the quantization and	Lab 14: Short term speech analysis
S-8	SLO-1	speech signal, and spectrogram plot	magnitude spectrum	harmonic product spectrum	aliasing effect of speech signal	Lab 14. Short term speech analysis
3-0	SLO-2					
S-9	SLO-1	Visualization	Concept of critical band	Zero crossing Rate	Autocorrelation method, Covariance method	Introduction to Text to speech conversion
3-9	SLO-2	Sound generation	Uniform filter bank , Non- uniform filter bank	Silence Discrimination using ZCR and energy	Solution of LPC equations	Introduction to Musical instrument classification
S-10	SLO-1	Speech production mechanism, Charistics of speech	Mel scale and bark scale,	Short Time Auto Correlation Function	Durbin's Recursive algorithm, Application of LPC parameters	Musical Information retrieval.
3-10	SLO-2	Understanding of speech	Speech perception: vowel perception	Pitch period estimation using Auto Correlation Function	Pitch detection using LPC parameters, Formant analysis	Sample Programs
S-11	SLO-1					
3-11	SLO-2	Lab 3:Cepstrum smoothed magnitude	Lab 6: (i)Linear prediction magnitude spectrum, (ii) (ii) Estimation of formant		Lab 12:: Speech signal to symbol	Lab 15: Study of Praat
S-12	SLO-1	spectrum		overlap and add (TD-PSOLA) method	transformation using wavesurfer	Lab 15. Sludy of Fraat
3-12	SLO-2		- ,	· · · · · · · · · · · · · · · · · · ·		

Learning	1.	lan McLaughlin, "Applied Speech and Audio processing, with MATLAB examples", 1 st Edition, Cambridge University Press, 2009	3.	Rabiner,B.H.Juang, "Fundamentals of Speech Recognition", 2 nd Edition, Prentice-hall Signal Processing Series, April 1993	
Resources		Ben Gold, Nelson Morgan, Dan Ellis, Wiley, "Speech and Audio Signal Processing: Processing and Perception of Speech and Music", 2nd Edition, John Wiley & Sons, 01-Nov-2011.		Ken Pohlmann, "Principles of Digital Audio", 6th Edition, McGraw-Hill, 2007 A.R.Jayan, "Speech and Audio Signal Processing", ISBN : 978-81-203-5256-8, PHI Learning Pvt. Ltd, 2016.	

	Bloom'sLevel of			Contin	uous Learning Ass	essment (50% wei	ghtage)			Final Examinatio	on (50% weightage)
	Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	(10%)#	Final Examinatio	n (50% weightage)
	Ininking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level I	Understand	20%	20%	13%	15%	13%	15%	13%	10%	13%	10%
Level 2	Apply	20%	20%	20%	20%	200/	20% 20%		20%	20%	20%
Level Z	Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 2	Evaluate	10%	10%	15%	15%	150/	15%	15%	15%	15%	15%
Level 3	Create	10%	10%	10%	13%	15%	10%	10%	10%	10%	10%
	Total	Total 100 % 100 % 100 %						10	0 %		

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Course	Code	18ECO105T	Course Name		Un	iderwater Acou	ustics				Cours Catego	-	C)		Ope	en Elec	ctive		L 3		T 0	P 0		C 3
	equisite urses		Nil		Co-requisite Courses		Nil					Progr Cou	essiv Irses	e						Nil					
Course C	Offering De	epartment		Electronics and C	Communication Engine	eering	Data Book / Codes/Sta	andard	ds									Nil							
Course L	earning R	ationale (CLR):	The purpose	of learning this cou	se is to:				Learni	ing					Progr	ram L	.earnir	ng Out	tcom	ies (P	LO)				
CLR-1 :					and how it can be use				1 2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Study abo sounds.	outOcean Acousti	c Processing	and sound wave pro	pagation and analyze	e sea floor cha	racteristics and ocean																lent		
CLR-3 :	Understa analysis.	nd about Underwa	ater reverbera	tion and how types o	f noises affects the ur	nderwater aco	ustics signal data							ch			oility						em	ment	earch
CLR-4 :	Study abo	outAcoustic transc	lucers.						(Bloom)	t (%)	de	,	art	Research			Sustainability		Work		e		I Achie	Manageme	Rese
				underwater applicat				Ę	(B)	nen	wlec	s	me	, Re	Usage	Ð	Sust		∧ E		Finance	Ð	ona	Ma	ъ
CLR-6 :	Understa	nd the basic theor	y and signal µ	rocessing applicatio	n for underwater com	munication an	d navigation.	ef This line	of Thinking (Blo	ted Attainment (%)	sering Knowledge	m Analysis	א משפט Suevelopment	iis, Design,	n Tool Usa	y & Culture	nment & S		ual & Team		t Mgt. & Fi	e'	1: Professional	2: Project ques	3: Analyze

	F	-	-	<u> </u>	<	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	-	Ĕ	م	Ĕ		-	÷	ž	g	с.	- e	2
Course Learning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expecter	Expecter	Enginee	Problem	Design 8	Analysis	Modern ⁻	Society &	Environr	Ethics	Individua	Commur	Project N	Life Lonç	PSO-1:	PSO – 2 Techniqu	PSO - 3
CLO-1: Acquire in-depth knowledge and analyze on Sound Navigation and Ranging (SONAR) equations and it characteristics.	L1	85	65	М	-	-	-	-	-	-	-	-	-	-	М	L		
CLO-2 : Analyze Ocean Acoustic Processing and sound wave propagation.	L2	85	65	М	Н	Н	Н	Н	-	-	-	-	-	-	L	Н	Ηŀ	Н
CLO-3: Acquire knowledge and analyze Underwater reverberation and various types of noises.	L1&L2	85	65	М		Н	Н	Н	-	-	-	-	-	-	L	Н	MI	Н
CLO-4: Acquire knowledge on working of underwater Acoustic transducers.	L1	85	65	Н	Н	Н	Н	Н	-	-	-	-	-	-	L	Н	ΗI	Н
CL0-5: Gain knowledge and apply SONAR concepts for underwater applications.	L1& L3	85	65	L		Н	Н	-	-	-	-	-	-	-	L	Н	M	Н
CLO-6 : Understand the development and dynamics of underwater acoustic engineering	L2 &L3	85	65	-	-	-	-	-	-	-	-	-	-	-	-	-		-]

Duratio	n (hour)	Learning Unit / Module 1 Sound Navigation and Ranging (SONAR)	Learning Unit / Module 2 Ocean Acoustic Processing and sound wave propagation	Learning Unit / Module 3 Reverberation and Noises	Learning Unit / Module 4 Acoustic Transduction	Learning Unit / Module 5 SONAR Application
		9	9	9	9	9
S-1	SLO-1	Introduction to SONAR equation,	Processing ocean sound-Sampling rules	Reverberation-Scattering, back scattering strength and target strength	Piezoelectric transducer-Introduction	Echo sounder
0-1	SLO-2	Source Intensity, Source Directivity	Spatial sampling and Temporal sampling	Surface and bottom scattering	Piezoelectric transducer-33-Mode Iongitudinal vibrator	Echo Sounder
	SLO-1	Transmission loss	Filter operations-Finite Fourier transformation	Volume scattering, bottom scattering, reverberation target strength	Piezoelectric transducer-33-Mode Iongitudinal vibrator	Sub-bottom profiling
S-2	SLO-2	Transmission loss	Filter operations-Time domain view of Band pass filtering. convolution operations, frequency domain	Calculation of reverberation for use in the sonar equation, Volume reverberation level	Electrostrictive transducers	Fishing sonars
S-3	SLO-1	Target Strength	Gated Signals-Dependence of Spectrum on ping carrier periodicity	Reverberation frequency spread and Doppler gain potential-Power spectral density of a CW pulse	Electrostrictive transducers	Side scan terrain mapping sonar
	SLO-2	Reflection Intensity Loss Coefficient	Power spectra of random signal-Signal having random characteristics, Spectral density,	Environmental frequency sampling	Magnetostrictive transducers	Side scan terrain mapping sonar
S-4	SLO-1	Sea-floor Loss,	Radom signal simulations-Intensity spectral density, Spectral smoothing	Frequency spreading due to transmitter and receiver motion	Magnetostrictive transducers	Acoustic positioning and navigation
	SLO-2	Sea-surface Loss	Matched filters and autocorrelation	Frequency spreading due to target,	Electostatic Transducers	Acoustic positioning and navigation

				important observation with respect to reverberation		
	SLO-1	Noise, Reverberation	Sounds in the oceans-natural physical sounds and biological sounds	Noise-Ambient noise models	Electostatic Transducers	3D Imaging Processing-data model
S-5	SLO-2		Sound propagation in the ocean and underwater acoustic channel-Sound wave and vibration, velocity of sound	Ambient noise-seismic noise, ocean turbulence, shipping noise	Variable Reluctance Transducers	3D Imaging Processing-acquisition of 3D information
S-6	SLO-1		Sound propagation in the ocean and underwater acoustic channel-Sound wave velocity of sound	Wave noise, thermal noise	Variable Reluctance Transducers	3D Imaging Processing-matrix approach and real time systems
3-0	SLO-2		Wave and ray theories of underwater sound fields	Rain noise, temporal variability of ambient noise, depth effects of noise	Moving coil transducers	3D Imaging Processing-Image representation, Acoustic image processing
S-7	SLO-1		Wave and ray theories of underwater sound fields	Under ice noise	Moving coil transducers	3D Imaging Processing-Segmentation and reconstruction of underwater tubular structures
5-1	SLO-2		Wave and ray theories of underwater sound fields	Spatial coherence of ambient noise	Equivalent circuits-Basics Circuit Resonance	3D Imaging Processing-Segmentation and reconstruction of underwater tubular structures
S-8	SLO-1	Active Sonar Sources- Source Level, Cavitation	Sound absorption in sea water and its characteristics	Self-noise-Flow noise	Circuit Q and Bandwidth	Acoustic communication-Cross attributes of the received signal
3-0	SLO-2	Near-Field Interactions Explosive Sources	Upper boundary of acoustic channel	Self-noise – Flow noise	Transducers as projectors-principle	Acoustic communication-channel transfer function
S-9	SLO-1		Lower boundary of acoustic channel and its characteristics	Self noise-turbulent noise coherence	Transducers as Hydrophones- principles of operations	Acoustic communication-combating multipath
3-9	SLO-2	Pros and Cons of Explosive Charges, Parametric Acoustic Sources	sound field in shallow water	Self noise-strumming noise	Transducers as Hydrophones- simplified equivalent circuit	Acoustic communication-diversity reception, equalization

	1.	Richard P HODGES, "Underwater Acoustics – Analysis, Design and Performance of SONAR",	4.	Charles H Sherman, John L Butler, "Transducers and Arrays for Underwater Sound", Springer; 2nd
		Wiley 1 edition2010, ISBN 978-0-470-68875-		edition, 2016, ISBN-10: 0-387-32940-4 ISBN-13: 978-0387-32940-6
Learning	2.	Rodney F W Coates, "Underwater Acoustics Systems", Macmillan New Electronics, Wiley, 1stedition	5.	Qihu Li, "Digital Sonar Design in underwater acoustics: Principles and applications", Springer,
Resources		, 1990, ISBN 978-0-333-42542-8		Zhejang University Press, 2012
	3.	Robert S H Istepanian and MilicaStojanovic, "Underwater Acoustic Digital Signal Processing and	6.	Herman Medwin, Clarence S.Clay, "Fundamentals of Acoustical Oceanography", Academic Press,
		Communication Systems", Springer, 2002 edition, ISBN 978-1-4419-4882-3		1998.

	Discusio		Continuous Learning Assessment (50% weightage)													
	Bloom's	CLA –	1 (10%)	CLA –	2 (15%)	CLA – S	3 (15%)	CLA – 4	(10%)#	Final Examination	n (50% weightage)					
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice					
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-					
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-					
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-					
	Total	10	0 %	10	0 %	100) %	100	0 %	10	100 %					

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. S. Dhanalakshmi, SRMIST

2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	
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Course Code	18EC0	D106J	Course Name		PCB Design ar	nd Man	ufacturing							Course O Category			Open Elective					L 2	T 0	P 2	<u>С</u> 3
	Pre-requisite Courses Nil Progressive Courses Nil Pre-requisite Courses Nil Progressive Nil																								
Course Offering Department Electronics and Communication Engineering Data Book / Codes/ Standards																									
Course Learning Rationale (CLR): The purpose of learning this course is to: Learning Program Learning Outcomes (PLO)																									
CLR-1 :				and Electronic co			1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 : CLR-3 : CLR-4 : CLR-5 : CLR-6 :	CLR-2 : Understand the design and other consideration involved in PCB design CLR-3 : Understand the PCB design consideration for special application circuits CLR-4 : Design a PCB layout using CAD tool CLR-5 : Explore various PCB manufacturing techniques							xpected Proficiency (%)	pected Attainment (%)		Engineering Knowledge	roblem Analysis	Design & Development	Analysis, Design, Research	Aodem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	ndividual & Team Work	ommunication	roject Mgt. & Finance	: Long Learning	O–1: Professional nievement	0 – 2: Project nagement Techniques	PSO – 3: Analyze & Research
Course Learn					arners will be able to:		Le	<u>لَّةُ</u> 80	<u>ă</u>			Pro	Å	Re	₽	So	Sü	튶	Ы	රි	Pr	Life	PSC	R PS	Re PS
CLO-1 :									70	4	H			L											
CLO-2: Select suitable design and consider appropriate parameters involved in PCB design								80	70	4	M		L											<u> </u>	
CLO-3 :								80	70	4	M			L]	I	
CLO-4 :	U	1	, ,				1,2, 3	80	70	4	M			М	Н]	I	
CLO-5 :	, , , , , , , , , , , , , , , , , , , ,							80	70	4	L				Н]	I	
CLO-6 :								1	1													ļ		I	1

		Learning Unit / Module 1	Learning Unit / Module 2	Learning Unit / Module 3	Learning Unit / Module 4	Learning Unit / Module 5		
Duration	n (hour)	12	12	12	12	12		
S-1	SLO-1	Nomenclature of a Printed Circuit Board	PCB Design Considerations - Important Design Elements	Design Rules for Analog Circuits	Schematic Capture - Introduction	Image Transfer Techniques- Screen		
5-1	SLO-2	Classification of Printed Circuit Boards	PCB Design Considerations - Important Performance Parameters	Design Nules for Analog Circuits	schematic capture tool	Printing, Pattern Transferring Techniques		
			PCB Design Considerations - Mechanical Design Considerations		Schematic Capture - Simulation of simple electronic circuit	Image Transfer Techniques- Printing Inks, Photo Printing, Laser Direct Imaging (LDI)		
S-2	SLO-2	Manufacturing of Multi-layer Boards - Flexible Boards, Challenges in modern PCB Design and Manufacture, PCB Standards	PCB Design Considerations - Mechanical Design Considerations	Design Rules for Digital Circuits		Copper Clad Laminates - Properties of Laminates, Types of Laminates, Evaluation of Laminates		
S-3	SLO-1 SLO-2	SHOV OF EIECHONIC COMPONENTS- PASSIVE	Design and analysis of RL and RC time	Schematic and PCB Layout in CAD tool. Regulated power supply design Full		Mini Project - PCB Layout Design of		
S-4		electronic components	constants. Schematic in CAD tool	wave rectifier circuit design with fixed voltage regulator	nulse counter using PCB design tool	electronic turn ON/OFF timer using IC555 using PCB design tool.		
S-5	SL0-1	•	PCB Design Considerations - Electrical Design Considerations	Design Rules for High Frequency Circuits		Etching Techniques – wet Etching chemicals		
		Diodes, Light Emitting Diodes (LED), Photodiode,	PCB Design Considerations - Conductor Patterns, Component Placement Rules	Design Rules for Fast Pulse Circuits	PCB Layout Design - Specifying Parts, Packages and Pin Names, Libraries	Etching Techniques - Mechanical Etching		
S-6		Transistors, Field-effect Transistors, Insulated Gate Bipolar Transistor (IGBT),	Fabrication and Assembly Considerations	Design Rules for Microwave Circuits	PCB Layout Design - Checking foot prints of the components, Part list, Net list,	PCB Assembly Process - Through-hole		

		Thyristor			Making Net list Files			
S-7		Study of electronic components- active		Schematic and PCB Layout in CAD tool.		Mini Project - Manufacture the PCB for		
		devices, analog and digital integrated		Regulated power supply designFull	counter: Schematic and PCB layout	electronic turn ON/OFF timer using		
S-8	SLO-1	circuits (IC)	Schematic in CAD tool	wave rectifier circuit design with fixed	using PCB design tool.	IC555and construct and test the		
	SLO-2			voltage regulator	using FCB design tool.	designed circuit.		
S-9	SLO-1	Digital Integrated Circuits, Random	Environmental Factors, Cooling	Design Rules for High-density	PCB Layout Design - Mounting Holes,	PCB Assembly Process - Surface Mount,		
3-9	310-1	Access Memory	Requirements					
	SLO-2	Read Only Memory	Packaging Density	Interconnection Structures	Adding Text, PCB Layout	Mixed Technologies		
S-10	SLO-1	Microcontrollers, Surface Mount Devices		Electromagnetic Interference/Compatibility	PCB Layout Design - DRC, Pattern	DOD Assessed by Deserve and Astronomy		
	SLO-2	Transformer, Relays, Connectors	Layout Design	(EMI/EMC)	Transfer, Layout printing	PCB Assembly Process - Soldering		
S-11		Study of testing and measuring		Schematic and PCB Layout in CAD tool.		Mini Project - Manufacture the PCB for		
	SLO-2	Instruments: Logic analyzer, spectrum	PCB Layout Design - of RL, RC and RLC			electronic turn ON/OFF timer using		
S-12		analyzer, IC tester (Analog and Digital),		Full wave rectifier circuit design with	electronic turn ON/OFF timer using	5		
	SLO-2	LCP motors CITCU		fixed voltage regulator	IC555 using PCB design tool.	IC555and construct and test the designed circuit.		

		1. Raghbir Singh Khandpur, "Printed Circuit Boards: Design, Fabrication, and Assembly" McGraw-Hill Electronic Engineering, 2006.		
	2.	Charles A. Harpe, "High Performance Printed Circuit Boards", McGraw Hill Professional, 2000.	5.	Douglas Brooks "Signal Integrity Issues and Printed Circuit Board Design", Prentice Hall PTR, 2003.
earning Resources	3.	Bruce R. Archambeault, James Drewniak, "PCB Design for Real-World EMI Control", Volume 696 of The Springer International Series in Engineering and Computer Science, Springer Science & Business	6.	Mark I. Montrose "Printed Circuit Board Design Techniques for EMC Compliance : A handbook for designers" Wiley, 2 Edition, 2015.
		Media, 2013.	7. 8.	Esim open source tool : <u>http://esim.fossee.in/</u> TINA/Orcad User manual
	4.	Kraig Mitzner, "Complete PCB Design Using OrCAD Capture and PCB Editor", Newnes/Elsevier, 2009.		

Learning Assess	Learning Assessment														
	Bloom's		Continuous Learning Assessment (50% weightage)												
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – S	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)				
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice				
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%				
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%				
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%				
	Total	10	0 %	100) %	100) %	100) %		-				

Course Designers												
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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in											

Course Code	18ECO107T		ourse lame		Fiber C	optics and (Optoelectronics	-	ourse		С	Open Elective					L 3	T P 0 0	-					
Pre-requ Course			Nil		Co-requisite Courses		Nil	Progressive Nil																
Course Of	fering Departmen	1	Electro	onics and Comn	nunication Engineer	ing	Data Book / Codes/Standards									Nil								
					g this course is to:				Learn	ing			1		Progra	am Le	arni	ng O	utcon	nes (I	PLO)			
	Analyze the basic optical fibers	laws and	theore	ms of light asso	ociated with the op	ical fiber c	ommunication and the classificati	on of 1	2	3		2	3	4	5	6	7	8	9	10	11	12	13 1	4 15
CLR-3 : CLR-4 :	Explore the fundar Gain to informatior	nentals of on Optic	f optoele cal modu	ectronics display Ilators and ampl		and Detecto		Thinking (Bloom)	Proficiency (%)	nent (%)	-	afine	nent	Research	0		Sustainability		Work		nce		rofessional ent Proiect Management	& Research
					electronic circuits ar engineering proble		ntify appropriate solutions	of Thinking	ed Profici	ed Attainment	2	Problem Analysis	Design & Development	Design,	Modem Tool Usage	& Culture	∞ŏ		Team	Communication	Mgt. & Finance	g Learning	Professional ment 2: Proiect Mai	aues ³ : Analyze 8
Course Lea	arning Outcomes	(CLO):	At the e	end of this cours	se, learners will be a	able to:		evel o	Expected	Expected		Problem	Design	Analysis,	Modem	Society	Environment	Ethics	Individual &	Commu	Project Mgt. &	Life Long	PSO-1: Pro Achievemen PSO - 2: Pr	PSO - 3
CLO-1 :	Review the basic t	neorems	related t	to fiber optic co	mmunication, and a	ttain knowl	edge of types of optical fibers	2	80	70	1	I H	-	-	-	-	-	-	-	-	-	-	-	H
CLO-2 : Understand the optical signal distortion factors in optical fiber communication						2	85	75	Ι		М	-	-	-	-	-	-	-	-	-	-	- M		
					splay devices, light		d detectors	2	75	70	I		М	-	-	-	-	-	-	-	-	-	-	• L
					ators and amplifiers			2	85	80	I		М	-	-	-	-	-	-	-	-	-	-	- H
	Understand the va							2	85	75	1		М	L	-	-	-	-	-	-	-	-	-	- L
CLO-6:	CLO-6: Acquire fundamental concepts related to optical communication and optoelectronic devices						2	80	75	Ι	I M	М	L	-	-	-	-	-	-	-	-	-	- H	

	ration iour)	Learning Unit / Module 1 Introduction to Optical Fibers	Learning Unit / Module 2 Transmission Characteristics of Optical Fibers	Learning Unit / Module 3 Display Devices, Light Sources and Detection Devices	Learning Unit / Module 4 Optoelectronic Modulators and Switching Devices	Learning Unit / Module 5 Optoelectronic Integrated Circuits
		9	9	9	9	9
	SLO-1	Evolution of fiber optic system	Attenuation – Absorption, Attenuation units	Display devices – Photo luminescence	Analog and Digital Modulation	Optoelectronic integrated circuits - Introduction
S-1	SLO-2	Elements of an optical fiber transmission link	Attenuation – Scattering losses	Cathode luminescence	Electro optic modulators – Electro optic effect – Longitudinal electro optic modulator	Need for Integration - Hybrid and Monolithic Integration
S-2	SLO-1	Elements of an optical fiber transmission link	Attenuation – Bending losses, microbending and macro bending losses	Electro luminescence	Electro optic modulators – Transverse electro optic modulator	Hybrid and Monolithic Integration
3-2	SLO-2	Advantages of fiber optic system	Attenuation - Core cladding losses	Injection luminescence	Acousto optic modulators – Transmission type – Raman Nath modulator	Materials and processing of OEICs
S-3	SLO-1	Characteristics and behavior of light	Signal distortion in optical waveguides	Light source materials	Acousto optic modulators – Reflection type – Bragg modulator	Application of optoelectronic integrated circuits
5-3	SLO-2	Total internal reflection	Types of dispersion-Intramodal and Intermodal dispersion	Surface emitting LEDs	Solving Problems	Slab and Strip Waveguides
S-4	SLO-1	Acceptance angle	Material dispersion	Edge emitting LEDs	Optical switching and logic devices – self- electro-optic-device	Integrated transmitters and receivers – Front end photo receivers
3-4	SLO-2	Numerical aperture, Critical angle	Material dispersion, Waveguide dispersion	Quantum efficiency and LED power – Internal quantum efficiency derivation	Optical switching and logic devices – Bipolar controller modulator	Integrated transmitters and receivers – photoreceiver noise and bandwidth

						considerations
S-5	SLO-1	Solving Problems	Waveguide dispersion	Quantum efficiency and LED power – External quantum efficiency and total LED power	Optical switching and logic devices- tunable threshold logic gate – Switching speed and energy.	Integrated transmitters and receivers – PIN-HBT photoreceivers
3-3	SLO-2	Solving Problems	Signal distortion in single mode fibers	Solving Problems	Optical Amplifiers – General applications of optical amplifiers	Integrated transmitters and receivers – OEIC transmitters – equivalent circuit for integrated receivers
S-6	SLO-1	Ray optics	Polarization mode dispersion	Semiconductor laser diode	Semiconductor optical amplifiers – Basic configuration	Integrated transmitters and receivers – Complex circuits and arrays
3-0	SLO-2	Types of rays	Polarization mode dispersion, Intermodal dispersion	Modes and threshold condition	Semiconductor optical amplifiers – Optical gain - Limitations	Integrated transmitters and receivers - optical control and microwave oscillators
S-7	SLO-1	Optical fiber modes	Intermodal dispersion	Photo detection principle	Erbium doped fiber amplifiers – energy level diagram and amplification mechanism	Guided wave devices – Waveguide and couplers
3-1	SLO-2	Optical fiber configurations	Solving Problems	PIN Photodiode	Erbium doped fiber amplifiers – EDFA configuration	Guided wave devices – Active guided wave devices
S-8	SLO-1	Single mode fibers	Solving Problems	PIN photodiode - Avalanche Photodiode	Solving Problems	Guided wave devices – Mach Zehnder Interferometers
3-0	SLO-2	Multimode Fibers	Pulse Broadening in Graded Index Waveguides	Avalanche Photodiode	Solving Problems	Active couplers
c 0	SLO-1	Step Index Fibers	Mode Coupling	Noise mechanism in photodetectors	Fiber Raman Amplifiers – Configuration – Forward pumping	Active Couplers
S-9	SLO-2	Graded Index Fibers	Design Optimization of Single Mode Fibers	Solving Problems	Fiber Raman Amplifiers – Backward pumping	Active Couplers

Learning	1. Gerd Keiser, "Optical Fiber Communications", 5th Edition, McGraw Hill Education (India), 2015.
Resources	2. Khare R P, "Fiber Optics and Optoelectronics", Oxford University Press, 2014.

J. Wilson and J. Hawkes, "Optoelectronics – An Introduction", Prentice Hall, 1995.
 Pallab Bhattacharya, "Semiconductor Optoelectronic Devices", Prentice Hall of India Pvt. Ltd, 2006.

Learning Asse	ssment											
	Bloom's				Einal Examinatio	n (50% weightage)						
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		ii (50 % weigiilage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember	40 %		30 %		30 %		30 %		30%		
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %		40%		
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%		
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	al 100 % 100 % 100 %				0 %	10	0 %	100 %			

Course Designers		
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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@ici.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECO108J	Course Name	EMBEDDED SYSTEM DESIGN USING ARDUINO	Course Category	0	Open elective courses	L 2	T O	P 2	C 3
Pre-requisite Courses	• Nil		Co-requisite Courses	Progressiv Courses	ve _{Nil}					
Course Offerin	ng Department	ECE	Data Book / Codes/Standard	ls Nil						

000130			0001363						oour	363														1
Course Of	ering Department	ECE		C	ata Book / Codes/	Standard	ds	Ni	I															
Course Lea	Course Learning Rationale (CLR): The purpose of learning this course is to: Learning Program Learning Outcomes (PLO)																							
CLR-1 :	Get to know about ARD	UINO hardware details and e	nvironment			1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	To understand the core	elements of ARDUINO progra	amming langua	age		Ê	(%)	(%)		a)								k					es	
CLR-3 :	Create insights to the co	ncepts of serial communicati	on			200	ncy (°			edge		Jent						Work		nce		a	niqu	
CLR-4 :	To use common input ar	nd output devices				13 (E		me		Knowl	.s	elopment	ć	sage	e			eam /		Finance	ing	sion	ect Techi	Ze S
CLR-5 :	Apply the ARDUINO pro	gramming into real time appl	cations			Thinking (Bloom)	Profici	Attainment		Kn	nalysis	evel	Design,	l Us	Cultur	t &		⊢ –	tion	~ŏ	earning	rofession. ent	oje(Jaly
CLR-6 :										ing	<	Š Š	ې تې ش	Tool Usaç	~~	ment		al &	ommunication	Mgt.		ΔĒ	2: Pr	3: Ar
	·					elot	ected	ecte		inginee	olem	esign	lysis, earch	Modern	Society	ironm tainal	S	Individual	nmr	Project I	Long)-1: iever	Ιĝ	1 0
Course Lea	arning Outcomes (CLO):	At the end of this c	ourse, learners	will be able to	Ľ	Leve	Expe	Expected		Eng	Proble	Des	Analy Rese	Mod	Soc	Environ Sustaina	Ethics	lndi	Con	Proj	Life	PSC	PSO Mane	PSO Rese
CLO-1 :	Analyze the programmin	g skill				2	80	70		Н		-	-	-	-	-	-	-	-	-	-	-	Н	Н
CLO-2 :	Apply the real time data'	s into digital				2	85	75		Н	Н	Н	Н	Н	-	-	-	Н	-	Н	-	-	Н	Н
CLO-3 :	Interact with almost man	y devices				2	75	70		Н	-	Н	Н	Н	-	-	-	Н	-	Н	-	Н	Н	-
CLO-4 :	Learn techniques to han	dle timer delays and IO devid	es			2	85	80		Н	Н	Н	Н	Н	-	-	-	Н	-	Н	-	Н	Н	-
CLO-5 :	Use and modifying the e	xisting libraries				2	85	75		Н	-	Н	Н	Н	-	-	-	Н	-	Н	-	Н	Н	-
CLO-6 :																								

	ation our)	12	12	12	12	12
S-1	SLO-1	Introduction to arduino platform	Introduction To Arduino C	Analog And Serial Communication	IO Programming	Case Studies
3-1	SLO-2	Block diagram	Arduino C Data Types .	Introduction To Analog Communication	Introduction To Timer/Counters	Wireless Communication Using Zigbee
	SLO-1 AT mega 328p architecture		Decision Making in C	Pulse Width Modulation	Introduction To Timer/Counters	Bluetooth
S-2	SLO-2	AT mega 328p architecture	Decision Making in C	RS232	Timer programming	Robotics -Motor And Sensor
	SLO-1	Lab 1 Getting Started With Adriano	Lab 4 -Sensor Interfacing For Temperature Monitoring	Lab 7: Actuators – Stepper Motor	Lab10:Interrupt Programming	Lab 13:Mini Project
S 3-4	SLO-2	CCS And AVR Studio 7 Blinking Led	Lab 4 -Sensor Interfacing For Displacement Measurement	Lab 7: Actuators – Stepper Motor	Lab10:Interrupt Programming	Lab 13:Mini Project
	SLO-1	Pin function	Program Loops in C	12C	Timer programming	Security-RFID, Infrared
S-5	SLO-2	Overview of main features-I/O ports	Functions in C	12C	Timer programming	Security-RFID, Infrared
S-6	SLO-1 SLO-2	Features-timers,interrupts	Introduction to Pointers	12C	Timer programming	Bio medical application
0.7.0	SLO-1	Lab 2 GPIO LED	Lab 5: PWM BASED SERVO MOTOR INTERFACING	Lab 8: DC MOTOR	Lab11:Watch Dog Timer	Lab14:Model Practical
S 7-8	SLO-2	Switch Based Led Control	Lab 5: PWM Based Servo Motor Interfacing	Lab 8: DC MOTOR	Lab11:Watch Dog Timer	Lab14:Model Practical

	SLO-1	Features-PWM,SERIAL PORT	Using Pointers Effectively	SPI Protocol	Interrupts	Bio medical application
S-9	SLO-2	Features-ADC	Structures, Unions, and Data Storage	SPI Protocol	Interrupt programming	Bio medical application
C 40	SLO-1	Introduction to Arduino IDE	Arduino Libraries	Interfacing with sensors	External interrupt	GPS Navigation
S-10	310-2			Interfacing with sensors	External interrupt	GPS Navigation
644.40	SLO-1	Lab 3:DISPLAY INTERFACE-7 SEGMENT	Lab 6:SERIAL COMMUNICATION	Lab 9: Repeat/Revision Of Experiments	Lab 12 : I2C	Lab:15 University Practical
S11-12		LCD 16x2 Matrix	Lab 6:Serial Communication	Lab 9: Repeat/Revision Of Experiments	Lab 12: I2C	Lab:15 University Practical

 Learning
 1.Michael-Margolis,Arduino-Cookbook., Revised edition, O'Reilly,1st edition, 2011

 Resources
 2. D.Dale.Wheat, Arduino.Internals, TIA publication, 5th edition, 2011

3. James M. Fiore, Embedded Controllers Using C and Arduino, ARDUINO open source community, 2018 4. Jack Purdum ,Beginning C for Arduino, Apress, 2012

Learning As	sessment											
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Einal Examination	o (50%) woightago)	
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#	Final Examination (50% weightage)		
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%	
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level 3 Evaluate Create		10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Total	100) %	10	0 %	10	0 %	100) %		-	

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2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in										

Course Code	9 18E0	CO109J	Course Name		Embedded Sys	stem Desig	n using Raspberry Pi	-	ours tego	-	O Open Elective				L 2	T 0		C 3								
Pre-req Cours			Nil		Co-requisite Courses		Nil		ogres ours	sive es								Ni	I							
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards Nil																										
Course L					g this course is to:				earn	ing						Prog	ram L	earni	ng O	utcor	nes (l	PLO)				
CLR-1 :				thon for Raspbe				1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :					cing motors using Ra	aspberry F	i																	lent		
CLR-3 :				PIO switch and				Ê							_			≥						ver	ent	сł
CLR-4 :							amming, light sensor ,gas detection	(Bloom)	Proficiency (%)	Attainment (%)					Research			Sustainability		2				-1: Professional Achievement	Management	Research
CLR-5 :					data sheet of temper	ature sen	sor, gas sensor ,ADC, ultrasonic	B		e le		adge		ent	ese			taina		& Team Work		ЭС		al Ac	ana	Re
			on and light s					<u> </u>		i i		Me	s.	bm	Ľ.	age	e	Sust		۳ (inar	Ę	ion	ž	Se Se
CLR-6 :	Utilize the	technology	of node js ,cl	oud service and	d MQTT Protocol for	moving s	ensor data to web	Thinking	i i i	Atta		Knc	alys	svelo	Design,	۱Us	Culture			Tea	tion	∞	ami	fess	roject	Analyze &
								É	100	, be		ring	Ané	ď	De.	T00	s Cl	neni		al &	licat	Agt.	g Le	Prot	nes er	- An
Course L	earning Ou	itcomes (CL	O): At the e	nd of this cours	se, learners will be al	ole to:		Level of	Exnected	Expected ,		Engineering Knowledge	Problem Analysis	Design & Development	Analysis,	Modern Tool Usage	Society & I	Environment &	Ethics	Individual	Communication	Project Mgt. & Finance	Life Long Leaming		PSO – 2: Pr Techniques	PSO – 3:
CLO-1 :	Apply pyth	hon for Rasp	berry Pi					2	80			H	H	-	-	Ħ	-	-	-	-	-	-	-	Ħ	-	-
CLO-2 :	Analyze da	ata sheet and	d functioning	of sensors				2	85	5 75		Н	Н	Н	Н	Н	-	-	-	-	-	-	-	-	-	Н
CLO-3 :	Apply pythe	on programn	ning on GPIO	of Raspberry F	Pi and interfacing of a	sensor		2	75	5 70		Н	Н	Н	Н	-	-	-	-	-	-	-	-	Н	-	-
CLO-4 :	Apply pythe	on programn	ning on GPIC	of Raspberry F	Pi to interfacing of ac	tuators		2	85	5 80]	Н	Н	Н	Н	Н	-	-	-	-	-	-	-	Н	-	-
CLO-5 :	Apply pythe	on programn	ning on GPIC	of Raspberry F	Pi to interfacing input	and displa	ay device	2	85	5 75		Н	-	Н	Н	-	-	-	-	-	-	-	-	Н	-	-
CLO-6:	Apply tech	nology of no	de js ,cloud s	ervice and MQ	TT Protocol for IOT	application	1	2	80) 70	1	Н	-	Н	-	Н	-	-	-	-	-	-	-	-	-	Н

	ration iour)	Learning Unit / Module 1 Basic python programming	Learning Unit / Module 2 Programming interrupts –Motor control, switches and keyboard interface	Learning Unit / Module 3 Sensor interface and programming	Learning Unit / Module 4 Temperature sensor and display interface programming	Learning Unit / Module 5 Publishing sensor data on web service
		12	12	12	12	12
		Python Basics- Editing Python Programs with IDLE, Variables, displaying Output, Reading User Input, Arithmetic, Creating Strings	Programming with Interrupts	Detecting Movement-PIR sensor	Measuring Temperature Using a Digital Sensor	publish sensor data on web service- building a home security dash board
S-1	SLO-2	Concatenating (Joining) Strings, Converting Numbers to Strings, Converting Strings to Numbers ,Find the Length of a String, Find the Position of One String Inside Another, Extracting Part of a String, Replacing One String of Characters with Another Inside a String ,Converting a String to Upper- or Lowercase	Programming with Interrupts	Data sheet analysis of PIR sensor	Data sheet analysis Digital Temperature Sensor	publish sensor data on web service- building a home security dash board
	SLO-1	Running Commands Conditionally, Comparing Values, Logical Operators,	Controlling GPIO Outputs Using a Web Interface	Adding GPS to the Raspberry Pi	Measuring Distance-ultrasonic rangefinder	MQTT Protocol
S-2	SLO-2	Repeating Instructions an Exact Number of Times ,Repeating Instructions Until Some Condition Changes , Breaking Out of a	Controlling GPIO Outputs Using a Web Interface	Data sheet analysis of GPS	Data sheet analysis ultrasonic rangefinder	MQTT Protocol- installation and setting account ,token creation ,reading sensor data and pushing to thingsboard

		Loop, Defining a Function in Python				
	SL0-1	Lab 1: Arithmetic and string	Lab 7: Programming on interrupts	Lab 13: Programming on PIR sensor	Lab 19: Programming on Digital Temperature Sensor	Lab 25: Publish sensor data on web service
S-3-4	SLO-2	Lab 2: Loop	Lab 8: Programming on Web Interface	Lab 14: Programming on GPS	Lab 20: Programming on ultrasonic rangefinder	Lab 26: Publish sensor data on web service
S-5	SLO-1	Creating a List , Accessing Elements of a List, Find the Length of a List , Adding Elements to a List , Removing Elements from a List,	Controlling Servo Motors using PWM	Using Resistive Sensors	Logging to a USB Flash Drive	basic of java scripts –node js
3-3	SLO-2	Creating a List by Parsing a String, Iterating over a List, Enumerating a List, Sorting a List, Cutting Up a List.Applying a Function to a List	Controlling the Speed of a DC Motor	Measuring Light	Logging to a USB Flash Drive	Modules-HTML module
S-6	SLO-1	Creating a Dictionary ,Accessing a Dictionary, Removing Things from a Dictionary,	Controlling the Direction of a DC Motor	Detecting Methane	Using a Four-Digit LED Display	Modules –file –event
	SLO-2	Iterating over Dictionaries	Using a Unipolar Stepper Motor	Data sheet analysis of gas sensor	Displaying Messages on an I2C LED matrix with data sheet discussion	Modules –file –event
S-7-8	SLO-1	Lab 3: Program on list	Lab 9: Programming on Stepper Motor	Lab 15: Programming on light sensor	Lab 21: Programming on Four-Digit LED Display	Lab 27: Programming on node js HTML module
3-1-0	SLO-2	Lab 4: Program on Dictionary	Lab 10: Programming on DC Motor	Lab 16: Programming on Gas sensor	Lab 22: Programming on I2C LED matrix	Lab 28: Programming on node js file and event module
S-9	SLO-1	Controlling Hardware-Connecting an LED- Controlling the Brightness of an LED	Using a Bipolar Stepper Motor	Measuring a Voltage using MCP3008 And data sheet of MCP3008	Displaying Messages on an Alphanumeric LCD	LED blinking using node.js
2-9	SLO-2	a Buzzing Sound	Building a Simple Robot Rover	Using Resistive Sensors with an ADC	Displaying Messages on an Alphanumeric LCD	LED blinking using node.js
S-10	SLO-1	Switching a High-Power DC Device Using a Transistor	Digital Inputs-Connecting a Push Switch- Toggling with a Push Switch-Using a Two- Position Toggle or Slide Switch	Measuring Temperature with an ADC	Cloud service for IOT	building java script client using MQTT broker
	SLO-2	Switching a High-Power Device Using a Relay	Using a Rotary (Quadrature) Encoder and Using a Keypad	Measuring Acceleration and data sheet discussion of Acceleration sensor	Cloud service for IOT	building java script client using MQTT broker
S-11,	SLO-1	Lab 5: LED blinking and Brightness control	Lab 11: Programming on Switch	Lab 17: Programming on ADC	Lab 23: Programming on an Alphanumeric LCD	Lab 29: Programming on LED blinking using node.js
12	SLO-2	Lab 6: Switching a High-Power DC Device	Lab 12: Programming on Keypad	Lab 18: Programming on Measuring Acceleration	Lab 24: Programming on an Alphanumeric LCD	Lab 30: Building java script client using MQTT broker

Learning Resources Simon Monk, "Raspberry Pi Cookbook", O'Reilly Media, Inc, 2014.
 Volker Ziemann, "A Hands-On Course in Sensors Using the Arduino and Raspberry Pi, CRC Press, 2018.

Colin Dow, "Internet of Thing: Programming Projects -Build modern IoT solutions with the Raspberry Pi 3 and Python",packtpub 2018.
 https://thingsboard.io/docs/
 https://www.w3schools.com/nodejs/nodejs_raspberrypi_blinking_led.asp

Learning Asses	ssment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Einal Examination	o (50% woightago)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		r (50 % weightage)
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	6) CLA – 4 (10%)#		Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level I	Understand	2070	2070	1070	1576	1576	1576	1570	1578	1570	1570
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 2	Analyze	2070	2070	2078	2070	2070	2070	2070	2078	2070	2070
Level 3	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
Level 5	Create	1070	1070	1576	1570	1370	1570	1370	1370	1370	1570

Total	100 %	100 %	100 %	100 %	100 %
# CLA – 4 can be from any combinatio	n of these: Assignments, Seminars, Tec	h Talks, Mini-Projects, Case-Studies, Se	If-Study, MOOCs, Certifications, Conf.	Paper etc.,	

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, <u>kumaranuj.anii@gmail.com</u>	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Dr. P. Vijayakumar, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course	18ECO110J	Course	3D Printing Hardware	and Softwara	Course	F	Professional Elective	L	Т	Р	С
Code	INECOTIO	Name	50 Finning Haruware	and Software	Category	E	Professional Elective	2	0	2	3
							·				
Pre-requis	ite	Nil	Co-requisite	Nil	Progre	ssive	Nil				
Courses	6	1111	Courses	1411	Cour	ses	1411				
Course Offe	ring Department	Electronics an	d Communication Engineering	Data Book / Codes/Standards			Nil				

Course L	earning Rationale (CLR):	The purpose of learning this course is to:		earn	ning						Prog	ram L	earni	ing O	utcor	mes (I	PLO)				
CLR-1 :	Understand the tools availa	ble for 3D printing	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Familiarize with 3D design	software and hardware																	ent		
CLR-3 :	Understand the 3D design	criteria and its limitations.	í a	1		-							×.						/em	ent	5
CLR-4:	Learn the contemporary tee	hnology available for 3D design and printing	(moc		(%)					search			bilit						Achievement	Management	Research
CLR-5 :	Understand various post pr	ocessing methods involved in 3D printing technology	(Blo		ient		dge		ent	ese			aina		Work		Ge		al Ac	inaç	
CLR-6 :	Develop the skillset on 3D (available.	component design and development using contemporary commercial software and hardware	Thinking	Proficiency	Attainment		Knowle	Analysis	& Development	Design, Re	Usage	Culture	& Sustainability		Team W	ioi	Mgt. & Finance	aming	sion	Project Ma es	Analyze &
Course L	earning Outcomes (CLO):	At the end of this course, learners will be able to:	evel of T	te l	Expected		Engineering Knowledge	Problem Ar	Design & D	Analysis, D	Modern Tool Usage	Society & C	Environment&	Ethics	Individual &	Communication	Project Mgt	Life Long Leaming	PSO-1: Profess	PSO – 2: P Techniques	PSO – 3: A
CLO-1 :	Apply the 3D printing tools		1	80		1	M				M										
CLO-2 :	Able to optimistically select	the 3D design software and hardware for the given problem	1	80) 60)	М				Н							1			
CLO-3 :	Capability to solve 3D desig	n components design problems	2	75	60)	М			М										М	
CLO-4 :	Choose the contemporary	technology available for 3D design and printing	3	80) 60	1			М											М	L
CLO-5 :		ing methods involved in 3D printing technology	2	80	60)		Н								1					
CLO-6 :	Ability to develop the skillse hardware available.	t on 3D component design and development using contemporary commercial software and	2	80	60)											М			М	

	ration iour)	Introductions to 3D design tools	Three-dimensional (3D) Modeling	3D Design Fundamentals and Projects	3D Printing and its Technologies	Post Processing - Product Visualization and Print Cleaning
	SLO-1	Introduction to Maya GUI - Object creation	An overview of CAD software packages -		History of 3D printing - Overview of 3D	
S-1	SLO-2	scale and with accuracy	Introduction to Fusion 360 - Drawing based workflow, Drawing constraints - Surfacing operations.	The good, the bad, and the ugly of design	Printing technologies	Workflows for printing
S-2	SLO-1	Duplication and arrayed duplication - Grid	Moving Parts and Articulation Hinges - Ball	Prominent Designers	Selective Laser Sintering (SLS) Direct	Software and Drivers - Formats for Printing
5-2	SLO-2	and point/vertex snapping	and sockets	r iominent Designers	Metal Laser Sintering (DMLS)	(SLA, OBJ, CAD, etc.)
s		Understanding NURBS: NURBS Surfaces advantages, Similarities and differences	Maya the proper way (NURRS Curves		Vacuum forming - Resin casting - Injection	
3-4	SLO-2	between NURBS and CAD drawings Curve		Franchises Success stories, Pop culture	Molding - Terms and standards for injection molding systems	Post and Export Print Lab setup
S-5		Understanding 3D geometry - Modeling workflows for Polygons - Additive vs.	Flexibility and elasticity, Locks, bolts, and	Early decision making criteria	Fused Deposition Modeling (FDM) -	Cleanup and cirticht modaling
3-0		Subtractive Tools - Mesh editing	fasteners Threading (taps and dies)	Eany decision making chiena	Stereolithography (SLA)	Cleanup and airtight modeling
S-6	SLO-1	Best Practices for constructing printable polygon meshes	Interfacing, support, and reinforcement	Knowing the product	Laminated Object Manufacturing (LOM) -	Loading models and arranging print stage
- •	SLO-2	Fundamental Structure - Combining,		······································	Electron Beam Melting (EBM)	g sinding print orago

		merging, and sewing up polygon meshes				
S 7-8	SLO-2	polygon meshes - Fundamental Structure - Combining, merging, and sewing up	based workflows, Similarities and	design phase Group critiques of in-	Printing Resolutions and Tolerances Materials Properties (Temperature, Flexibility, Strength, Brittleness)	Printing - Removing support material
S-9	SI 0-1	Understanding two-manifold vs. non-	Form and function visualizing the assembly			Special topics – 3D Scanners and its types
3-3		Exporting geometry - Laying out a simple model on a stage for print	process	product Vision and Reality	(SLM)	Special lopics – 3D Scanners and its types
C 40	010-1	Hollow forms and the importance of reducing volume Cost of size, cost of		Calculating the total cost Progress checks	Final cleanup and processing of files for	Reverse engineering, Concepts and its
S-10	SLO-2	volume, cost of detail, cost of time State table	Complex interactions and motorizations	and group critiques of in-progress projects	printing	hardware and software
S	010 .				Planning for injection molding - 3D Printing for injection molding	High speed machining
11-12	SLO-2			progress projects	ion injouron moraling	n ngh opood maanning

Learning Resources		Hod Lipson, Melba Kurman, Fabricated: The New World of 3D Printing, Wiley, 2013 Matthew Griffin, Design and Modeling for 3D Printing, Maker Media, Inc., 2013. Rob Thompson, Manufacturing Processes for Design Professionals, Thames & Hudson; Reprint edition, 2007. <u>https://web.stanford.edu/class/me137/</u> SolidWorks Gallery: http://www.3dcontentcentral.com/default.aspx	7. 8. 9.	3D Anatomy Models: http://lifesciencedb.jp/bp3d/?lng=en AutoDesk Fusion360 HomePage: http://fusion360.autodesk.com International Journal of Rapid Manufacturing Academic Journals on 3D Printing International Journal of Rapid Manufacturing
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Learning Assess	sment										
	Dia ami'a			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examination	n (50% weightage)
	Bloom's Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		r (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level I	Understand	20%	20%	13%	13%	15%	15%	15%	10%	10%	13%
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 2	Analyze	20%	20%	20%	20%	20%	20%	20%	2076	20%	20%
Laural 2	Evaluate	100/	100/	150/	150/	150/	150/	150/	150/	150/	150/
Level 3	Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total	100	0 %	100	0 %	10	0 %	-			

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	1. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	1. Mr. S. Karuppudaiyan, Mechanical, SRMIST
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	2. Dr. P. Eswaran, SRMIST

Course Code	18ECO121T	Course Name	BASIC BIOMED	ICAL ENGINEERING		ourse tegory	,	0				C)pen E	lectiv	′e					L 3	T F 0 (Р С) 3
Pre-requ Cours	INII		Co-requisite Courses				gress ourse		Vil													
Course Of	ffering Department		s and Communication Engineering ation in Biomedical Engineering	with Data Book / Codes/Standard	s	Nil																
Course Le	earning Rationale (CL	R): The purpos	se of learning this course is to:			L	earnir	ıg					Prog	ram L	_earni	ng O	utcom	nes (F	PLO)			
CLR-1 :	Analyze the scopes an	d roles of Biome	edical Engineering			1	2	3		1 2	2 3	4	5	6	7	8	9	10	11	12	13 ′	14 15
CLR-2 :	Utilize biomedical instr	umentation mod	lules												~					;	the Ne	
CLR-3 :	Utilize medical imaging	principles and	its applications			Ê						Research			Sustainability					-	<u>c</u> ज	solu.
CLR-4 :	Analyze the scope of b					noo	(%) Y	t (%		dge	ent	see			aina		/ork		e		Solving at <u>a. & Medicii</u> & Develon	are
CLR-5 :	Utilize biomaterials and	d its applications	3			(B)	enci	nen		N O	, ū	Å	age	Ð	Sust		≤ E		inan	p o	2 % C	la plir
CLR-6 :	Gain the knowledge al	out Biomedical	Engineering			Thinking (Bloom)	ofici	ain		Y Y	& Development	sign	Us;	Culture	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~		Геа	ion	& Finance	Learning	blem (Engg. sion &	al Devices : multidisciplinary ch for health care
						T	ЧЪ ЧЪ	i Ati		ĥ.	De	B		& Cl	lent		~~	licat	1gt	- Le	of E	e let
Course Le	earning Outcomes (Cl	.O): At the end	d of this course, learners will be able	e to:		Level of .	Expected Proficiency	Expected Attainment (%)		Engineering Knowledge	Design & Develop	Analysis, Design,	Modem Tool Usage	Society 8	Environment &	Ethics	Individual & Team Work	Communication	Project Mgt.	Life Long	PSO-1: Problem interface of Engo PSO-:?: Design 8	Medical Devices PSO-3: multidisciplinary research for health care
CLO-1 :	Analyze the areas in w	hich biomedical	engineers can work			2	85	75			-	-	-	I	-	-	-	-	-	L	-	- L
CLO-2 :	Analyze the basic bion					3	85	75		L		-	-	-	-	-	-	-	-	-	-	- L
CLO-3 :	Analyze basic medical	imaging principi	les			3	85	75		М		-	-	-	-	-	-	-	-	-	-	
CLO-4 :	Apply the concepts of	biomechanics or	n human body			3	85	75		L	-	-	-	-	-	-	-	-	-	-	-	- L
CLO-5 :	Identify domains where					3	85	75			-	-	-	-	-	-	-	-	-	-	-	
CLO-6 :	Analyze the application	ns of Biomedical	l Engineer			3	85	75		М	-	-	-	-	-	-	-	-	-	L	-	- L

Du	ration	Introduction to Biomedical Engineering	Biomedical Instrumentation	Medical Imaging system	Biomechanics	Biomaterials
(h	nour)	9	9	9	9	9
S-1	SLO-1	Evolution of the modern health care system	Introduction: Bioinstrumentation	X-Ray production	Introduction: Principal Areas of Biomechanics	Biomaterials Introduction
3-1	SLO-2	Modern Healthcare system	Basic Bioinstrumentation System	X-Ray Imaging principle	Fundamentals of biomechanics and qualitative analysis	Classification of Biomaterials
S-2	SLO-1	What is Biomedical Engineering	Physiological Systems of the body	Application of X-ray imaging	Kinematics of Human Body Models	Properties of Biomaterials: Mechanical
5-2	SLO-2	Roles played by the Biomedical Engineers	Sources of Biomedical Signals	CT-Imaging principle	Kinetics of Human Body Models	Properties of Biomaterials: Chemical
S-3	SLO-1	Types of Biomedical Engineering	Origin of Bioelectric Signals	CT-Imaging Applications	Modelling of Bio systems	Properties of Biomaterials: Biological
5-3	SLO-2	Surgical instruments and medical devices	Origin of Bioelectric Signals	MRI- Introduction	Tissue Biomechanics	Biomedical alloys and its medical applications- titanium
S-4	SLO-1	Biomaterials	Various Electrodes used for picking the biomedical signals	MRI Imaging principles	Modelling in Cellular Biomechanics	Biomedical alloys and its applications- Stainless steel, Cobalt-Chromium alloys
5-4	SLO-2	Biomechanics	Various Electrodes used for picking the biomedical signals	MRI Imaging principles	Fluid mechanics	Introduction to ceramics
S-5	SLO-1	Tissue Engineering	ECG Introduction	MRI Imaging Applications	Mechanics of the musculoskeletal system impact	Alumina, Zirconia
3-5	SLO-2	Neural Engineering	ECG system Block diagram and its uses	Ultrasound basics	Mechanics of Blood Vessels	Titanium, Hydroxyapatite
S-6	SLO-1	Telehealth	EEG Introduction	Ultrasound Imaging	Cardiac Biomechanics	Glass ceramics

	SLO-2	Bio signal processing	EEG system Block diagram and its uses	Ultrasound Application	Biomechanics of Chest and Abdomen	Introduction to polymers
0.7	SLO-1	Medical Imaging	EMG Introduction	fMRI Imaging	Cochlear Mechanics	Types of polymers
S-7	SLO-2	Computational modelling	EMG system Block diagram and its uses	fMRI Imaging Application		Biodegradable polymers and its applications
S-8	SLO-1	BioMEMS	Cardiac pacemakers and its uses	PET- Imaging	Gait analysis	Composites and its applications
3-0	SLO-2	Mobile POCT	Cardiac Defibrillators and its uses	PET Imaging Application	Biomechanics in physical education	Wound-Healing process
S-9	SLO-1	Professional Status of Biomedical Engineering	Patient Monitoring System IntroPduction	SPECT Imaging	Biomechanics in strength and conditioning	Biomaterials for artificial valve, Ear
3-9	SLO-2	Protessional Societies	Patient Monitoring System Block diagram and its uses		Biomechanics in sports medicine and rehabilitation	Biomaterials for artificial Skin, Eye

Learning Resources	 Anthony Y. K. Chan, Biomedical Device Technology: Principles and Design, Charles C Thomas publisher, 2008 R.S Khandpur, Handbook of Biomedical Instrumentation, 3rd ed., McGraw Hill, 2014 Joseph J. Carr, John M.Brown, Introduction to Biomedical Equipment Technology, 4th ed., Pears 	 Jonn Enderle, Joseph Bronzino, Introduction to Biomedical Engineering, Academic Press, 2011 Andrew R Webb, Introduction to Biomedical Imaging, Wiley-IEEE Press, 2003 Suiata V, Bhat Biomaterials, 2nd ed. Alpha Science International, 2005
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Learning Ass	sessment												
	Bloom's			Einal Examinatio	n (50% weightage)								
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)		
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Lovel 1	Remember	40 %		30 %		30 %		30 %		30%			
	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-		
Level 2	Apply	40 %		40 %		40 %		40 %		40%			
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-		
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%			
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-		
	Total	Total 100 %		10	0 %	10	0 %	10	0 %	100 %			

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Sathyanarayanan J, Mindray Medical India Pvt Ltd, sathyanarayananjayagopal@mindray.com	1. Dr. S. Poonguzhali, Anna University, poongs@annauniv.edu	1. Ms. Oinam Robita Chanu, SRMIST
2. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	2. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	2.Dr. D. Kathirvelu, SRMIST
3. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@ici.com	3. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECO122T	Cou Nar		e HOSPITAL INFORMATION SYSTEMS							0	Open Elective									L 3	T F 0 C	C 3		
Pre-requi Course					o-requisite Courses	Nil				gress ourses		Nil													
Course Offering Department Electronics and Communication Engineering with specialization in Biomedical Engineering Data Book / Codes/Standards								Nil																	
Course Learning Rationale (CLR): The purpose of learning this course is to:								Le	earnir	ıg					Progr	am L	earnii	ng Oi	utcon	nes (F	PLO)				
CLR-1: Utilize the planning and organizational activities of Hospitals							1	2	3		1 2	3	4	5	6	7	8	9	10	11			4 15		
CLR-3 : U CLR-4 : U CLR-5 : A	Itilize the features in Analyze the reporting	nd proced n staff and g system a	ures ab I safety and rec	liagnostic services out support services management in hosp ent advancement in h a field of telemedicine	spital hospital adm	ř	ent		Thinking (Bloom)	roficiency (%)	Expected Attainment (%)		Engineering Knowledge	& Development	Analysis, Design, Research	ol Usage	Culture	it & Sustainability		ndividual & Team Work	ation	roject Mgt. & Finance	earning	roblem Solving at the of Engg. & Medicine Design & Develop	ipli th o
Course Learning Outcomes (CLO): At the end of this course, learners will be able to:								Level of Thi	Expected Proficiency			Engineering	Design & Develop	Analysis, D	Modem Tool Usage	Society & C	Environment &	Ethics	Individual &	Communication	Project Mgt	Life Long Learning		<u>Medical De</u> PSO-3: mul rese <u>arch fo</u>	
CLO-1: Analyze the role of hospitals and ensure proper healthcare delivery							2	85	75		L -	-	-	-	М	-	-	-	-	-	-	L			
								3	85	75		М -	-	-	-	-	-	-	-	-	-	-	L	· -	
									3	85	75		М -	-	-	-	-	М	L	-	-	-	-	Μ	- L
								3	85	75		M -	-	-	-	-	-	L	-	-	-	L	L	<u> </u>	
							3	85	75	Ļ	L -	-	-	-	М	-	L	L	-	-	-	LI	<u> </u>		
CLO-6 : Implement the various standards in hospital and healthcare services							3	85	75		L -	-	-	-	М	-	-	-	-	-	-	L	· -		

	ration	Planning and designing of hospitals	Inpatient and Outpatient services	Material management services	Management services in hospitals	Patient record and advancement in healthcare services
ų	nour)	9	9	9	9	9
S-1	SLO-1	Hospital as a social system	Design and planning of emergency department	Pharmacy services- goals of hospital pharmacy services	Human resource management- Human resource development	Medical record management- Importance of medical record
	SLO-2	Primary health care and hospitals	Health information and counselling	Staff organization and divisions of hospital pharmacy services	Hospital staff skill development	Methods of record keeping
S-2	SL0-1	Hospital planning and design-Guiding principles in planning	Outpatient services – Types and functions of outpatient department	Benefits of formulatory system	Nursing management-Functions of nursing management	Electronic medical record-Benefits and drawbacks
	SLO-2	Regionalization of Hospital service	Physical features of outpatient department	Other services of hospital pharmacy	Nursing management- organizational structure	Record retention and disposal
S-3	SLO-1	Role of health promotion approach in hospitals	Ward/Indoor services-Components of the ward system	Transport services-Types of ambulance	Biomedical waste management- Types and Composition of Biomedical Waste	Office management -skills required by the office staff
	SLO-2	Health promoting hospital system	Design of special units	Communication and physical facilities of ambulance service	Categories of biomedical waste	Functions of office management
S-4	SLO-1	Healthy hospital environment	Operation theatre services-Planning and designing of Operation theatres	Staff transport services	Concept of total quality management	Operations research in hospitals-Phases of operation research
3-4	SLO-2	Components of healthy hospital environment	Types of Operation theatres	Other transport services in hospitals	Types of approaches in quality management	Operations research in hospitals- Tools and techniques of operations research

S-5	SLO-1	Creating manpower services	Policies and procedures of operation theatres	Medicolegal services- Steps for Medicolegal Examination	Quality assessment and management tools	Emerging health insurance – components of health insurance
3-5	SLO-2	Hospital engineering: Key to efficient healthcare services	Assessing operation theatre utilisation	Problems faced by healthcare professionals in medicolegal service	Clinical audit	Emerging health insurance-Types of health insurance
S-6	SLO-1	Designing disabled friendly hospitals- Barriers faced and implications in Persons with disabilities	Clinical laboratory services-Introduction and role of laboratory medicine	Food safety in hospitals-Need of food safety	Quality improvement-Cause and effect method	Advantages and common problems of health insurance schemes
	SLO-2	Need for disabled-friendly health services	Testing procedure in clinical laboratory	Sources of food contamination	Pareto analysis	Role of health and hospital administrators in Health insurance
S-7	SLO-1	Barrier-Free Environment to Universal Design	Radio diagnosis and imaging services- Planning and equipments of radiology department	Materials management- Principles of material management	Failure mode and effect analysis	Telemedicine clinic –functions and classification of telemedicine
	SLO-2	Overcoming the barriers	Advancement in radiology service	Concepts of Inventory control	Triggers of quality improvement strategy in a hospital	Challenges for telemedicine
S-8	SLO-1	Energy conservation- Classification	Radiation oncology service-Radiotherapy facilities	Modern techniques for inventory control	Occupational safety-Roles and responsibilities	Growth of mobile phones and potential of mobile health
3-0	SLO-2	Types of energy streams in hospitals	Nuclear medicine services-Categorization and nuclear medicine department	Integrated concept for materials management	Prevention of hazards specific to health sector	Mobile health and its applications
S-9	SLO-1	Need for energy conservation	Planning of nuclear medicine department	Purchase and procurement system- Essentials for procurement process	Hospital security-Physical security	Challenges in implementing information and Communication technology in healthcare
	SLO-2	Energy conservation opportunities in hospitals	Ancillary requirements	Purchase system	Organizational chart of security wing	Information and communication technology applications in healthcare

Learning Resources	 SonuGoel, Anil Kumar Gupta, Amarjeet Singh, Hospital administration A problem approach, 1st ed., Elsevier, 2014 	 - solving 2. Sakharkar B M, Principles of hospital administration and planning, 2nd ed., Jaypee Brothers Medical Publishers, 2 3. Kunders G D, Hospitals: Facilities planning and management, 1st ed., Tata Mcgraw Hill, 2008 	009
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Learning Asse	essment													
	Bloom's		Einal Examinatio	n (50%) woightaga)										
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#	Final Examination (50% weightage)				
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice			
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-			
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-			
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-			
	Total		0 %	-	0 %	-	0 %		0 %	10	0 %			

CLA – 4 can be from any combination of these: Assignments, Seminars, Tech Talks, Mini-Projects, Case-Studies, Self-Study, MOOCs, Certifications, Conf. Paper etc.,

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Sathyanarayanan J, Mindray Medical India Pvt Ltd, sathyanarayananjayagopal@mindray.com	1. Dr. S. Poonguzhali, Anna University, poongs@annauniv.edu	1. Dr. D. Ashokkumar, SRMIST
2. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	2. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	2.Mr. P. Muthu, SRMIST
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Course Code	18ECO123T	Course Name	BIOMEDICAL IMAG					Cour Categ		0					0	pen E	lectiv	е				_	L 3	Т 0	P 0	C 3
Pre-requ Cours				Co-requisite Courses	Nil			1	rogre Cour	ssive ses	Nil															
Course Of	burse Offering Department Electronics and Communication Engineering with specialization in Biomedical Engineering Data Book / Codes/Standards							N																		
Course Le	Course Learning Rationale (CLR): The purpose of learning this course is to:								Lear	ning						Progr	am L	earni	ing O	utcon	nes (F	PLO)				-
CLR-1 :	Utilize the working pr	inciple of X-ray	' imaging						1 2	2 3	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-1: Utilize the working principle of X-ray imaging CLR-2: Analyze the principle behind tomographic imaging and the reconstruction techniques CLR-3: Interpret the theory behind nuclear medicine and utilize the working of imaging modal CLR-4: Analyze the physics of ultrasound and the different imaging modes using ultrasound CLR-5: Utilize the physical principle of nuclear magnetic resonance and magnetic resonance CLR-6: The learner will be to gain knowledge in the working principle of imaging modalities unclear medicine, ultrasound and magnetic resonance imaging. Course Learning Outcomes (CLO): At the end of this course, learners will be able to:					dalities in nu d ce image re	construction		Level of I hinking (Bio Evanated Brofinionau	Expected Floridericy (%)	Expected	Engineering Knowledge	Problem Analysis	Design & Development	Analysis, Design, Research	Modern Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	ong Leaming	PSO-1: Problem interface of Enge	D-:2: Design & Devel dical Devices	PSO-3: multidisciplinary research	
CLO-1 :	Analyze the physics								2 8			М	-	-	-	-	-	-	-	-	-	-	-	М	-	-
CLO-2 :	Identify the principle	behind working	of tomographic	imaging and reco	nstruction pro	ocedures.			3 8			М	-	-	-	-	-	-	-	-	-	-	-	М	-	-
CLO-3 :	Analyze the working principle of nuclear medicine imaging modalities						3 8			М	-	-	-	-	-	-	-	-	-	-	-	М	-	-		
CLO-4 :							3 8			М	-	-	-	-	-	-	-	-	-	-	-	М	-	-		
								3 8			М	-	-	-	-	-	-	-	-	-	-	-	М	-	-	
CLO-6 :	D-6 : Understand the basic principle and working of medical Imaging systems							3 8	5 7	5	М	-	-	-	-	-	-	-	-	-	-	-	М	-	-	
				• •							1							1								

Du	ration	X-ray	Computed Tomography	Ultrasound	Magnetic Resonance Imaging	Nuclear medicine 9	
()	iour)	9	9	9	9		
S-1	SLO-1	General principles of Imaging with X-rays		Characteristics of sound: Propagation, wavelength, frequency and speed	Principles of NMR Imaging	Radionuclide decay terms and relationship	
3-1	SLO-2	X-ray Production –X-ray source	Comparison between tomographic and planar imaging	Pressure, Intensity and dB scale	Free Induction decay	Nuclear transformation	
S-2	SLU-1 X-ray tube current, tube output		Basic principle: Technique of producing CT images	Interaction of ultrasound with matter: Acoustic impedance, reflection, refraction	Excitation, Emission	Radionuclide production	
0-2	SLO-2	Beam intensity, X-ray Energy Spectrum	Contrast scale	Scattering, Attenuation	Relaxation times-T1 & T2	Radiopharmaceuticals	
S-3	SLO-1		System components: first generation, second generation, third generation,	Transducers: Piezoelectric materials, resonance transducers	Spin echo technique	Radiation detection and measurement: types of detectors, Gas-filled detectors	
3-5	SLO-2	Photoelectric effect	Fourth, fifth and spiral/helical CT	Damping block, matching layer, Resolution	Spin echo contrast weighting	Scintillation detectors	
S-4	SLO-1	Linear and Mass attenuation coefficient of X-rays in tissue	X-ray source, types of detectors	Transducer arrays	T1 weighted image	Semiconductor detectors	
5-4	SLO-2		Gantry and slip ring technology, Collimation and filtration	Multi-element linear array scanners	T2 weighted image	Pulse height spectroscopy	
S-5	SLO-1	Antiscatter grids Intensifying screens	Processing system	Multi-linear and phased array	Gradient recalled sequence	Non-imaging detector applications	

	SLO-2	X-ray films	Iterative reconstruction, back projection reconstruction		Proton density weighted images, pulse sequence for fast imaging	Counting statistics	
S-6	SLO-1	Instrumentation for computed and digital radiography	Filtered back projection	Basic pulse echo apparatus: A-scan	Slice selection gradient	Nuclear imaging	
3-0	510-2	X-ray Image characteristics: Signal to Noise ratio	Helical /Spiral CT: Helical pitch	B-Mode	Frequency encode gradient	Anger scintillation camera	
S-7	SLO-1	patial resolution, Contrast to Noise ratio Basic reconstruction approaches		M-mode	Phase encode dradient	Basic principle :Emission computed tomography	
3-1	SLO-2	X-ray contrast agents, X-ray angiography	Slice sensitivity profile	Echocardiograph	ZD SDIN ECNO DATA ACQUISITION	Single photon emission computed tomography	
S-8			Multislice CT	Duplex scanner	Basic NMR components: Main magnet, RF transmitter/receiver	Positron emission tomography	
3-0	SLO-2	X-ray mammography	Detector configuration	Intravascular imaging	Body coils, gradient coils	Imaging techniques and scanner instrumentation	
S-9	SLO-1	Dual energy Imaging	Measurement of X-ray dosage	Artefacts: Refraction, shadowing and enhancement	fMRI : Basic principle	Dual modality: PET/CT	
	SLO-2	Abdominal X-ray scans	Methods for dose reduction	Reverberation	BOLD concept, MR spectroscopy	Working and applications	

Learning Resources	1.	R.S.Khandpur, Handbook of Biomedical instrumentation, 3rd ed., Tata McGraw Hill, 2014	2.	Jerrold T. Bushberg, John M. Boone, The ess Wilkins, 2011
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Jerrold T. Bushberg, John M. Boone, The essential physics of medical imaging, 3rd ed., Lippincott Williams & Wilkins, 2011

Learning Assessment											
	Bloom's		Continuous Learning Assessment (50% weightage)							Final Examination (E0% weighteen)	
	Level of Thinking	CLA – 1 (10%)		CLA – 2 (15%)		CLA – 3 (15%)		CLA – 4 (10%)#		Final Examination (50% weightage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total 100 %		100 %		100 %		100 %		100 %		

Course Designers								
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts						
1. Sathyanarayanan J, Mindray Medical India Pvt Ltd, <u>sathyanarayananjayagopal@mindray.com</u>	1. Dr. S. Poonguzhali, Anna University, poongs@annauniv.edu	1. Dr. T. Jayanthi, SRMIST						
2. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	2. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	2.Dr. U. Snekhalatha, SRMIST						
3. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	3. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in							

Cour Cou		18ECO124T	Course Name		HUMA	N ASSIST DEVICES		-	ourse tegory	,	0				C	Dpen E	Electiv	e				l ;	L - 3 (F F) 0	C 3
	equisite urses	Nil			Co-requisite Courses	Nil				gress ourse		Nil													
Course	e Offering	Department			munication Enginee nedical Engineering		/ Codes/Standards		Nil																
Course	e Learnin	g Rationale (CL	R): The pu	rpose of learnii	ng this course is to:				L	earnir	ng					Prog	ram L	earni	ng Ou	tcom	es (Pl	_0)			
CLR-1			07		ssisting human disa	ability			1	2	3		1 2	2 3	4	5	6	7	8	9	10	11			4 15
CLR-2 CLR-3		ze various device the various assi													-E			lity					at the	p cine	. 4
CLR-4		the various assi							(moc	(%)	(%)		ge	ŧ	Research			inab		Ł		e	indi	<u>Medi</u> velo	ary
-	CLR-5 : Utilize the various assist device used in orthopaedic								(Bld	ency	nent		Med	bme	Re	age		usta		Ň		nanc	p d	8 De	iplin
CLR-6	: Analy.	ze the working p	rinciples of ca	ardiac assist de	evices and Artificial	kidney			Jking	ofici	tainr		kno Kno	ivelo	sign	IUs	ulture	t & S		Tear	tion	& Fi	arni	of Engg. & Medici Design & Develop	idisc
									Thir	d Pr	d At		sring	& De	, De	T00	န ဂ	meni		al &	nicat	Mgt.	g Le	Des	Dev
		g Outcomes (CL	e able to:			Level of Thinking (Bloom)	Expected Proficiency (%)	Expected Attainment (%)			Problem Analysis Design & Development	Analysis, Design,	Modem Tool Usage	Society & Culture	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	interface of Enga. & Medicine PSO-:2: Design & Develop	Medical Devices PSO-3: multidisciplinary			
	LO-1: Comprehend the assistive technology (AT) used for mobility LO-2: Analyze the Assist technology used for hearing								2	85	75		M		-	-	-	-	-		-	-		M	
CLO-2					mpairment of vision				3	85 85	75 75		M ·	· ·	-	-	-	-	-	-	-	-	-	- 1	
		ate the assist de			mpairment of vision				3	85	75				-	-	-	-	-		-	-	-	- I M I	
CLO-5		ze the latest use			Ith care				3	85	75		M ·		-	-	-	-	-	-	-	-		M	
CLO-6		n the prosthetic I							3	85	75		M		-	-	-	-	-	-	-	-		M	
							-																		
	ation																								
(h	our)		9			9		9							9				9						
S-1	SLO-1	Basic assessme mobility	ent and evalu	ation for	Basic ear anatomy	v, Mechanism of hearing	Anatomy of eye				,	Anatomy of upper & lower extremities -				E	Basic Anatomy and physiology of heart.				eart.				
3-1	SLO-2	Basic assessme mobility	ent and evalu	ation for	Common tests au	diograms	Categories of visual i	mpairr	nent		(Classific	ation	of amp	utation	types	6	(Cardia	c assi	st dev	vices			
S-2	SLO-1	Manual wheelch	nairs		Air conduction, Bo	ne conduction	Intraocular Devices				/	Prosthe	sis pre	scriptic	n			I	Intra-A	ortic E	Balloo	n Pun	np (IA	BP),	
	SLO-2	Electric power w	vheelchairs		Masking technique	98,	Extraocular Devices					Hand ar		•				ŀ	Prosth	etic he	eart va	alves			
S-3	SLO-1	Power assisted	wheelchairs		SISI		Permanent Vision Re	storati	on		ŀ	Differen powered	l limb	orosthe	sis			E	Evalua	tion o	f pros	thetic	valve		
	SLO-2	0-2 Wheel chair standards & tests - Hearing aids principles Non-Permanent V					Non-Permanent Visio	on Res	toratio	n		Differen bowered				externa	ally	ŀ	Heart p	bacem	aker				
S-4	SLO-1					Voice Control Sound					Foot ort	hosis					(CABG							
	SI 0-2 Control systems, navigation in virtual				Sensor Technology Adapted for the Vision Impaired				on I	Pediatric orthoses				E	Extracorporeal support										
S-5	SLO-1 Wheel chair seating and pressure ulcers. Cochlear Implants Librail				Libraille					Wrist-hand orthosis				١	Vascular prosthesis										
- •	SLO-2	LO-2 EOG based voice controlled wheelchair Internal Hearing Aid GRAB				GRAB	feedback in orthotic system Vascular prosthe			sthesis															
S-6	SLO-1	-1 BCI based wheelchair External Hearing Aid ma					mathematical Braille			Components of upper limb prosthesis Artificial heart															

	SLO-2	Fuzzy logic expert system for automatic tuning of myoelectric prostheses	Permanent Hearing Restoration	Blind mobility aids	Components of lower limb prosthesis	Intermittent positive pressure breathing (IPPB) type assistance for lungs
S-7	SLO-1	Intelligent prosthesis	Non-Permanent Hearing Restoration	Reading writing & graphics access,	Lower extremity- and upper extremity- orthoses	Dialysis for kidneys
3-1	SLO-2	Intelligent prosthesis	Touch Tactile Haptic Technology	Orientation & navigation Aids	Lower extremity- and upper extremity- orthoses	Artificial Kidney
	SLO-1	Future trends in assistive technology	Sound Coding Translation	Wearable Assistive Devices for the Blind	functional electrical stimulation	Haemodialysis
S-8	SLO-2	virtual reality based training system for disabled children	Acoustic Transducers Hearing Quality	Wearable tactile display for the fingertip.	Sensory assist devices	Membrane dialysis
S-9	SI ()-1	Information technology, telecommunications,	Electric Electronic Stimulation	Cortical implants	Sensory assist devices	Portable dialysis monitoring and functional parameter
5-9	SLO-2	new media in assisting healthcare	Hearing Enhancement	Retinal implants	Slints – materials used	Latest use of assistive technology for chronic heart diseases and healthcare

Learning Resources		Levine S.N. Advances in Bio-medical engineering and Medical physics, 1 st ed., Vol. I, II, IV, Interuniversity publications, 1968. Marion. A. Hersh, Michael A. Johnson, Assistive Technology for visually impaired and blind, 1 st ed., Springer Science & Business Media, 2010 Kopff W.J, Artificial Organs, 1 st ed., John Wiley and Sons, 1976 Daniel Goldstein, Mehmet Oz, Cardiac assist Devices, Wiley, 2000	6. 7. 8. 9.	Albert M.Cook, Webster J.G, Therapeutic Medical Devices, Prentice Hall Inc., 1982 Gerr .M. Craddock Assistive Technology-Shaping the future, 1 st ed., IOS Press, 2003 Brownsell, Simon, et al., A systematic review of lifestyle monitoring technologies, Journal of telemedicine and telecare 17.4 (2011): 185-189 Yadin David, Wolf W. von Maltzahn, Michael R. Neuman, Joseph.D, Bronzino, Clinical Engineering, 1 st ed., CRC Press, 2010
	5.	Kenneth J. Turner, Advances in Home Care Technologies: Results of the match Project, 1st ed., Springer, 2011	10.	

Learning Asses	sment											
	Bloom's				Final Examinatio	n (50% weightage)						
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA –	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)	
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember	40 %		30 %		30 %		30 %		30%		
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %		40%		
Level 2	Analyze	40 /0	-	40 70	-	40 70	-	40 /8	-	4070	-	
Level 3	Evaluate	20 %		30 %	-	30 %	_	30 %		30%		
Level 3	Create	20 70	-	50 %	-			50 %	-		-	
	Total	100) %	100) %	10	0 %	100	0 %	100 %		

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
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Course Code	18ECO125T	Course Name	BIOMEDICAL DEVICES		urse egory		0				Opei	n Elect	ive					L . 3	T F 0 (
Pre-requisite Courses	Nil		Co-requisite Courses				gress ourses														
Course Offerin	ng Department		nics and Communication Engineering with lization in Biomedical Engineering	Data Book / Codes/Standards		Nil															
Course Learn	Course Learning Rationale (CLR): The purpose of learning this course is to:							g				Pro	ogram	Learr	ning C	Outcor	mes (I	PLO)			
CLR-1 : Utili								3	1	2	3	4 5	56	7	8	9	10	11	12	13 1	4 15
CLR-2 : Utili	CLR-2: Utilize the quality management principles and good management practices													y					at the	e e	-i
	ze the various qua					(n	(%)					arch		Sustainability		~			t	l Sulvirig at trie g. & Medicine & Develop	/ solu
	ze the various qua					Thinking (Bloom)		nt (%	gge		lent	Researc		tain		Vor		nce	, in the second s	& Me	Devices multidisciplinary for health care
			icable to healthcare globally and nationally			g (E	ienc	Iemei	owle	<u>.</u>	udo	L R	e ade	Sus		L L L		ina	ing		alth cipli
CLR-6 : Imp	lement the global s	standards in h	pealthcare			nkin	ofic	ttain	ž	alys	evel	Design, I	Culture			Tea	tion	8	g Learnir Droblem	of Engg	tidis
							ЧÞ	A b	ring	١An	& D	ې ق	မ ျ မ	2		al &	nica	Mgt	g Le	e de e	De De
Course Learn	ing Outcomes (CL	.0): At the e	end of this course, learners will be able to:			Level of	Expected Proficiency	Expected Attainment (%)	Engineering Knowledge	Problem Analysis	Design & Development	Analysis,	Mouerri I ou Usage Society & Culture	Environment &	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	nterface	Medical Devices PSO-3: multidisciplinary research for health care
CLO-1 : Ana	CLO-1: Analyze the underlying concepts of quality and quality control concepts of an organization						85	75	-	-	-			-	-	-	-	-	L	-	- L
CLO-2 : Eva						3	85	75	L	-	-			-	-	-	-	-	-	-	- L
CLO-3 : Eva						3	85	75	М	-	-			-	-	-	-	-	-	-	
CLO-4 : Ana						3	85	75	L	-	-			-	-	-	-	-	-	-	- L
CLO-5 : Ana						3	85	75	-	-	-		· -	-	-	-	-	-	-	-	
CLO-6 : Ana						3	85	75	М	-	-			-	-	-	-	-	L	-	- L

Du	ration	Introduction to quality	TQM principles	Statistical process control	TQM tools	Quality systems
(h	iour)	9	9	9	9	9
S-1	SLO-1	Definition of Quality	Customer satisfaction – Customer Perception of Quality	The seven tools of quality	Benchmarking	ISO 9000 Systems
5-1	SLO-2	Dimensions of Quality	Customer Complaints	Cause-and-effect diagram	Reasons to Benchmark	ISO 9000 Systems
S-2	SLO-1	Quality Planning	ality Planning Service Quality Check sheet			ISO 9000:2000 Quality System – Elements
5-2	SLO-2	Quality Planning	Customer Retention	Check sheet	Benchmarking Process	ISO 9000:2000 Quality System – Elements
S-3	SLO-1	Quality costs	Employee Involvement	Control chart	Quality Function Deployment (QFD)	Need for Accreditation of hospitals
3-3	SLO-2	Quality costs	Motivation	Control chart	Quality Function Deployment (QFD)	Need for Accreditation of hospitals
S-4	SLO-1	Basic concepts of Total Quality Management	Empowerment	Histogram	House of Quality	FDA Regulations
0-4	SLO-2	Principles of TQM	Teams and Team Work	Histogram	House of Quality	FDA Regulations
S-5	SLO-1	Leadership – Concepts	Recognition and Reward	Pareto chart	QFD Process - Benefits	Joint Commission
3-5	SLO-2	Role of Senior Management	Performance Appraisal	Pareto chart	QFD Process - Benefits	Joint Commission

S-6	SLO-1	Quality Council	Juran Trilogy	Scatter diagram	Total Productive Maintenance (TPM) – Concept	Regulatory Bodies of India
0-0	SLO-2	Quality Statements	Juran Trilogy	Scatter diagram	Total Productive Maintenance	Medical Council of India
S-7	SLO-1	Strategic Planning	PDSA Cycle	Stratification	Improvement Needs	Pharmacy Council Of India
3-1	SLO-2	Strategic Planning	PDSA Cycle	Stratification	Improvement Needs	Pharmacy Council Of India
S-8	SLO-1	Deming Philosophy	Kaizen	Six sigma	FMEA	Indian Nursing Council
9-9	SLO-2	Deming Philosophy	Kaizen	Six sigma	FMEA	Indian Nursing Council
S-9	SLO-1	Barriers to TQM Implementation	5S	Six sigma	Stages of FMEA	Dental Council of India
2-9	SLO-2	Barriers to TQM Implementation	5S	Six sigma	Stages of FMEA	Homeopathy Central Council

Learning Resources		Rose J.E, Total Quality Management, Kogan Page Ltd., 1993 Cesar A. Cacere, Albert Zana, The Practise of clinical Engineering, Academic Press, 1997 Greg Bounds, Beyond Total Quality Management-Toward the emerging paradigm, McGraw Hill, 2013	4. 5.	Joseph J.Carr, Elements of Electronics Instrumentation and Measurement, 2 nd ed., Pearson Education, 2003 Jerrold T. Bushberg, John M. Boone, The essential physics of medical imaging, 3 rd ed., Lippincott Williams & Wilkins, 2011
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Learning Assess	sment											
	Bloom's			Einal Examination	n (50% weightage)							
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA – S	3 (15%)	CLA – 4	l (10%)#		in (50 % weightage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember	40 %		30 %		30 %		30 %		30%		
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %	_	40%		
Level Z	Analyze	40 /0	-	40 70	-	40 70	-	40 /0	-	4070	-	
Level 3	Evaluate	20 %		30 %		30 %		30 %	-	30%		
LEVEI J	Create	20 70	-	50 %	-	50 70	-	50 %	-	50%	-	
	Total 100 %				0 %	100) %	100) %	100 %		

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Sathyanarayanan J, Mindray Medical India Pvt Ltd, <u>sathyanarayananjayagopal@mindray.com</u>	1. Dr. S. Poonguzhali, Anna University, poongs@annauniv.edu	1. Dr. D. Kathirvelu, SRMIST
2. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	2. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	2. Dr. D. Ashok Kumar, SRMIST
3. Mr. Hariharasudhan - Johnson Controls, Pune, <u>hariharasudhan.v@jci.com</u>	3. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course	195001267	Course	Sporte Diamonhanian	Course	E	Professional Flective	L	Т	Р	С
Code	18ECU1261	Name	Sports Biomechanics	Category	E	Professional Elective	3	0	0	3

Pre-requisite Courses	18ECE267J	Co-requisite Courses	Nil		Progressive Nil Nil
Course Offering	Department	Electronics and Communication Engi	neering	Data Book / Codes/Standards	Nil

Course L	earning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng	Program Learning Outcomes (PLO)													
CLR-1 :	Understand the fundamental muscle action and locomotion in biomechanical point of view	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 14	15
CLR-2 :	Get an idea about the movement patterns and causes of movements	Ê	()								y						le the	
CLR-3 :	Understand the qualitative and quantitative analysis of sports movements	l	%)	(%)				arch			bilit						b ci at	solt
CLR-4 :	Acquire an idea about the basic concept of jumping & aerial movement and throwing & hitting	B	nc)	ent	dge		ant	Research			aina		Work		8		ving Medi evelo	lary are
CLR-5 :	Get an idea about the injury prevention, rehabilitation and special Olympic sports	g	icie	E	N N	s	ŭ	Å,	age	m	Sustainability		۳V		nan	و	N SO	s ciplinary alth care
CLR-6 :	Get an overall idea about the applications of biomechanics in sports	Thinking (Bloom)	rof	Attainment	Kno	ilysi	Development	Design,	Us	Culture	∞ŏ		Feam	u	& Finance	arni	lem ign	disc hea
			P P P		ing	Analysis	De	B	Tool Usage	& CL	nent		. 8	licat	Mgt.	- P	^{orob} of E Des	for for
Course L	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected Proficiency (%)	Expected	Engineering Knowledge	Problem	Design &	Analysis,	Modem 7	Society &	Environment	Ethics	Individual	Communication	Project N	Life Long	PSO-1: F nterface PSO-:2: I	PSO-3: r PSO-3: r research
CLO-1 :	Illustrate the muscle action in sport and locomotion	1	80	70	М												L	
CLO-2 :	Analyze the movement patterns and its causes	1,2	80	70	М												М	
CLO-3 :	Describe the Qualitative and Quantitative analysis of sports movements	2	80	70	М												М	
CLO-4 :	Analyze the movement of action such as jumping, throwing, hitting and aerial movement	2	80	70			L										LL	L
CLO-5 :	CLO-5: Identify the injury scenario and special Olympic sports		80	70													L L	L
CLO-6 :																		

	ration	Muscle Action in Sport and Exercise and locomotion- Biomechanical view	Movement patterns and its causes	Qualitative and Quantitative analysis of sports movements	Jumping and Aerial Movement, Throwing and Hitting	Injury Prevention, Rehabilitation and Special Olympic Sports
(r	nour)	9	9	9	9	9
S-1	SLO-1	Introduction to Biomechanics	Introduction to Movement patterns	Introduction to Analysis of Sport Movements	Introduction to Aerial movement	Mechanisms of Musculoskeletal Injury
5-1	SLO-2	Applications of Biomechanics	Defining human movements	A structured analysis framework	Types of Aerial Movement - Rotation during flight,Motion of the mass centre	Musculoskeletal Loading During Landing
0.0			Fundamental movements-Walking, Running	Preparation stage		Sport-Related Spinal Injuries and their Prevention
S-2		Mechanical Properties and Performance in Skeletal Muscles	Fundamental movements-Throwing, Jumping	Observation stage	Control of aerial movement	Sport-Related Spinal Injuries and their Prevention
	SLO-1	Muscle-Tendon Architecture	qualitative and quantitative movement	Evaluation and diagnosis stage	$IDIFORDCHOD \cap HIAD \cup IDIFDO$	Impact Propagation and its Effects on the Human Body
S-3	SLO-2	Athletic Performance	Comparison of qualitative and quantitative movement analysis	Intervention stage – providing appropriate feedback		Impact Propagation and its Effects on the Human Body
S-4	SLO-1	Eccentric Muscle Action in Sport and Exercise	Movement patterns-geometry of motion	lidenuivino critical leatures of a movement	1 0	Neuromechanics of the Initial Phase of Eccentric Contraction

	SLO-2	Stretch–Shortening Cycle of Muscle Function	Fundamentals of movement	Identifying critical features of a movement	Determinants of Successful Ski-Jumping Performance	Induced Muscle Injury
S-5	SLO-1	Biomechanical Foundations of Strength	Linear motion and the centre of mass	The use of videography in recording sports movements	Principles of Throwing	Manual Wheelchair Propulsion
3-0	SLO-2	Power Training	The geometry of angular motion and the coordination of joint rotations	The use of videography in recording sports movements	The Flight of Sports Projectiles	
S-6	SLO-1	Factors Affecting Preferred Rates of Movementin Cyclic Activities	Forces in sport	Recording the movement	Javelin Throwing: an Approach to	Sports after Amputation
3-0	SLO-2	The Dynamics of Running	Combinations of forces on the sports performer	Experimental procedures -Two dimensional videography	Performance Development	Spons aner Ampulation
S-7	SLO-1	Resistive Forces in Swimming	Momentum and the laws of linear motion	Experimental procedures -Three dimensional videography	Shot Putting	Biomechanics of Dance
5-1	SLO-2	Propulsive Forces in Swimming	Force-time graphs as movement patterns	Data processing	Hammer Throwing: Problems and Prospects	Biomechanics of Dance
	SLO-1	Performance-Determining Factors in Speed Skating	Determination of the centre of mass of the human body	Projectile motion	Hammer Throwing:Problems and Prospects	
S-8	SLO-2	Cross-Country Skiing: Technique	Fundamentals of angular kinetics and Generation and control of angular momentum	Linear valacities and accolorations caused	Hitting	Biomechanics of Martial arts
S-9	SLO-1	Cross-Country Skiing: Equipment	Measurement of force	Rotation in three-dimensional space	Kicking	Biomechancis of YOGA
	SLO-2	Factors Affecting Performance	Measurement of pressure	Rotation in three-dimensional space	Simple concept problems	

Learning Resources Susan J Hall, "Basic Biomechanics", McGraw-Hill Higher Education, 7th edition, 2014
 Vladimir M. Zatsiorsky, Biomechanics in Sports: Performance Enhancement and Injury Prevention, 1st ed., Blackwell Science Ltd, 2000 Jules Mitchell, "Yoga Biomechanics", 1 edition, Handspring Publishing Limited, 2018
 Roger Bartlett, Introduction to Sports Biomechanics: Analysing Human Movement Patterns, 2nd ed., Routledge, 2007

Learning As	earning Assessment												
	Bloom's			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Examination (50% weightag			
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA – S	3 (15%)	CLA – 4	4 (10%)#	Final Examination (50 % weightage)			
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice		
Level 1	Remember Understand	40 %	-	40 %	-	40 %	-	30 %	-	30%	-		
Level 2	Apply Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-		
Level 3	Evaluate Create	20 %	-	20 %	-	20 %	-	30 %	-	30%	-		
	Total	100	0 %	100 %		100) %	10	0 %	100 %			

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. Sathyanarayanan J, Mindray Medical India Pvt Ltd, sathyanarayananjayagopal@mindray.com	1. Dr. S. Poonguzhali, Anna University, poongs@annauniv.edu	1. Ms. Oinam Robita Chanu, SRMIST
2. Mr. Anuj Kumar, Bombardier Transportation, Ahmedabad, kumaranuj.anii@gmail.com	2. Dr. Meenakshi, Professor of ECE, CEG, Anna University, meena68@annauniv.edu	2. Dr .D. Ashok kumar, SRMIST
. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@ici.com	3. Dr. Venkatesan, Sr. Scientist, NIOT, Chennai, venkat@niot.res.in	

Course Code	18ECO131J	Course Name	VIRTUAL	VIRTUAL INSTRUMENTATION		y	0	Open Elective			Open Elective			L 2	T 0	P 2	C 3					
Pre-requ Cours			Co-requisite Courses	Nil		gress ourse		Nil														
Course Offering Department Electronics and Communication Engineering Data Book / Codes/Standards Nil																						
Course Le	arning Rationale (Cl	.R): The pur	pose of learning this course is to:		L	.earni	ng					Progr	am Le	arnin	ıg Ou	tcom	ies (P	PLO)				
CLR-1 :	Study the concepts o	Virtual instru	mentation and to learn the program	nming concepts in VI.	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Study about the vario	us real time da	ata acquisition methods.								ch			llity						for ems	S tor	
	,		Interfacing concepts.		(Bloom)	Proficiency (%)	(%)	ge		ŧ	Research			Sustainability		¥		e	-	control te syste	DCS	
CLR-4 :	To study the program	ming techniqu	es for various control techniques	using VI software	(Bic	ancy	Attainment (%)	vled		Development		ge		usta		N N		Finance			S S	s
CLR-5 :	To study various ana	ysis tools for F	Process control applications.		king	officie	ainn	Kno	lysis	velo	sign	Use				Tear	ы	ъ М	Leaming	discret	ilize PL	tive tskil
CLR-6 :	To study various real	time measure	ment systems		of Thinking	Pro	d Att	ning	Ana	De	, De	Tool	& Cu	nent		- 8 I	licat	Agt.	Le	월 🛷 🗄	Jtilize f syste	Effectiv ement s
					el of	Expected	Expected	Engineering Knowledge	Problem Analysis	Design & I	Analysis, Design,	Modern Tool Usage	Society 8	Environment &	s	ndividual & Team Work	Communication	^o roject Mgt. &	Б,	÷ ĕ	5 in	PSO-3: Effective management skills
Course Le	urse Learning Outcomes (CLO): At the end of this course, learners will be able to:				Leve	Exp	БХр	Eng	Prot	Des	Ana	Mod	Soci	Envi	Ethics	Indiv	Con	Proj	Life	PSO contir	PSC cont	PSC man
CLO-1 :	1: An ability to understand the purpose of virtual instrumentation and understand the construction of VI				1,2	80	70	Н												Н		
CLO-2:	CLO-2 : An ability to understand and apply various data acquisition methods.				2	85	75	Н												Н	Н	
CLO-3 :	CLO-3 : An ability to understand and implement the available interfacing instruments				2	75	70	Н	Н	Н	Н	Н								Н	Н	Н
CLO-4 :			2,3	85	80	Н	Н	Н	Н	Н							_	Н		Н		
CLO-5 :	D-5 : An ability to understand and develop a program foran engineering application.			2,3	85	75	Н	Н	Н	Н	Н				Н	Н	Н	Н	Н	Н		
CLO-6:	: An ability to understand and implement various measurement systems				2,3	80	70	Н	Н	Н	Η	Н				Н	Η	Η	Н	Н	Н	

Durth		Learning Unit / Module 1	Learning Unit / Module 2	Learning Unit / Module 3	Learning Unit / Module 4	Learning Unit / Module 5
Duratio	n (hour)	12	12	12	12	12
	SLO-1	Historical perspective, Need of VI, Advantages of VI, Virtual Instruments versus Traditional Instruments	A/D Converters, Organization of the DAQ VI system -	Introduction to PC Buses	Introduction to Non continuous controllers in LabVIEW	PC based digital storage oscilloscope
S-1	SLO-2	Review of software in Virtual Instrumentation ,Software environment Architecture of VI, Introduction to the block diagram and Front panel Pallets	D/A Converters, Types of D/A	Local Buses-ISA, PCI,	Introduction to continuous controllers in LabVIEW	Sensor Technology
	SLO-1	Creating and saving a VI, Front Panel Tool Bar, Block diagram Tool Bar, Palettes	plug-in Analog Input/output cards - Digital Input and Output Cards,	RS232, RS422	Design of ON/OFF controller	Applications of sensor Technology
S-2	SLO-2	Creating sub VI, Creating an ICON, Building a connector pane, Displaying VI'S, Placing and Saving Sub VI'S on block diagram, Example of full adder circuit using half adder circuit	Organization of the DAQ VI system -	RS485	Proportional controller for a mathematically described processes using VI software	Signal processing Techniques
	SLO-1	Lab-1: Front Panel controls and Indicator	Lab-12: Measurement of diode I-V		Lab-22: On-off temperature controller	
S-3	SLO-2	Lab-2: Verification of Arithmetic Operations	characteristics using LabVIEW	Lab-17: Load cell Data acquisition	using LabVIEW	Lab-28: Design of DSO
S-4	SLO-1	Lab-3: Verification of Half Adder	Lab-13: Temperature measurement using	using RS232	Lab-23: Continuous Control of temperature	Lab-29: Analysis of different signal
3-4	SLO-2		LabVIEW and DAQ hardware.		using LabVIEW	Filters using LabVIEW

S-5	SLO-1	Loops-For Loop,	Opto Isolation need	Interface Buses-USB,PXI	Modeling of level process	Spectrum Analyzer
5-5	SLO-2	While Loop	Performing analog input and analog output	VXI,	Basic control of level process in LabVIEW	Waveform Generator
	SLO-1	Arrays,	Scanning multiple analog channels	SCXI	Modeling of Reactor Processes	Data visualization from multiple locations
S-6	SLO-2	Clusters, plotting data	Issues involved in selection of Data acquisition cards	PCMCIA	Basic control of Reactor process in LabVIEW	Distributed monitoring and control
S-7	SLO-1	Lab-5: Program to find Addition of First n natural numbers using for loop		Lab-18: DC motor control using VXI	Lab-24: On-off Level controller using	Lab-30: Real time spectrum analysis
	SLO-2	Lab-6: Program to find Addition of First n odd numbers using while loop.	Lab-14: Flow measurement in water using	•	LabVIEW	using LabVIEW
S-8	SLO-1	Lab-7: Implementation of Array functions.	LabVEW and DAQ hardware	Lab-19: GPIB with VISA functions	Lab-25: Continuous Control of pressure	Lab-31: Arbitratory Waveform Generator
	SLO-2	Lab-8: Calculation of BMI using cluster			controller using LabVIEW	using LabVIEW
S-9	SLO-1	Charts	Data acquisition modules with serial communication	Instrumentation Buses - Modbus and GPIB	Case studies on development of HMI in VI	Vision and Motion Control
3-9	SLO-2	Graphs	Design of digital voltmeters with transducer input	Networked busses – ISO/OSI	Case studies on development of HMI in VI	Examples on Integrating Measurement with vision and motion
S-10	SLO-1	Case and Sequence Structures	Timers and Counters	Reference model,	Case studies on development of SCADA in VI	NI Motion control
5-10	SLO-2	Formula nodes, String and File Input/Output.	Timers and Counters	Ethernet and TCP / IP Protocols	Case studies on development of SCADA in VI	Speed control system
S-11	SLO-1	Lab-9: Monitoring of temperature using Charts and Graphs	Lab-15: Design of digital voltmeters with	Lab-20: Online temperature control	Lab-26: On-off pressure controller using	
3-11	SLO-2	Lab-10: Program for implementing Seven segment display	transducer input using LabVIEW	using LabVIEW using TCP/IP	LabVIEW	Lab-32: Minor Project
S 12	SLO-1	Lab-11: Program to perform Traffic light	Lab-16: Pressure measurement using	Lab-21: Online temperature control	Lab-27: Continuous Control of pressure	
3-12	S-12	control	LabVEW and DAQ hardware DAQ.	using Web publishing tool	controller using LabVIEW	

	1. Nadovich, C., Synthetic Instruments Concepts and Applications, Elsevier, 2005	4. Jamal, R., Picklik, H., Labview – Applications and Solutions, National Instruments Release.
Learning	2. Bitter, R., Mohiuddin, T. and Nawrocki, M., Labview Advanced Programming Techniques, 2 nd ed., CRC Press, 2007	Johnson, G., Labview Graphical programming, McGraw-Hill, 1997
Resources	3. Gupta, S. and Gupta, J. P., PC Interfacing for Data Acquisition and Process Control", 2 nd ed., Instrument Society of	6. Wells, L.K., Travis, J., Labview for Everyone, Prentice Hall, 1997
	America, 1994	7. Buchanan, W., Computer Busses, CRC Press, 2000

Learning Ass	sessment										
	Dia amia			Conti	nuous Learning Ass	essment (50% weigl	htage)			Final Evenination	- (E00/
	Bloom's Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#	Final Examination	n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember Understand	20%	20%	15%	15%	15%	15%	15%	15%	15%	15%
Level 2	Apply Analyze	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%
Level 3	Evaluate Create	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%
	Total 100 %) %		0 %		0 %	10	0 %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. D. Karthikeyan, Controlsoft Engineering India Pvt Ltd, <u>karthikeyan.d@controlsoftengg.in</u>	1. Dr. J. Prakash, MIT, Chennai, prakaiit@rediffmail.com	1. Dr. K. A. Sunitha, SRMIST

2. V. Venkate	swaran, Instrumenta	tion Consulta	nt <u>,vvenkat99@</u>	<u>gmail.com</u>		2. Dr. D. Ned	umaran, Madras Un	iversity, <u>dnı</u>	naran	<u>@gm</u>	ail.com						2	. Mrs.	A. Bri	indha,	, SRM	list			
Course Code	18ECO132T	Course Name		ANALYT	TICAL INSTRU	JMENTATION		Course Category		0					Oper	i Electi	ve					L 3	T 0		C 3
Pre-requis Courses Course Offe		Electror	nics and Comm	Co-requisite Courses unication Engin	INII	Data Book / C	odes/Standards		gress ourse		Nil														
Course Lear	ning Rationale (CLI	R): The purp	oose of learning	this course is t	to:			L	earniı	ng] [Pro	ogram	Learn	ning C	utcor	nes (l	PLO)				
CLR-1 : Ur	derstand the princip	le and theory	of analytical ins	struments				1	2	3		1	2	3 4	1 5	6	7	8	9	10	11	12	13	14	15
CLR-3 : St CLR-4 : St CLR-5 : Ide	nderstand the quantit udy the concept of so udy the various spec entify and solve engin nderstand the workin	eparation scie troscopic tech neering proble	nce and its app aniques and its ams associated	lications instrumentation with Radiation	Techniques	25		Thinking (Bloom)	Expected Proficiency (%)	d Attainment (%)		ing Knowledge	Problem Analysis	t Development	s, Designi, Research Tool Heare	& Culture	nent & Sustainability		ndividual & Team Work	nication	^o roject Mgt. & Finance	Learning	A discrete	ilize PL system	Effective management
Course Lear	ning Outcomes (CL	O): At the e	nd of this cours	e, learners will	be able to:			Level of	Expecte	Expected /		Engineering	Problem	Design &	Modem T	Society &	Environment &	Ethics	Individua	Communication	Project N	2	PSO 1: Au continous	PSU-2: 4 control o	PSO-3: I skills
	pply the principles an							1,2	80	70			Н	Ll	. H		Н						Н	Н	L
	oply the principles of							1,2	85	75			Н	Ll	. F								Н	Н	L
	alyze and understar							1,2	75	70			Н	LI	. <i>F</i>								Н	Н	L
	analyze and unders							1,2	85	80			Н		. H								Н	Н	L
	identify and solve e				ion Technique	S		1,2		75	-		H H		. H								Н	Н	L
CLO-6 : To	understand the wor	kirig ol analyti	icai instruments	in moustries				1,2	80	70		Π	п		. F	I H							Н	Н	L

Dette	. ()	Learning Unit / Module 1	Learning Unit / Module 2	Learning Unit / Module 3	Learning Unit / Module 4	Learning Unit / Module 5
Duratio	n (nour)	9	9	9	9	9
S-1	SLO-1	Introduction to Chemical instrumental analysis		Chromatography, Importance, Basic working of Chromatography	Spectral methods of analysis- Properties or parameters of electromagnetic radiation	NMR spectrometers ,Importance and basic working of NMR Spectroscopy
	SLO-2	Spectral method of analysis	Working of Dissolved oxygen analyzer	Gas chromatography Instrumentation		Magnetic assembly, Probe unit, Instrument stabilization
S-2	SLO-1	Electro analytical and seperative methods	sodium analyzer, Importance of measuring sodium in Industry, Principle working	Basic parts of a gas chromatography	Beer's law UV-visible spectrophotometers Transmittance and absorbance	Types of NMR spectrometer, Minimal type
5-2	SLO-2	Instrumental methods of analysis-basic components and their classification	Working of sodium analyzer	Carrier gas supply Sample injection system	Beer's law Application of beer's law	Multipurpose NMR,Wideline
S-3	SLO-1	Sampling systems	Silica analyzer, Importance of measuring Silica in Industry, Principle working	Chromatographic column, Selection of column	Derivations of beer's law	Applications of NMR Spectrometer
3-3	SLO-2	Importance of Sampling system in chemical Industries and Safety aspects	Working of Silica Analyzer	Thermal compartment, Detection system, Recording system	Single beam and double beam instruments	Mass Spectrometers, Basic working and Importance
S-4	SLO-1	PH Measurement, Principle of PH measurement & Importance of PH measurement in Industries		Liquid chromatography-Principles, types and applications	IR spectrophotometers Instruments of IR	Components of Mass Spectrometers
3-4	SLO-2	Types of Electrodes, Reference Electrodes and types	Types of Moisture measurement	High pressure liquid chromatography	Types of IR Components required for three types of IR	Types of Mass spectrometers Magnetic Sector analyzer, Double focusing spectrometers
S-5	SLO-1	Secondary Electrodes and Types	Methods of oyvden analyzers and	Instrumentation or basic component of HPLC	Instruments of dispersive instrument , IR Radiation Sources and types	Time of flight analyzers, Quadrupole Mass analyzers

	SLO-2	Indicator electrodes	Paramagnetic oxygen analyzer Electro analytical method	Solvent reservoir and its treatment system	Importance of Monochromators and types of Monochromators	Application of mass spectrophotometers
	SLO-1	pH meters direct reading type pH meter null detector type pH meter	CO monitor,Importance of measuring CO	Pumping system, Types of working systems and Importance	Samples And Sample Cells detectors	nuclear radiation detectors, importance of measurement
S-6		ion selective electrodes Types of ion selective electrodes Glass membrane electrodes Liquid membrane electrodes Solid membrane Electrodes	Types of CO monitor	Pulse dampers	FTIR spectrometers, Main components Advantages, disadvantages	GM counter
S-7	SLO-1	Biosensors Features of Biosensor Block diagram of bio sensor	NO2 analyzer, Importance of NO2 measurement	Sample injection system and types	Types of sources Selection factors	Working setup, advantages of GM Counter
	SLO-2	Applications of Biosensors in industries	Types of NO ₂ measurement	Liquid chromatographic column working , Types of Column thermostats	Types of detectors Selection factors	proportional counter, Basic Principle
S-8	SLO-1	conductivity meters ,Importance in Chemical Industries	H ₂ S analyzer, Importance of H ₂ S Measurement	Detection system types	atomic absorption spectrophotometer instruments for atomic absorption spectroscopy	Working setup, advantages of GM Counter
	SLO-2	Types of Conductivity meters	Types of H₂S measurement	Types of Recording system	radiation source chopper	solid state detectors, Basic Principle
SLO-1		Air pollution Monitoring Instruments	Dust and smoke measurement- dust measurement and Importance Types of dust measurement	Application of HPLC, Advantages of HPLC over gas chromatography	production of atomic vapor by flame, Parts by flame photometer Emission system	Working setup, advantages of Solid state detectors
S-9	SLO-2	Estimation of Air pollution	Thermal analyzer , Importance of Thermal analyzers, Types of Thermal analyzer	Detectors types, Factors Influencing the Selection of Detectors	Monochromators And types, Types of Detectors and recording systems and their selection criteria	scintillation counter, Basic principle

Learning Resources	 Khandpur. R.S, "Handbook of Analytical Instruments", Tata McGraw Hill publishing Co. Ltd., 2006 Bella. G, Liptak, "Process Measurement and analysis", CRC press LLC.,2003. Francis Rousseau and Annick Rouesssac "Chemical analysis Modern Instrumentation Methods and Techniques", John wiley & sons Ltd.2007. 	 James W.Robinson, "Undergraduate Instrumental Analysis", Marcel Dekker., 2005. Dwayne Heard, "Analytical Techniques for atmospheric measurement", Blackwell Publishing, 2006.
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	Bloom's			Cont	nuous Learning Asse	essment (50% weig	htage)			Final Examinatio	n (50% weightage)	
	Level of Thinking	CLA –	1 (10%)	CLA – 2 (15%)		CLA –	3 (15%)	CLA – 4	4 (10%)#		Jii (Ju /o welyiilaye)	
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
	Remember	40 %		30 %		30 %		30 %		30%		
Level 1	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %		40%		
Level Z	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Louis 2	Evaluate	20.0/		30 %		20.0/		20.0/		30%		
Level 3	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	100	0 %	10	0 %	10	0 %	10	0 %	10	0 %	

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. D. Karthikeyan, Controlsoft Engineering India Pvt Ltd, karthikeyan.d@controlsoftengg.in	1. Dr. J. Prakash, MIT, Chennai, prakaiit@rediffmail.com	Dr. K. A. Sunitha, SRMIST
2. V. Venkateswaran, Instrumentation Consultant, vvenkat99@gmail.com	2. Dr. D. Nedumaran, Madras University, <u>dnmaran@gmail.com</u>	Mrs. A. Brindha, SRMIST

Course	18ECO133T	Course		Course	0	Open Elective	L	Т	Р	С
Code	10EC01331	Name	LOGIC AND DISTRIBUTED CONTROL STSTEM	Category	0	Open Elective	3	0	0	3

Pre-requisite Courses		Co-requisite Courses	Nil	Progressive Courses	Nil
Course Offering Depa	artment	Electronics and Communication	Data Book / Codes/Standards	Nil	

Course Learning Rationale (CLR): The purpose of learning this course is to:	L	earni	ng						Prog	ram L	earni	ing O	utcor	nes (l	PLO)		
CLR-1: Understand basic components of PLC	1	2	3		1	2	3	4	5	6	7	8	9	10	11	12	13 14 15
CLR-2: Understand the use of timers and counters in process automation				1							٨						for for nent
CLR-3: Understand DCS architecture	Ê	-	_					Research			Sustainability						
CLR-4: Understand operator and engineering interface in DCS	(Bloom)	(%) <i>k</i>	t (%)		dge		ent	see			aina		Work		8		ontrol 8 DC anage
CLR-5: Understand HART signal standard and Field bus	(BI	ency	Attainment		× le	s	Development		age	Ð	usti		ε		Finance	g	0 <u>4</u> 0 <u>8</u>
CLR-6 : Understand Field bus signal standard.	hinking	ofici	ainr		К Кро	lysi	velo	Design,	Usé	Culture	ø		Tear	ion	& Fi	aming	matic discre e PLC e PLC stems trive n
	Thin	<u>ъ</u>			ing	Analysis	De	De	Tool Usage	& Cu	Jent		. 8 I	icat	Mgt.	l Le	Autor Utilize of sys Effec
Course Learning Outcomes (CLO): At the end of this course, learners will be able to:	Level of .	Expected	Expected		Engineering Knowledge	Problem	Design &	Analysis,	Modern 7	Society 8	Environment	Ethics	Individual	Communication	Project N	Life Lonç	PSO 1: A continous PSO-2: U control o PSO-3: E ekills
CLO-1: Select PLC based on I/O's	2,3	80	80	1	Ħ	M	L	-	-	-	-	-	M	-	M	Ē	M M
CLO-2: Apply timers and counters in process automation	1,2	80	80	1	Н	Н	Н	Н	Н	-	L	-	Н	М	L	L	H H H
CLO-3: Select LCU based on application	1	80	80	1	Н	М	-	-	-	-	-	-	L	-	-	L	M L M
CLO-4 : Analyse data's in Operator displays	3	80	80]	Н	Н	-	Н	-	-	-	-	Н	М	-	L	H L M
CLO-5 : Interpret industrial data communication modes	3	80	80	1	Н	-	-	-	-	-	-	-	-	L	-	L	H - L
CLO-6 : Gain knowledge on field bus	3	80	80]	Н	L	-	-	-	-	-	-	-	-	-	L	H - L

Duratio	on (hour)	9	9	9	9	9
	SLO-1	Programmable logic controllers	PLC Programming Languages	Evolution of DCS	Operator Interfaces Requirements	Introduction to HART
S-1	SLO-2	PLC vs Computer	Ladder Diagram	Hybrid System Architecture	Process Monitoring	Evolution of Signal standard
S-2	SLO-1	Parts of a PLC	Functional block	Central Computer system Architecture	Process Control	HART Networks: Point-to-Point
	SLO-2	Architecture	Sequential Function Chart	DCS Architecture	Process Diagnostics	Multi-drop
S-3	SLO-1	PLC size and Application.	Instruction List	Comparison of Architecture	Process Record Keeping	Split range control valve
3-3	SLO-2	Fixed and Modular I/O	Structured Text	Local Control Unit Architecture	Low Level Operator Interface	HART Field Controller Implementation
	SLO-1	Discrete Input Modules	Wiring Diagram	Architectural Parameters	High Level Operator Interface	Hart Commends: Universal
S-4	SLO-2	Discrete Output Modules	Ladder logic Program	Comparison Of LCU Architecture	Hardware Elements In The Operator Interface	Common Practice
S-5	SLO-1	Analog Input Modules	On-Delay Timer Instruction	LCU Language Requirements	Operator Input And Output Devices	Device Specific
3-0	SLO-2	Analog Output Modules	Off-Delay Timer Instruction	Function Blocks	Operator Display Hierarchy	Wireless Hart
S-6	SLO-1	Special I/O Modules	Retentive Timer	Function Block Libraries	Plant-Level Display	Field Bus Basics
3-0	SLO-2	High Speed Counter Module	Cascading Timer	Problem-Oriented Language	Area- Level Display	Field Bus Architecture
S-7	SLO-1	Power Supplies	Up-Counter	LCU Process Interfacing Issues	Group- Level Display	Field Bus Standard
3-1	SLO-2	Isolators	Down-Counter	Security Requirements	Loop- Level Display	Field Bus Topology
	SLO-1	Input/output Devices: Switches	Cascading Counters	Security Design Approach	Engineering Interface Requirements	H1 Field Bus
S-8	SLO-2	sensors	Combining Counter And Timer Functions	On-Line Diagnostics	Requirement For Operator Interface Configuration	H2 Field Bus
S-9	SLO-1	Relays	Math Operation	Redundant Controller Design	Low Level Engineering Interface,	Interoperability
3-9	SLO-2	Solenoid valve	Program	One-On-One, One-On-Many Redundancy	High Level Engineering Interfaces	Interchangeability

	Learning Resources	2.	Frank D. Petruzella, <u>Programmable Logic Controller</u> , <u>Tata McGraw Hill Fifth Edition</u> , 2017 Bolton. W, Programmable Logic Controllers,6th Edition, Elsevier Newnes, Sixth Edition 2016. Krishna Kant, Computer Based Industrial Control, Second edition, Prentice Hall of India, New Delhi,2015	4. 5.	Bowten, R HART Application Guide, HART Communication foundation, 2015. Berge, J, Field Busses for process control: Engineering, operation, maintenance, ISA press,2015	
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Learning Asses	sment										
	Bloom's			Conti	nuous Learning Ass	essment (50% weig	htage)			Final Examination	n (50% weightage)
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		i (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40 %		30 %		30 %		30 %		30%	
Lever	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %		40 %	_	40 %		40%	
Leverz	Analyze	40 /0	-	40 /0	-	40 70	-	40 /0	-	4070	-
Level 3	Evaluate	20 %		30 %		30 %		30 %		30%	
Level 5	Create	20 %	-	30 %	-	30 %	-	30 %	-	30%	-
	Total	100	0 %	100) %	10	0 %	100) %	100) %

Course Designers		
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts
1. D. Karthikeyan, Controlsoft Engineering India Pvt Ltd, karthikeyan.d@controlsoftengg.in	1. Dr. J. Prakash, MIT, Chennai, prakaiit@rediffmail.com	Mr. J. Sam Jeba Kumar, SRMIST
2. V. Venkateswaran, Instrumentation Consultant, vvenkat99@gmail.com	2. Dr. D. Nedumaran, Madras University, dnmaran@gmail.com	Dr. G. Joselin Retna Kumar, SRMIST

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Course Code	18ECO134T	Course Name	SENSOR	S AND TRANS	Course Category		L 3	Т 0	P 0	C 3		
Pre-requisite Courses	Nil		Co-requisite Courses	Nil		Progre		Nil				
Course Offering	Department	Electronics	and Instrumentation Engineering		Data Book / Codes/Standards	Nil						

Course Le	earning Rationale (CLR):	The purpose of learning this course is to:	l	earnin	9					Pro	ogram	Learn	ning O	utcom	Program Learning Outcomes (PLO)					
CLR-1 :	Gain knowledge on classification	on, and characteristics of transducers	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CLR-2 :	Acquire the knowledge of differ	rent types of inductive and capacitive sensors	(_														ູ່ທ	r	ŧ
CLR-3 :	Acquire the knowledge of differ	rent types of thermal and radiation sensors	(Bloom)	(%)	(%)				ксh			bility						l for stems	Sfe	emei
CLR-4 :	Acquire the knowledge of different types of magnetic sensors				lent	dge		ent	Resea			aina		Work		lce		ontrol e syst	& D(inag
CLR-5 :	Acquire the knowledgeof different types of sensors measuring non-Electrical quantity				Attainment	Knowledge	<u>.</u>	evelopment	n, R	sage	e	Sustainability		N N		Finance	<u>i</u>	latic c	PLC.	eme
CLR-6 :			of Thinking	cted Proficiency	ected Att	Engineering Kn	m Analysis	& D	iis, Design,	n Tool Usage	y & Culture	nment &		ual & Team	Communication	Project Mgt. & F	ong Leaming	: Automa uous& dis	: Utilize F	8: Effectiv
Course Le	earning Outcomes (CLO):	At the end of this course, learners will be able to:	Level	Expected	Expe	Engine	Problem	Design	Analysis,	Moder	Society	Environme	Ethics	Individual	Comr	Projec	Life L(PSO [,] contin	PSO-2	PSO-3
CLO-1 :	To demonstrate the various typ	es of basic sensors.	2,3	80	80	Н	-	Н	-	-	Н	Н	Н	-	-	-	Н	Н	-	-
CLO-2 :	Understand the inductive and o	apacitive sensors which are used for measuring various parameters.	1,2	80	80	Н	-	-	Н	-	Н	-	-	-	-	-	Н	-	Н	-
CLO-3 :	Understand the thermal and ra	diation sensors	1	80	80	-	-	-	-	-	Н	-	-	Н	Н	-	-	Н	-	-
CLO-4 :	Have an adequate knowledged	n the various magnetic sensors	3	80	80	-	Н	Н	-	-	-	-	-	-	-	-	-	-	Н	-
CLO-5 :	0-5 : To demonstrate the various types of basic sensors measuring non electrical quantity			80	80	-	-	Н	-	Н	-	-	-	-	-	-	Н	-	-	Н
CLO-6 :	.0-6 : Select the right transducer for the given application			80	80	Н	-	Н	-	-	Н	Н	Н	-	-	-	Н	Н	-	-

Duratio	n (hour)	9	9	9	9	9
S-1	SLO-1	Introduction to sensors/ transducers, Principles	Introduction to Inductive sensor	Thermal sensors: Introduction	Magnetic sensors: Introduction	Measurement of Non-Electrical quantity: Introduction
	SLO-2	Classification based on different criteria	Sensitivity and linearity of the sensor	Thermal Expansion type.	Villari effect	Flow Measurement – Introduction.
	SLO-1	Characteristics of measurement systems	Transformer type transducer	Acoustics temperature sensors.	Wiedmann effect	Ultrasonic Flow Meters.
S-2	SLO-2	Static characteristics Accuracy, Precision, Resolution, Sensitivity	Electromagnetic transducer	Thermo-emf sensor.	Hall effect	Hot Wire Anemometers.
S-3	SLO-1	Dynamic characteristics.	Magnetosrtictive transducer	Materials for thermos-emf sensors.	Construction,	Electromagnetic Flow meters.
3-3	SLO-2	Environmental Parameters	Materials used in inductive sensor	Thermocouple construction	performance characteristics,	Principle and types.
	SLO-1	Characterization and its type	Mutual Inductance change type	Types.	and its Application	Measurement of Displacement.
S-4	SLO-2	Electrical characterization.	LVDT: Construction.	Thermo-sensors using semiconductor device	Introduction to smart sensors	Introduction and types.
S-5	SLO-1	Mechanical Characterization.	Material, input output relationship,	Pyroelectric thermal sensors	Film sensors: Introduction	Measurement of Velocity/ Speed.
3-3	SLO-2	Thermal Characterization	Synchros-Construction	Introduction	Thick film sensors	Introduction and types.
S-6	SLO-1	Optical Characterization.	Capacitive sensor: Introduction	characteristics	Microelectromechanical systems	Measurement of Liquid Level.
3-0	SLO-2	Errors and its classification.	Parallel plate capacitive sensor	Application	Micromachining.	Introduction and types.

S-7	SLO-1	Selection of transducers.	Variable thickness dielectric capacitive sensor	Radiation sensors.	Nano sensors	Measurement of Pressure.
3-1	SLO-2	Introduction to mechanical sensors	Electrostatic transducer		Applications: Industrial weighing systems: Link–lever mechanism.	Introduction and types.
S-8	SLO-1 Resistive potentiometer and typ		Piezoelectric elements	Characteristics	Load cells – pneumatic, elastic and their mounting.	Measurement of Vibration.
3-0	SLO-2	Strain gauge: Theory, type, design consideration, sensitivity.	Ultrasonic Sensors	Geiger counters	different designs of weighing systems.	Introduction and types.
S-9	SLO-1	Resistive transducer: RTD, materials used in RTD	Calculation of sensitivity.	Scintillation detectors	conveyors type.	Application of sensors in industries
3-9	SLO-2	Thermistor: thermistor material, shape	Capacitor microphone, response characteristics	Application on radiation sensors	weighfeeder type.	Application of sensors in home appliances

Learning Resources	1. 2. 3.	Patranabis, D., "Sensors and Transducers", 2 nd Edition, Prentice Hall India Pvt. Ltd, 2010. Doeblin, E.O., "Measurement Systems: Applications and Design", 6 th Edition, Tata McGraw-Hill Book Co., 2011. Bentley, J. P., "Principles of Measurement Systems", 4 th Edition, Addison Wesley Longman Ltd., UK, 2004.	5	Murthy, D.V.S., "Transducers and Instrumentation", Prentice Hall of India Pvt. Ltd., New Delhi, 2010. Neubert H.K.P., "Instrument Transducers – An Introduction to their performance and Design", Oxford University Press, Cambridge, 2003.
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Learning Asses	ssment										
	Bloom's			Final Examinatio	n (50% weightage)						
	Level of Thinking	CLA –	1 (10%)	CLA –	2 (15%)	CLA –	3 (15%)	CLA – 4	l (10%)#		n (50% weightage)
	Level of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice
Level 1	Remember	40 %		30 %		30 %		30 %		30%	
Level I	Understand	40 %	-	30 %	-	30 %	-	30 %	-	30%	-
Level 2	Apply	40 %		40 %	_	40 %	_	40 %	-	40%	
Level 2	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-
Level 3	Evaluate	20 %		30 %		30 %		30 %	-	30%	
Level 5	Create		-		-		-				-
	Total	100	0 %	100) %	10	0 %	100) %	10	0 %

Course Designers										
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts								
1. D. Karthikeyan, Controlsoft Engineering India Pvt Ltd, <u>karthikeyan.d@controlsoftengg.in</u>	1. Dr. J. Prakash, MIT, Chennai, prakaiit@rediffmail.com	Mrs. K. Vibha, SRMIST								
2. V. Venkateswaran, Instrumentation Consultant, vvenkat99@gmail.com	2. Dr. D. Nedumaran, Madras University, <u>dnmaran@gmail.com</u>	Dr. G. Joselin Retna Kumar, SRMIST								

Course	18ECO135T	Course	FUNDAMENTALS OF MEMS	Course	0	Open Elective	L	Т	Ρ	С
Code	102001331	Name	FUNDAMENTALS OF MEMIS	Category	0	Open Elective	3	0	0	3

Pre-requisite Courses	Co-requisite Courses		Progressive Courses	il l
Course Offering Department	Electronics and Communication Engineering	Data Book / Codes/Standards	Nil	

Course Le	Course Learning Rationale (CLR): The purpose of learning this course is to:			ing	Program Learning Outcomes (PLO)													
CLR-1 :	Understand the importance of micro system technology	1	2	3	1	2	3	4	5	6	7	8	9	10	11	12	13 1	14 15
CLR-2 :	Learn the operating principle of various micro sensors and actuators	ĉ									λ					r.	ns for	nagement
CLR-3 :	Impart the applications of various micro fabrication techniques	(moc	Proficiency (%)	(%)				arch			Sustainability						vor id syster DCS i	em
CLR-4 :	Understand the differences and need for microfabrication	(Blo	ŝ	Attainment	dge		ent	se			aina		Work		e Ce	4		u ag
CLR-5 :	Operate MEMS design tools to design simple micro devices	Thinking	icie	inm	×le	s	Development	, Re	age	Ð	Sust		× ¤		Finance		C c c	and
CLR-6 :	Understand recent developments and challenges in MEMS	ink	p	Vtta	Knc	Analysis	velc	Design,	Tool Usage	Culture	ంన		Team	<u>io</u>	δ	eaming	e Disc	ctive
	earning Outcomes (CLO): At the end of this course, learners will be able to:	Level of	Expected	Expected	Engineering Knowledge	Problem An	Design & De	Analysis, De	Modern Toc	Society & C	Environment	Ethics	Individual &	Communication	Project Mgt.	Life Long Le	continuous8 PSO-2: []fili	control of sy PSO-3: Effe skills
CLO-1 :	Appreciate the fundamental concepts in MEMS technology	2,3	80%	80%	Н	-	-	-	-	Н	-	-	-	-	-	Н	Н	- H
CLO-2 :	Understand the fabrication and machining techniques of MEMS devices	1,2	80%	80%	Н	-	-	-	-	Н	-	-	-	-	-	Н	-	- H
CLO-3 :	Familiarize with the concepts of packaging of MEMS devices	1	80%	80%	Н	-	-	Н	-	Н	-	-	-	-	-	Н	Н	- H
CLO-4 :	CLO-4: Appreciate the significance of micro fabrication processes				Н	-	-	Н	-	-	-	-	-	-	-	Н	-	- H
CLO-5 :	Design and Simulate simple structures using MEMS software	3	80%	80%	Н	-	Н	Н	Н	-	-	Н	Н	-	-	Н	Н	- H
CLO-6:	Analyze recent trends and developments in MEMS technology	3	80%	80%	Н	-	-	Н	-	-	-	-	-	-	-	Н	Н	- H

Du	iration	Introduction	Fabrication overview	Micromachining	Bonding & Sealing	Recent trends
ł)	hour)	9	9	9	9	9
S-1	SLO-1	Introduction to MEMS and Brief recap of Macro devices	Introduction to Micro fabrication process	Introduction of micro machining(MMC) process	Introduction to MEMS packaging	Introduction to design tools and simulation
	SLO-2	Microelectronics and Micro systems	Significance of each technique	Significance of MMC	Challenges in packaging	FEM analysis
S-2	SLO-1	Scaling laws in geometry	Process Description of Photolithography	Bulk MMC process – merits and demerits	Different levels of Packaging	Design of a silicon die for a micro pressure sensor
	SLO-2	Silicon as ideal material and as substrate	Implementation of Photolithography	Sequence of steps	Die, device and system level	Simulation in software
S-3	SLO-1	Si wafer production	Process Description of CVD	Significance of Isotropic etching	Differences in IC packaging technology	Application of MEMS in automotive industry
5-3	SLO-2	Cz process	Implementation, merits and demerits of CVD	Anisotropic etching	And MEMS packaging	Airbag deployment
S-4	SLO-1	Sequential steps in wafer processing	Process Description of PVD	Surface MMC process	Die Preparation	Optical MEMS Application
	SLO-2		Implementation, merits and demerits of PVD	Sequence of steps	Plastic encapsulation and its significance	Micro mirrors
S-5	SLO-1	Chemical and mechanical properties of Si and compounds	Process Description, implementation of Ion implantation	Challenges in surface MMC	Types of wire bonding Thermo compression type	Micro fluidics Application
3-3	SLO-2	Chemical and mechanical properties of Polymers, Quartz and GaAs	Oxidation process	Interfacial & Residual stresses	Thermo sonic, Ultra sonic type	Lab on chip module

S-6	SLO-1	Chemical, Biomedical type Micro sensors	Diffusion process	LIGA process- description merits and demerits	Types of surface bonding – Adhesive	IR and Gas sensing
	SLO-2	Piezoelectric type of Micro sensors	Wet etching methods	Implementation	soldering, SOI type of bonding	Thermal sensors
S-7	SLO-1	Thermal, SMA, Piezoelectric actuators	Properties of etchants	Process Design-block diagram and description	Anodic bonding and lift off process	Micro power generation
	SLO-2	Electro static type Micro Actuators	Dry etching methods	Electro-mechanical design, Thermo- electric design	Precautions to be taken	Micro TEG
S-8	51 U-1	Micro devices- operation of Micro gears and micromotors	Production of plasma	CAD- block diagram description and	Types of sealing- Micro shells, Hermetic sealing	Chemical sensors
3-0		Micro devices –operation of Micro valves and pumps	Etch stop methods	implementation	Micro 'O' rings,Reactive seal	Micro humidity sensors
S-9	SLO-1	Case study	Coop study	Case study	Selection of packaging materials	Micro pressure sensors
3-9	-9 SLO-2	Case study	Case study	Case study	Material requirements	Paper MEMS

Learning Resources Tai-Ran Hsu, "MEMS and MICROSYSTEMS", 22nd reprint edition, Wiley & sons, 2015
 M. Madou, "Fundamentals of Micro fabrication", Taylor and Francis group, 2002

VardhanGardener, "Micro sensors and smart devices", John Wiley & Sons, 2001
 NPTEL link: https://nptel.ac.in/downloads/112108092/

Learning Asse	essment											
	Bloom's		Continuous Learning Assessment (50% weightage)							Final Examination (50% weightage)		
	Level of Thinking	CLA –	1 (10%)	CLA – 2	2 (15%)	CLA – S	3 (15%)	CLA – 4	4 (10%)#		n (50% weightage)	
	Lever of Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Level 1	Remember	30 %		30 %		30 %		30 %		30%		
Level I	Understand	30 %	-	30 %	-	30 %	-	30 %	-	30%	-	
Level 2	Apply	40 %		40 %		40 %		40 %		40%		
Level Z	Analyze	40 %	-	40 %	-	40 %	-	40 %	-	40%	-	
Level 3	Evaluate	30 %		30 %		30 %		30 %		30%		
Level 3	Create	30 %	-	30 %	-	30 %	-	30 %	-	30%	-	
	Total	100	0 %	100) %	100	0 %	10	0 %	10	0 %	

Course Designers					
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts			
1. D. Karthikeyan, Controlsoft Engineering India Pvt Ltd, karthikeyan.d@controlsoftengg.in	1. Dr. J. Prakash, MIT, Chennai, <u>prakaiit@rediffmail.com</u>	1. Dr. A. Vimala Juliet, SRMIST			
2. Mr. Hariharasudhan - Johnson Controls, Pune, hariharasudhan.v@jci.com	2. Dr. D. Nedumaran, Madras University, <u>dnmaran@gmail.com</u>	2. R.Bakiyalakshmi,SRMIST			

B. Tech in Electronics and Communication Engineering

2018 Regulations

Project Work, Seminar, Internship in Industry / Higher Technical Institutions (P)

Department of Electronics and Communication Engineering

SRM Institute of Science and Technology SRM Nagar, Kattankulathur – 603203, Kancheepuram District, Tamilnadu

Course	18ECP109L /	Course		Course		Project Work, Seminar, Internship In Industry / Higher	L	Т	Ρ	С
Code		Category	Р	Technical Institutions (P)	0	0	20	10		

Pre-requisite Courses	Co-requisite Courses		Progressive Courses Nil
Course Offering Department	Electronics and Communication Engineering	Data Book / Codes/Standards	As required for the project work

Course Learn	ning Rationale (CLR):	The purpose of learning this course is to:			
CLR-1 :	To prepare the student to gain major design and or research experience as applicable to the profession				
CLR-2 :	Apply knowledge and skills acquired through ear	Apply knowledge and skills acquired through earlier course work in the chosen project			
CLR-3 :	Make conversant with the codes, standards , app	Make conversant with the codes, standards , application software and equipment			
CLR-4 :	Carry out the projects within multiple design cons	Carry out the projects within multiple design constraints			
CLR-5 :	Incorporate multidisciplinary components				
CLR-6:	Acquire the skills of comprehensive report writing				

Course Learning	g Outcomes (CLO):	At the end of this course, learners will be able to:
CLO-1 :	Design a system / process or gain research insigh	t into a defined problem as would be encountered in engineering practice taking into consideration its impact on global, economic, environmental and social context.

Learning Assessment					
Continuous Learning	Assessment tool	Review I	Review II	Review III	Total
Assessment	Weightage	5%	20%	25%	50%
First Frankration	Assessment tool	Project Report	Viva Voce *		Total
Final Evaluation	Weightage	20%	30%		50%

* Student has to be present for the viva voce for assessment. Otherwise it will be treated as non-appearance for the examination with final grade as 'Ab'

Course	18ECP107L	Course	MINOR PROJECT	Course		Project Work, Seminar, Internship In Industry / Higher	L	Т	Ρ	С
Code		Category	Р	P Technical Institutions (P)		0	6	3		

Pre-requisite Courses	Co-requisite Courses		Progressive Nil Nil
Course Offering Department	Electronics and Communication Engineering	Data Book / Codes/Standards	As required for the project work

Course Learn	ning Rationale (CLR):	The purpose of learning this course is to:				
CLR-1 :	Prepare the student to formulate an engineering	Prepare the student to formulate an engineering problem within the domain of the courses undergone				
CLR-2 :	Seek solution to the problem by applying codes / standards/ software or carrying out experiments or through programming					

Course Learning	Outcomes (CLO):	At the end of this course, learners will be able to:
CLO-1 :	Identify a small part of major system or process, u	nderstand a problem associated with it and find solution or suggest a procedure leading to its solution.

Learning Assessment							
Continuous Learning Assessment	Assessment tool	Review I	Review II	Final Review *	Total		
	Weightage	20%	30%	50%	100%		

* Student has to be present for final review for assessment. Otherwise it will be treated as non-appearance for the examination with final grade as 'Ab'

Course	18ECP102L /	/ Course	Industrial Training 1/II	Course		Project Work, Seminar, Internship In Industry / Higher		Т	Ρ	С
Code	18ECP105L	Name		Category	Ρ	Technical Institutions (P)	0	0	2	1

Pre-requisite Courses	Co-requisite Courses		Progressive Nil Nil
Course Offering Department	Electronics and Communication Engineering	Data Book / Codes/Standards	As exposed to during the duration of training

Course Learning Rationale (CLR):		The purpose of learning this course is to:				
CLR-1: Provide an exposure to the students on the practical application of theoretical concepts in an industry or research institute						

Course Learning Outcomes (CLO):		At the end of this course, learners will be able to:			
CLO-1 :	Gain confidence to carry out supervisory, managerial, and design roles in an industrial context.				

Learning Assessment							
	Assessment tool						
Continuous Learning Assessment	Weighters	Training Report	Presentation *				
	Weightage	75%	25%				

* Student has to be present for the presentation for assessment. Otherwise it will be treated as non-appearance for the examination with final grade as 'Ab'

Course	18FCP108		Course		Internship Cours		P	Project Work, Seminar, Internship Ir	rnship In Industry / Higher		Т	Ρ	С
Code		Name			•	Category		Technical Institutions	; (P)	0	0	6	3
Pre-requisi Courses	INII		Co-requisite Courses	Nil		Progr Cou		Nil					
Course Offer	ring Department	Ele	ctronics and Communication En	gineering	Data Book / Codes/Standards	As expo	sed to d	uring the duration of internship					
Course Learning Rationale (CLR): The purpose of learning this course is to:													
CLR-1 :	Provide an ex	cposure to the	students on the practical applica	tion of theore	etical concepts in an industry or rese	earch institute a	nd also	to gain hands on experience in the context	of design, production	i and i	nainte	enanc	е
Course Learr	ning Outcomes (C	L O):	At the en	At the end of this course, learners will be able to:									
CLO-1 :	Gain confiden	ce to carry ou	t supervisory, managerial, and d	esign roles in	an industrial context or research er	nvironment							
Learning Ass	sessment												
		Asse	ssment tool			Final review							
Continuous L	Learning Assessm	ent	10/2:2					Training Report	Presentation*		-		
		vveig	Weightage				75%	25%	-	-			

75%

* Student has to be present for the presentation for assessment. Otherwise it will be treated as non-appearance for the examination with final grade as 'Ab'

25%

Course Code	18ECP103L / 18ECP106L	Course Name	Seminar 1/1				Course Category				P Project Work, Seminar, Internship In Industry / Higher L Technical Institutions (P)				C
couc					catogory		U	2	1						
Pre-requisite Courses	Pre-requisite Courses Nil Co-requisite Courses Nil Nil Nil Nil Courses Nil														
Course Offerin	Course Offering Department Electronics and Commun		munication Engineering	Data Book / Codes/Standards	ards As applicable										
Course Learning Rationale (CLR):			The purpose of learnin	The purpose of learning this course is to:											

CLR-1: Identify an area of interest within the program or a related one (multidisciplinary), carry out a literature survey on it, gain understanding and present the same before an audience.

Course Learning Outcomes (CLO):		At the end of this course, learners will be able to:			
CLO-1 :	Carry out a self-study of an area of interest and communicate the same to others with clarity.				

Learning Assessment								
	Assessment tool	Presentation						
Continuous Learning Assessment	Weightage	Presentation material	Presentation skills / ability to answer questions / understanding of the topic*					
	Weightage	60%	40%					

* Student has to be present for the presentation for assessment. Otherwise it will be treated as non-appearance for the examination with final grade as 'Ab'