



SRM INSTITUTE OF SCIENCE AND TECHNOLOGY, RAMAPURAM CAMPUS  
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING  
CLASS TIMETABLE - ODD SEMESTER (2023-24)

Class: 4A

Class incharge: Mr. Ajay Daniel

TIME	08.00-8.50	8.50 -9.40	9.50-10.40	10.40-11.30		12.20-01.10	01.10-02.00	02.00-02.50	02.50-03.40
PERIOD	1	2	3	4		5	6	7	8
Day 1	FPGA - Lab		ARM - Lab		Lunch 11.30-12.20	18ECC301T	18CSO105T	18ECE306J	18ECE205J
Day 2	18ECE306J	18ECE205J	18CSO105T	18ECC301T		18ECC301T	18ECP107L		
Day 3	18ECC301T	18ECE306J	18ECE205J	18CSO105T		18ECP107L			
Day 4									
Day 5									

S.NO	SLOT	COURSE CODE	COURSE TITLE	STAFF NAME	L	T	P	C
1	B	18ECC301T	Wireless Communications	Mrs. Padmavathi B	3	1	0	4
2	A	18ECE205J	FPGA-based Embedded Systems Design	Mr. Ajay Daniel	2	0	2	3
	Lab			Mr. Ajay Daniel, Dr. Ramani P*, Ms. Supraja*, Dr. Roopa*				
3	E	18ECE306J	ARM-based Digital Signal Processing	Ms. Sri Sabarimani K	2	0	2	3
	Lab			Ms. Sri Sabarimani K, Dr. Durga Devi K*, NS. I*, Dr. Vidhyalakshmi*				
4	G	18CSO105T	Android Development	New Staff I	3	0	0	3
5		18ECP107L	Minor Project	Dr Phani Kumar, Mr. Ajay Daniel, Dr. Christeena Joseph	0	0	6	3

Timetable Coordinator

HOD/ECE

Academic

Dean

**SRM INSTITUTE OF SCIENCE AND TECHNOLOGY, RAMAPURAM CAMPUS**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**CLASS TIMETABLE - ODD SEMESTER (2023-24)**

Class incharge: Dr. Lathamanju P

Class: 4B

Class: 4B

Class incharge: Dr. Lathamanju P

TIME	08.00-8.50	8.50 -9.40	9.50-10.40	10.40-11.30		12.20-01.10	01.10-02.00	02.00-02.50	02.50-03.40
PERIOD	1	2	3	4		5	6	7	8
Day 1	18ECC301T	18ECP107L	18CSO105T	18ECE205J	Lunch 11.30-12.20	18ECP107L	18ECE306J	18ECC301T	18CSO105T
Day 2	ARM-DSP Lab		FPGA Lab			18CSO105T	18ECC301T	18ECE306J	18ECE205J
Day 3	18ECE205J	18ECC301T	18ECE306J	18ECP107L		18ECP107L			
Day 4									
Day 5									

S.NO	SLOT	COURSE CODE	COURSE TITLE	STAFF NAME	L	T	P	C
1	B	18ECC301T	Wireless Communications	Mr. Kapileswar N	3	1	0	4
2	A	18ECE205J	FPGA-based Embedded Systems Design	Dr. Ramani P	2	0	2	3
	Lab			Dr. Ramani P, Mrs. Arthi Elaveni M*, NS 2*, Dr Chriteena Joseph				
3	E	18ECE306J	ARM-based Digital Signal Processing	Dr. Durga Devi K	2	0	2	3
	Lab			Dr. Durga Devi K, Dr Roopa M *, Dr Lathamanju*, Padmavathi B*				
4	G	18CSO105T	Android Development	Mrs. Supraja G	3	0	0	3
5		18ECP107L	Minor Project	Dr Vani R, Dr Lathamanju, Dr Nagarajan S	0	0	6	3

Dean

Timetable Coordinator

VP Academic



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING  
CLASS TIMETABLE - ODD SEMESTER (2023-24)

Class: 4C

Class incharge: Dr. Judy Simon

Class: 4C

Class menage: Dr. Gady Simon

TIME	08.00-8.50	8.50 -9.40	9.50-10.40	10.40-11.30		12.20-01.10	01.10-02.00	02.00-02.50	02.50-03.40
PERIOD	1	2	3	4		5	6	7	8
Day 1					Lunch 11.30-12.20				
Day 2									
Day 3		18ECP107L				18ECE205J	18ECC301T	18CSO105T	18ECE306J
Day 4	18ECE306J	18ECE205J	18CSO105T	18ECC301T		18ECC301T	18ECP107L		
Day 5	FPGA Lab		ARM - DSP Lab			18ECC301T	18ECE205J	18ECE306J	18CSO105T

S.NO	SLOT	COURSE CODE	COURSE TITLE	STAFF NAME	L	T	P	C
1	B	18ECC301T	Wireless Communications	Mr. Kapileswar N	3	1	0	4
2	A	18ECE205J	FPGA-based Embedded Systems Design	Dr. Roopa M	2	0	2	3
	Lab			Dr. Roopa M, Dr Ramani*, Dr Durga Devi*, New Staff 4*				
3	E	18ECE306J	ARM-based Digital Signal Processing	Ms. Sri Sabarimani K	2	0	2	3
	Lab			Mrs. Arthi Elaveni M, Ms. Sri Sabarimani K*, Mr Ajay Daniel, Dr Megala V				
4	G	18CSO105T	Android Development	Mr. Moovendan M	3	0	0	3
5		18ECP107L	Minor Project	Dr Roopa, Dr Judy Simon, Dr Vidhya Lakshmi	0	0	6	3

Timetable Coordinator

HOD/ECE

VR Academic

Dean



