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Dr.R.Thangam Working as an Assistant Professor in the Department of ECE at SRMIST, Ramapuram. She Graduated in Electrical and Electronics Engineering at Madras University, Chennai, Tamil Nadu, India. She secured Master of Engineering in Applied Electronics in Engineering at Anna University, Chennai, India. She Pursued Ph.D. in the field of Digital pulse skipping modulation strategies for DC-DC converters at MIT, Anna University, Chennai, India. She is in teaching profession for more than 13 years. She has presented number of papers in National and International Journals, Conference and Symposiums.

Areas of Research:

FPGA Implementation, MATLAB / Simulink, System Generator, Xilinx – VHDL and Verilog Programming.

Selected Publications:

Web of Science/SCI/Scopus

1.Thangam, R & Joy Vasantha Rani, SP 2020, 'Non-linear controller: Voltage controlled PFC based fuzzy MDPSM controller with predictive Input voltage', Journal of Circuits Systems and Computers, Vol. 29, no.13. ISSN:1793-6454, (Annexure I), Impact Factor: 1.363

2. Thangam, R & Joy Vasantha Rani, S P, 'FPGA based ripple-free on-time nonlinear digital PSM controller for a SMPS', Journal of Electrical Engineering, University 'Politehnica' Timisoara, Romania (Published). (Annexure I, Impact Factor: 0.506).

3.Suvarchala, K., Thangam, R., "Analysis of buck converter using power factor pre-regulator Matlab Simulink" IJPT, Dec-2016, Vol. 8, Issue No.4, PP.No 19460-19463.

4. Y. Apoorva, K.Suvarchala, R.Thangam, "Pulse skipping modulated buck converter using Matlab Simulink" IJPT, Dec-2016, Vol. 8, Issue No.4, PP.No 20754-20758.

5. R.Thangam, Y.Apoorva, K.Suvarchala, "Automatically controlled grass cutter by using solar energy" IJPT, Dec-2016, Vol. 8, Issue No.4, PP.No 19571-19579.

6. Y.Apoorva, Chaitanya Kumar, R.Manikandan, R.Thangam, "Synchronous buck converter using pi and PID controller" IJPT, Dec-2016, Vol. 8, Issue No.4, 21284-21288.7. Mrs. R.Thangam, M.Narayana reddy, P. Krishna Teja, "Designing solar power inverter using

Matlab/Simulink," IJPT, Dec-2016, Vol. 8, Issue No.4, 19747-19752.

8. Samith Rao.P, Lokesh.N, Thangam.R, "Mitigation of ripple content using coupled inductor based ky converter" IJPT, Dec-2016, Vol. 8, Issue No.4, 20555-20560.

9. Y.Apoorva, R.Thangam, "ANFIS based buck and boost converter Simulink model" Journal of Advanced Research in Dynamical and Control Systems Vol. 9. Sp– 17, 2017.

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