Dr. S. SARAVANAN, M.E., Ph.D.,

**Assistant Professor**,

**Dept of EEE, CET, SRMIST** 

# Ramapuram Campus, Chennai



Saravanan Sivasamy received the B.E., M.E. and Ph.D. degrees in Electrical and Electronics Engineering from Anna University, India, in 2009, 2012 and 2022. He is continuing his work at SRM Institute of Science and Technology, Ramapuram Campus, Chennai, India. He is having 12 years of teaching experience in the field of Electrical and Electronics Engineering. His special interests in research are Electric motor design for Electric Vehicles, Power Electronics Drives and Industrial Internet of Things. He has published more than 15 research articles in journals and conferences.

### **Areas of Research:**

Electric motor design for Electric Vehicles, Power Electronics Drives and Industrial Internet of Things.

#### **Selected Publications:**

## Web of Science/SCI

- 1. Published an article titled "Performance Investigation of Doubly Salient Outer Rotor Switched Reluctance Motor using Finite Element Analysis" in Circuit World Journal, Emerald Publications, Nov 2022, Vol. 48 No. 4, pp. 412-424, ISSN: 0305-6120, DOI: 10.1108/CW-06-2020-0115.
- 2. Published an article titled "Performance Inspection of Double Stator Single Rotor Permanent Magnet Machine using Finite Element Analysis" in the Journal of Electrical Engineering, June 2020, Vol. 21, No.1, ISSN: 1582-4594.
- 3. Published an article titled "A Review on Deregulation of Power System" in the journal Wutan Huatan Jisuan Jishu, Volume XVI, Issue V, May/2020, pg. 325-331, ISSN:1001-1749.
- Published an article titled "Plan and Execution of Solar Charge Controller with MPPT Algorithm Utilizing Arduino" in International Journal for Research and Scientific Innovation (IJRSI), Oct 2018, ISSN: 2321-2705.
- 5. Published an article titled "Fuzzy Logic Controlled Doubly Fed Induction Generator for Wind Energy Conversion System" in Journal of Emerging Technologies and Innovative Research (JETIR), Sept 2018, ISSN: 2349-5162.

- Published an article titled "Novel Approach to Reduce Harmonic Distortion using Pseudorandom PWM Technique" in International Journal for Research and Development in Engineering (IJRDE), April 2014, ISSN: 2279-0500.
- Published an article titled "Selective Harmonic Elimination Technique in Multilevel Inverter Fed Induction Motor" in CiiT International Journal of Programmable Device Circuits and Systems, August 2011, ISSN: 0974-9624.

#### **CONFERENCE PUBLICATIONS:**

- Presented a paper on "Finite Element Analysis for Double Stator Single Rotor Permanent Magnet Machine" in TNSCST Sponsored International Conference on Applications of Mathematics in Science and Technology during 25th & 26th July 2019 at AAA College of Engineering and Technology, Sivakasi.
- 2. Presented a paper on "Comparative Study of Various Pulse Width Modulation Techniques in Cascaded Multilevel Inverter" in 4th International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications (ICTIEECC-2019) on 13th March 2019 at PET Engineering College, Vallioor.
- Presented a paper on "Wireless Monitoring System for Improving Agricultural Procedures" in 4th International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications (ICTIEECC-2019) on 13th March 2019 at PET Engineering College, Vallioor.
- 4. Presented a paper on "Designing and Modeling of Automatic Control for Critical Loads" in 3rd International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications (ICTIEECC-2019) on 14th March 2018 at PET Engineering College, Vallioor.
- 5. Presented a paper on "Review on Bio-Inspired Algorithms Based Optimization of Switching Angle for Selective Harmonic Elimination in Multilevel Inverters", in International Conference on Energy Efficient Technologies for Sustainability, at St.Xavier's Catholic College of Engineering, TamilNadu, India. 5th to 7th April, 2018. Available at SSRN: https://ssrn.com/abstract=3193910
- 6. Presented a paper entitled "Design and Implementation of Improved Pulse Width Modulation Technique for Multilevel Inverter" in International Conference on Advances

in Engineering and Technology conducted by E.G.S Pillay Engineering College, April

2011.

7. Presented a paper on the topic "Design and Implementation of Improved Pulse Width

Modulation Technique for Multilevel Inverter" in the National Conference on Emerging

Trends in Engineering and Technology conducted by Anna University of Technology,

Madurai.

8. Presented a paper on the title "Simulation of Improved Pulse Width Modulation

Technique for Multilevel Inverter" in the 3rd National Conference on Recent Trends in

Electrical Technology conducted by Velalar College of Engineering and Technology.

9. Presented a technical paper on "Electric Circuit Fault Diagnosis using Fuzzy Logic System" in

the National Conference on Information Science and Security (NISS 2010).

**Professional Bodies:** 

Member -IAENG

**Google Scholar:** 

https://scholar.google.co.in/citations?hl=en&user=DLoE5h0AAAAJ

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